

Product Summary

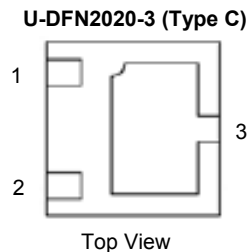
V_{BR} (MIN)	I_{PP} (MAX)	V_C typ at 160A
25.0V	200A	27.1V

Description

This new generation TVS is designed to protect sensitive electronics from the damage due to ESD and Surge. The combination of small size and high ESD surge capability makes it ideal for use in portable applications such as cellular phones, digital cameras, and MP3 players.

Applications

- Cellular Handsets
- Portable Electronics
- Computers and Peripherals



Features

- Low Profile Package (0.60mm Typical) and Ultra-Small PCB Footprint Area (2.3mm × 1.7mm Max) Suitable for Compact Portable Electronics
- Provides ESD Protection per IEC 61000-4-2 Standard: Air ±30kV, Contact ±30kV
- Provides Surge and Lightning Protection per IEC 61000-4-5 Standard: I_{PP} Max 200A
- One Channel of ESD and Surge Protection
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- Halogen and Antimony Free. "Green" Device (Note 3)**
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please [contact us](mailto:contact@diodes.com) or your local Diodes representative. <https://www.diodes.com/quality/product-definitions/>**

Mechanical Data

- Case: U-DFN2020-3
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: NiPdAu over Copper Leadframe. Solderable per MIL-STD-202, Method 208 @4
- Weight: 0.004 grams (Approximate)



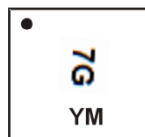
1 and 2 must be electrically connected at the PCB

Ordering Information (Note 4)

Part Number	Compliance	Marking	Reel Size (inches)	Tape Width (mm)	Quantity per Reel
D24V0S1UG3LP20-7	Standard	7G	7	8	3,000/Tape & Reel

- Notes:
- No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
 - See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 - Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 - For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

Marking Information



7G = Product Type Marking Code
 YM = Date Code Marking
 Y = Year (ex: 1 = 2021)
 M = Month (ex: 9 = September)

Date Code Key

Year	2020	2021	2022	2023	2024	2025	2026	2027	2028	2029	2030	2031
Code	H	I	J	K	L	M	N	O	P	R	S	T

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

Maximum Ratings (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	Conditions
Peak Pulse Power Dissipation	P_{PP}	5680	W	8/20 μs (Note 6)
Peak Pulse Current	I_{PP}	200	A	8/20 μs , per Figure 3
ESD Protection – Contact Discharge	$V_{ESD_CONTACT}$	± 30	kV	Standard IEC 61000-4-2
ESD Protection – Air Discharge	V_{ESD_AIR}	± 30	kV	Standard IEC 61000-4-2

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 5)	P_D	500	mW
Thermal Resistance, Junction to Ambient $T_A = +25^\circ\text{C}$	$R_{\theta JA}$	250	$^\circ\text{C/W}$
Operating and Storage Temperature Range	T_J, T_{STG}	-55 to +150	$^\circ\text{C}$

Electrical Characteristics (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Conditions
Reverse Working Voltage	V_{RWM}	—	—	24	V	—
Reverse Current	I_R	—	—	0.5	μA	$V_R = V_{RWM}$
Reverse Breakdown Voltage	V_{BR}	25.0	—	28.5	V	$I_R = 1\text{mA}$
Reverse Clamping Voltage (Note 6)	V_{CL}	—	25.1	27	V	$I_{PP} = 100\text{A}, t_P = 8/20\mu\text{s}$
		—	27.1	30		$I_{PP} = 160\text{A}, t_P = 8/20\mu\text{s}$
		—	28.4	32		$I_{PP} = 200\text{A}, t_P = 8/20\mu\text{s}$
ESD Clamping Voltage (Note 7)	V_C	—	28.0	30.0	V	$I_{PP} = 1\text{A}, t_P = 100\text{ns}$
		—	30.1	33.0		$I_{PP} = 30\text{A}, t_P = 100\text{ns}$
		—	27.5	31.0		$I_{PP} = 80\text{A}, t_P = 100\text{ns}$
Capacitance	C_T	—	630	—	μF	$V_R = 0\text{V}, f = 1\text{MHz}$
		—	170	—		$V_R = 24\text{V}, f = 1\text{MHz}$

Notes: 5. Device mounted on FR-4 PCB pad layout (2oz copper) as shown on Diodes Incorporated's suggested pad layout, which can be found on our website at <http://www.diodes.com/package-outlines.html>.

6. Clamping voltage value is based on an 8x20 μs peak pulse current (I_{PP}) waveform, Measured from Pin1 and Pin2 to Pin3.

7. Transmission Line Pulse Test (TLP) settings: $t_P=100\text{ns}$, $t_R=1\text{ns}$, I_{TLP} and V_{TLP} averaging window is from 70ns to 90ns.

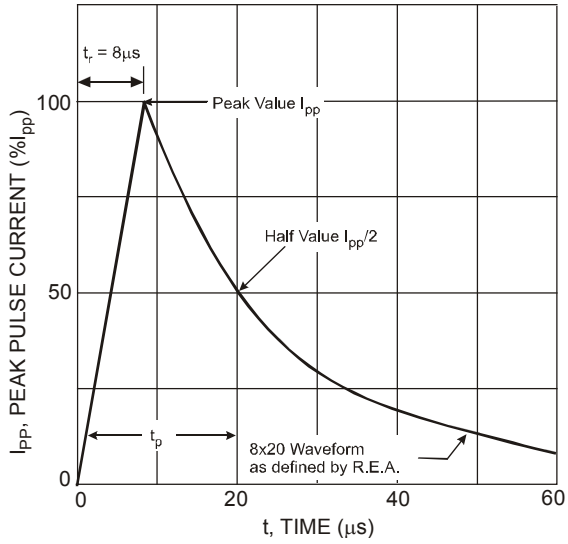


Figure 1 Typical 8x20µs Puls Waveform

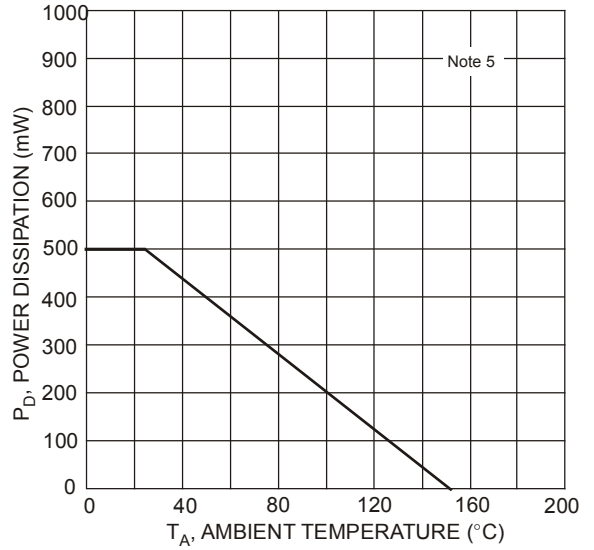


Figure 2 Power Derating Curve

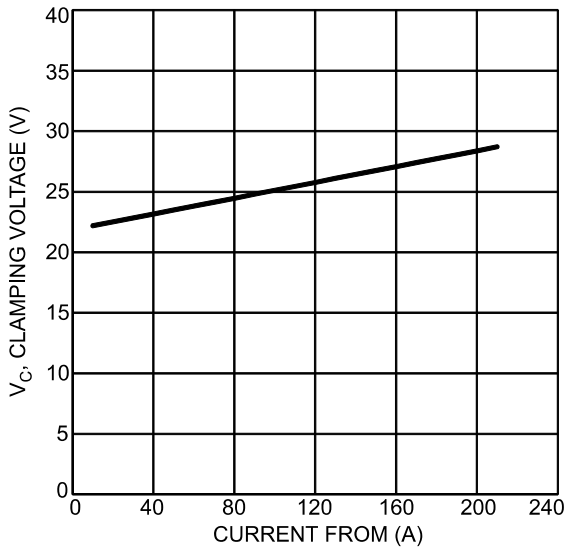


Figure 3 Clamping Voltage Characteristic ($t_p = 8/20\mu s$)

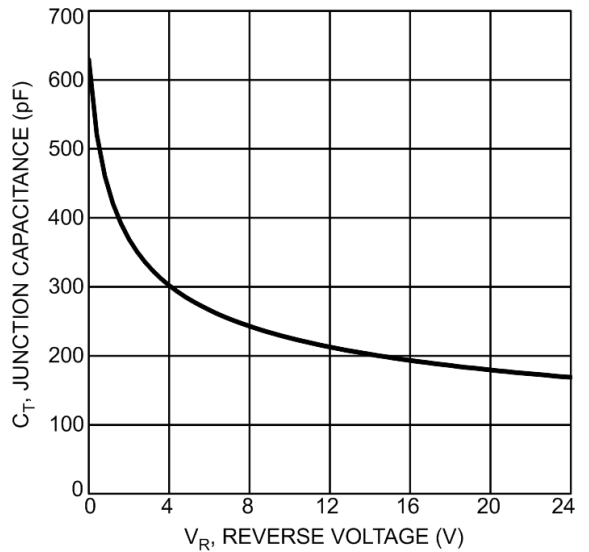


Figure 4 Typical Capacitance, Pin1 or Pin2 to Pin3

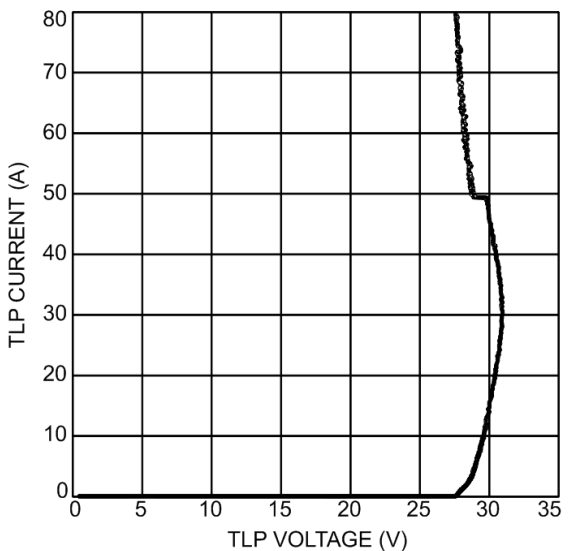
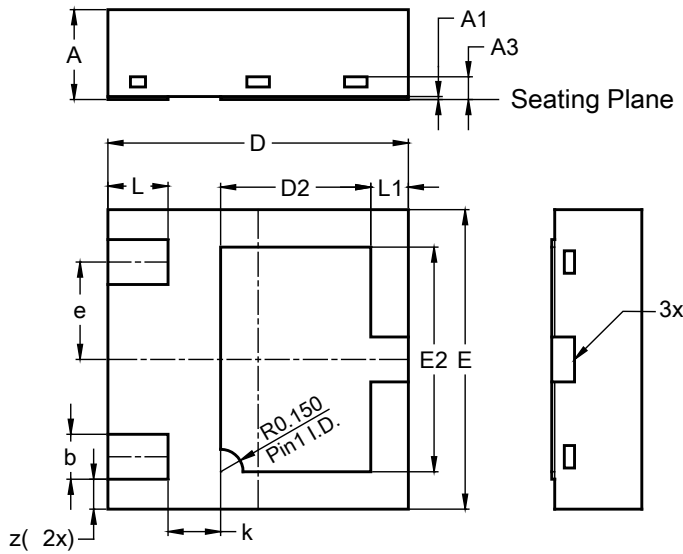


Figure 5 TLP Curve ($t_p = 100ns$)

Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

U-DFN2020-3 (Type C)

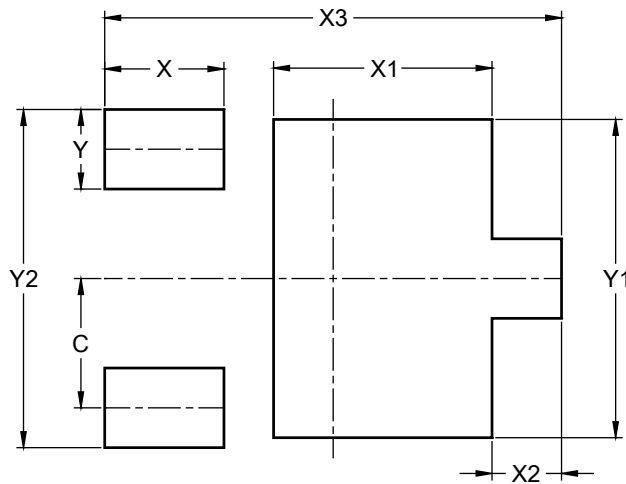


U-DFN2020-3 (Type C)			
Dim	Min	Max	Typ
A	0.55	0.65	0.60
A1	0.00	0.05	0.02
A3	--	--	0.152
b	0.25	0.35	0.30
D	1.95	2.05	2.00
D2	0.90	1.10	1.00
E	1.95	2.05	2.00
E2	1.40	1.60	1.50
e	0.65BSC		
k	--	--	0.35
L	0.35	0.45	0.40
L1	0.20	0.30	0.25
z	--	--	0.20
All Dimensions in mm			

Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

U-DFN2020-3 (Type C)



Dimensions	Value (in mm)
C	0.650
X	0.600
X1	1.100
X2	0.350
X3	2.300
Y	0.400
Y1	1.600
Y2	1.700

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