

N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV _{DSS}	R _{DS(ON)} Max	I _D Max T _A = +25°C
	4.0Ω @ V _{GS} = 10V	335mA
60V	4.1Ω @ V _{GS} = 5V	330mA
	4.2Ω @ V _{GS} = 4V	327mA

Description and Applications

This MOSFET is designed to minimize the on-state resistance (RDS(ON)) yet maintain superior switching performance, making it ideal for high efficiency power management applications.

- Motor Control
- **Power Management Functions**
- Backlighting

Features and Benefits

- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- **ESD Protected**
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please contact us or your local Diodes representative.

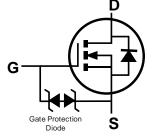
https://www.diodes.com/quality/product-definitions/

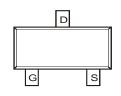
Mechanical Data

- Case: SOT23
- Case Material: Molded Plastic. "Green" Molding Compound, UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Matte Tin Finish Annealed over Alloy 42 Leadframe. Solderable per MIL-STD-202, Method 208 (23)
- Terminal Connections: See Diagram
- Weight: 0.008 grams (Approximate)









Top View

Equivalent Circuit

Top View

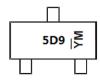
Ordering Information (Note 4)

Part Number	Case	Packaging
DMN65D9L-7	SOT23	3000/Tape & Reel
DMN65D9L-13	SOT23	10000/Tape & Reel

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. Notes:

- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/

Marking Information



5D9 = Product Type Marking Code YM = Date Code Marking \overline{Y} = Year (ex: I = 2021) M = Month (ex: 9 = September)

Date Code Key

Year	2018		2021	2022	2023	2024	2025	2026	2027	2028	2029	2030
Code	F		ı	J	K	L	М	N	0	Р	R	S
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec

1 of 7 DMN65D9L March 2021 Document number: DS40594 Rev. 5 - 2 © Diodes Incorporated



Maximum Ratings (@TA = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V _{DSS}	60	V
Gate-Source Voltage			V _{GSS}	±16	V
Continuous Drain Current (Note 6) $V_{GS} = 10V$ Steady $T_A = +25^{\circ}C$ State $T_A = +70^{\circ}C$			l _D	335 268	mA
Maximum Body Diode Forward Current (Note 6)			ls	335	mA
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)			I _{DM}	700	mA
Pulsed Source Current (10µs Pulse, Duty Cycle = 1	l%)		Ism	700	mA

Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

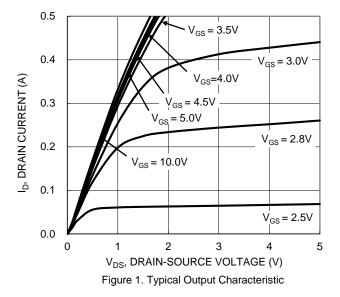
Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)		P_{D}	270	mW
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	Reja	293	°C/W
Total Power Dissipation (Note 6)		PD	670	mW
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	R _{0JA}	186	°C/W
Operating and Storage Temperature Range		TJ, TSTG	-55 to +150	°C

Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

			_			
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BVDSS	60	_	_	V	$V_{GS} = 0V$, $I_D = 10\mu A$
Zero Gate Voltage Drain Current	IDSS		-	1.0	μΑ	$V_{DS} = 60V$, $V_{GS} = 0V$
Gate-Source Leakage	Igss	-	_	±10	μΑ	$V_{GS} = \pm 16V$, $V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	Vgs(TH)	1.0	_	2.5	V	$V_{DS} = V_{GS}$, $I_D = 250\mu A$
			3.2	4.0		$V_{GS} = 10V, I_D = 0.5A$
Static Drain-Source On-Resistance	R _{DS(ON)}	_	2.9	4.1	Ω	$V_{GS} = 5V, I_D = 0.2A$
			3.0	4.2		$V_{GS} = 4V, I_{D} = 0.2A$
Diode Forward Voltage	VsD	_	0.8	1.1	V	V _{GS} = 0V, I _S = 115mA
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	Ciss	l	41	_	рF	V 05V V 0V
Output Capacitance	Coss		4.4	_	pF	V _{DS} = 25V, V _{GS} = 0V f = 1.0MHz
Reverse Transfer Capacitance	Crss	_	2.6	_	pF	1 = 1.000112
Gate Resistance	Rg	_	900	_	Ω	$f = 1MHz$, $V_{GS} = 0V$, $V_{DS} = 0V$
Total Gate Charge	Qg	_	0.4	_	nC	451/1/ 401/
Gate-Source Charge	Qgs	_	0.2	_	nC	Vgs = 4.5V, Vps = 10V, Ip = 250mA
Gate-Drain Charge	Q_{gd}	_	0.1	_	nC	1D = 25011A
Turn-On Delay Time	t _D (ON)	_	3.7	_	ns	
Turn-On Rise Time	tR	_	3.6	_	ns	$V_{DD} = 30V, V_{GS} = 10V,$
Turn-Off Delay Time	tD(OFF)	1	102	_	ns	$R_g = 25\Omega$, $I_D = 200mA$
Turn-Off Fall Time	tF	_	22	_	ns	
Reverse Recovery Time	t _{RR}		20	_	ns	I _F =1A, di/dt = 100A/µs
Reverse Recovery Charge	Qrr	_	7.9	_	nC	I _F = 1A, di/dt = 100A/μs

- 5. Device mounted on FR-4 PCB, with minimum recommended pad layout.
- 6. Device mounted on 1" x 1" FR-4 PCB with high coverage 2oz. copper, single sided.
 7. Short duration pulse test used to minimize self-heating effect.
- 8. Guaranteed by design. Not subject to product testing.





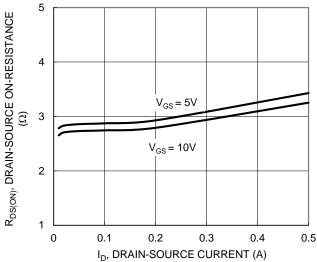


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

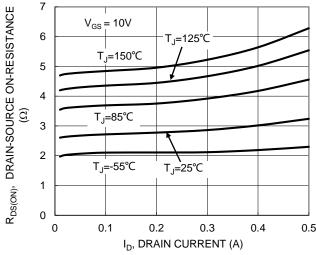


Figure 5. Typical On-Resistance vs. Drain Current and Junction Temperature

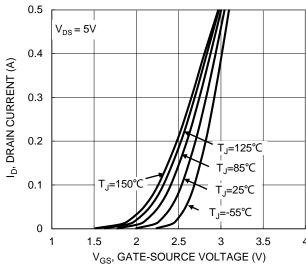


Figure 2. Typical Transfer Characteristic

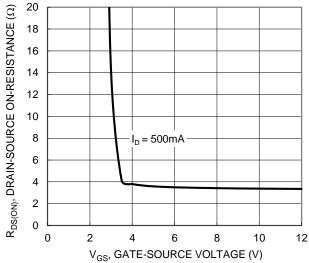


Figure 4. Typical Transfer Characteristic

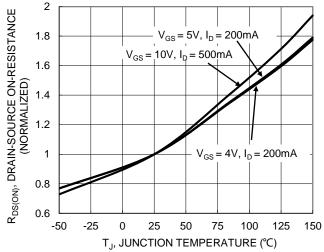


Figure 6. On-Resistance Variation with Junction Temperature



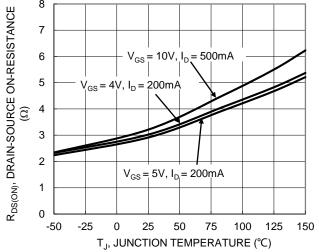


Figure 7. On-Resistance Variation with Junction Temperature

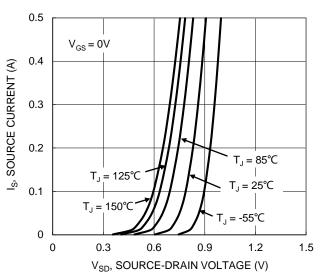


Figure 9. Diode Forward Voltage vs. Current

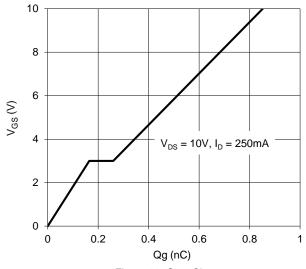


Figure 11. Gate Charge

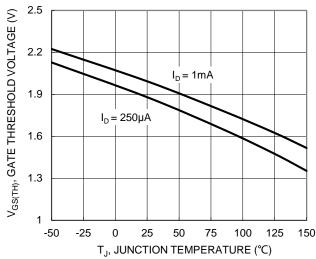
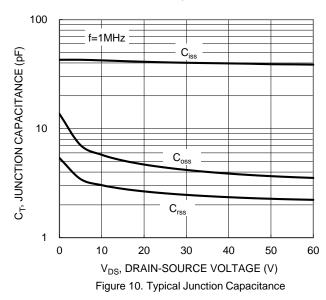


Figure 8. Gate Threshold Variation vs. Junction Temperature



1 R_{DS(ON)} Limited ID, DRAIN CURRENT (A) 0.1 $P_W = 100 \text{ms}$ 0.01 P_W =1s $T_{J(Max)} = 150^{\circ}C$ P_W =10s T_A = 25℃ Single Pulse DUT on 1*MRP Board $V_{GS} = 10V$ 0.001 0.1 10 100 V_{DS}, DRAIN-SOURCE VOLTAGE (V) Figure 12. SOA, Safe Operation Area



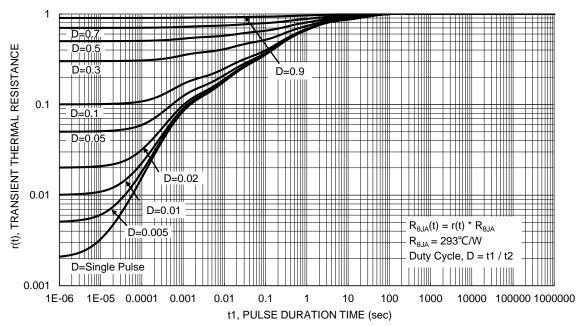


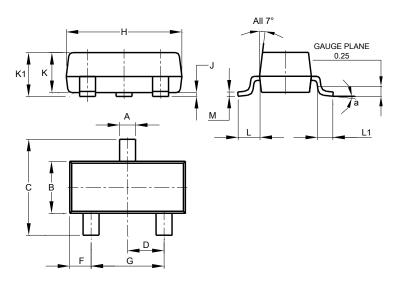
Figure 13. Transient Thermal Resistance



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT23

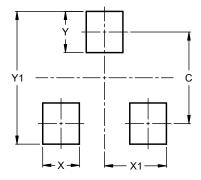


SOT23							
Dim	Min	Max	Тур				
Α	0.37	0.51	0.40				
В	1.20	1.40	1.30				
С	2.30	2.50	2.40				
D	0.89	1.03	0.915				
F	0.45	0.60	0.535				
G	1.78	2.05	1.83				
Н	2.80	3.00	2.90				
7	0.013	0.10	0.05				
K	0.890	1.00	0.975				
K 1	0.903	1.10	1.025				
L	0.45	0.61	0.55				
L1	0.25	0.55	0.40				
М	0.085	0.150	0.110				
а	0°	8°					
All Dimensions in mm							

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT23



Dimensions	Value (in mm)
С	2.0
X	0.8
X1	1.35
Y	0.9
Y1	2.9



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