

ZXTC2061E6

#### 12V COMPLEMENTARY MEDIUM POWER TRANSISTOR IN SOT26

#### **Features**

- NPN + PNP Combination
- BV<sub>CEO</sub> > 12 (-12)V
- BV<sub>EBO</sub> > 7 (-7)V
- Continuous Collector Current I<sub>C</sub> = 5 (-3.5)A
- V<sub>CE(sat)</sub> < 32 (-70)mV @ 1A</li>
- $R_{CE(sat)} = 25 (45) m\Omega$
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

#### **Description**

Advanced process capability has been used to achieve this high performance device. Combining NPN and PNP transistors in the SOT26 package provides a compact solution for the intended applications.

#### **Mechanical Data**

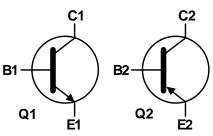
- Case: SOT26
- Case Material: Molded Plastic, "Green" Molding Compound;
   UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Plated Leads, Solderable per MIL-STD-202, Method 208 (63)
- Weight: 0.015 grams (Approximate)

#### **Applications**

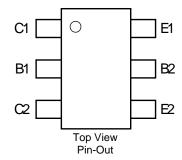
- MOSFET and IGBT Gate Driving
- Motor Drive







Device Symbol



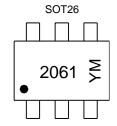
#### Ordering Information (Note 4)

Product	Complianace	Marking	Reel size (inches)	Tape width (mm)	Quantity per reel
ZXTC2061E6TA	AEC-Q101	2061	7	8	3,000

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com/quality/lead\_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

### **Marking Information**



2061 = Product Type Marking Code YM = Date Code Marking Y or  $\overline{Y}$  = Year (ex: C = 2015)

M or  $\overline{M}$  = Month (ex: 9 = September)

Date Code Key

	Year	201	5	2016	2017	2018	2019	2020	2021	1 20	22	2023	2024	2025
ſ	Code	С		D	E	F	G	Н	- 1	,	J	K	L	М
	Month	h	Jar	n Fek	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
	Code	)	1	2	3	4	5	6	7	8	9	0	N	D

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### Absolute Maximum Ratings - Q1 (NPN Transistor) (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	V <sub>CBO</sub>	20	V
Collector-Emitter Voltage	V <sub>CEO</sub>	12	V
Emitter-Base Voltage	V <sub>EBO</sub>	7	V
Continuous Collector Current	Ic	5	Α
Peak Pulsed Collector Current	I <sub>CM</sub>	12	A
Base Current	I <sub>B</sub>	1	А

### Absolute Maximum Ratings - Q2 (PNP Transistor) (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	V <sub>CBO</sub>	-12	V
Collector-Emitter Voltage	V <sub>CEO</sub>	-12	V
Emitter-Base Voltage	V <sub>EBO</sub>	-7	V
Continuous Collector Current	Ic	-3.5	А
Peak Pulsed Collector Current	I <sub>CM</sub>	-10	A
Base Current	I <sub>B</sub>	-1	А

## Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit	
	(Notes 5 & 9)		0.7 5.6		
	(Notes 6 & 9)		0.9 7.2		
Power Dissipation Linear Derating Factor	(Notes 6 & 10)	P <sub>D</sub>	1.1 8.8	W mW/°C	
	(Notes 7 & 9)		1.1 8.8		
	(Notes 8 & 9)		1.7 13.6		
	(Notes 5 & 9)		179		
	(Notes 6 & 9)		139		
Thermal Resistance, Junction to Ambient	(Notes 6 & 10)	$R_{\theta JA}$	113	°C/W	
	(Notes 7 & 9)		113	-C/VV	
	(Notes 8 & 9)		73		
Thermal Resistance, Junction to Lead	(Note 11)	R <sub>0</sub> JL	87.58		
Operating and Storage Temperature Range		T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C	

### ESD Ratings (Note 12)

Notes:

Characteristic	Symbol	Value	Unit	JEDEC Class
Electrostatic Discharge - Human Body Model	ESD HBM	4,000	V	3A
Electrostatic Discharge - Machine Model	ESD MM	400	V	С

- when operating in a steady-state condition.

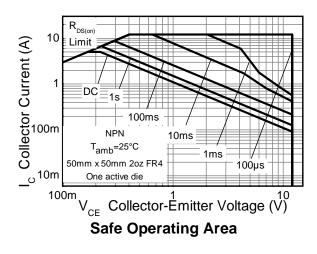
  6. Same as Note 5, except the device is surface mounted on 25mm x 25mm 1oz copper.
- 7. Same as Note 5, except the device is surface mounted on 50mm x 50mm 2oz copper.
- 8. Same as Note 7, except the device is measured at t < 5 seconds.
- 9. For device with one active die, both collectors attached to a common heatsink.
- 10. For device with two active dice running at equal power, split heatsink 50% to each collector.
- 11. Thermal resistance from junction to solder-point (at the end of the collector lead).
- 12. Refer to JEDEC specification JESD22-A114 and JESD22-A115.

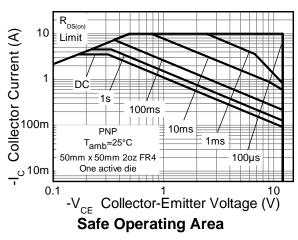
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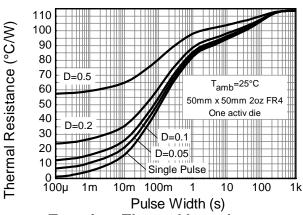
<sup>5.</sup> For a device surface mounted on 15mm x 15mm FR4 PCB with high coverage of single sided 1oz copper, in still air conditions; the device is measured

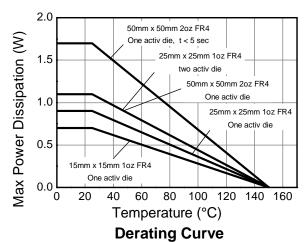


## **Thermal Characteristics and Derating Information**

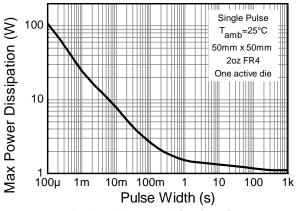








**Transient Thermal Impedance** 



**Pulse Power Dissipation** 



## Electrical Characteristics - Q1 (NPN Transistor) (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS						
Collector-Base Breakdown Voltage	BV <sub>CBO</sub>	20	40	_	V	$I_C = 100\mu A, I_E = 0$
Collector-Emitter Breakdown Voltage (Note 13)	$BV_{CEO}$	12	17	_	V	$I_C = 10 \text{mA}, I_B = 0$
Emitter-Base Breakdown Voltage	BV <sub>EBO</sub>	7	8.4	_	V	$I_E = 100 \mu A, I_C = 0$
Collector Cut-Off Current	I <sub>CBO</sub>	1	<1	50 0.5	nΑ μΑ	V <sub>CB</sub> = 20V V <sub>CB</sub> = 20V, T <sub>A</sub> = +100°C
Collector Cut-Off Current	I <sub>EBO</sub>	_	<1	50	nA	V <sub>EB</sub> = 5.6V
ON CHARACTERISTICS (Note 13)						
DC Current Gain	h <sub>FE</sub>	500 480 260	800 750 390	1,500		$I_{C} = 10 \text{mA}, V_{CE} = 2 \text{V}$ $I_{C} = 1.0 \text{A}, V_{CE} = 2 \text{V}$ $I_{C} = 5 \text{A}, V_{CE} = 2 \text{V}$
Collector-Emitter Saturation Voltage	V <sub>CE(sat)</sub>		32 50 65 145	40 60 80 180	mV	$I_C = 1.0A$ , $I_B = 100mA$ $I_C = 1.0A$ , $I_B = 10mA$ $I_C = 2.0A$ , $I_B = 40mA$ $I_C = 5A$ , $I_B = 100mA$
Base-Emitter Saturation Voltage	V <sub>BE(sat)</sub>	_	920	1,000	mV	I <sub>C</sub> = 5A, I <sub>B</sub> = 100mA
Base-Emitter Turn-On Voltage	V <sub>BE(on)</sub>	-	810	900	mV	$I_C = 5A$ , $V_{CE} = 2V$
SMALL SIGNAL CHARACTERISTICS						
Output Capacitance	Cobo	-	26	35	рF	V <sub>CB</sub> = 10V, f = 1.0MHz
Current Gain-Bandwidth Product	f <sub>T</sub>		260		MHz	V <sub>CE</sub> = 10V, I <sub>C</sub> = 50mA, f = 100MHz
Delay Time	t <sub>d</sub>		71	1	ns	
Rise Time	tr		70	_	ns	V <sub>CC</sub> = 10V, I <sub>C</sub> = 1A, I <sub>B1</sub> = -I <sub>B2</sub> = 10mA
Storage Time	ts	_	233	_	ns	$V_{CC} = 10V$ , $I_{C} = 1A$ , $I_{B1} = -I_{B2} = 10IIIA$
Fall Time	t <sub>f</sub>		72	_	ns	

## Electrical Characteristics - Q2 (PNP Transistor) (@TA = +25°C, unless otherwise specified.)

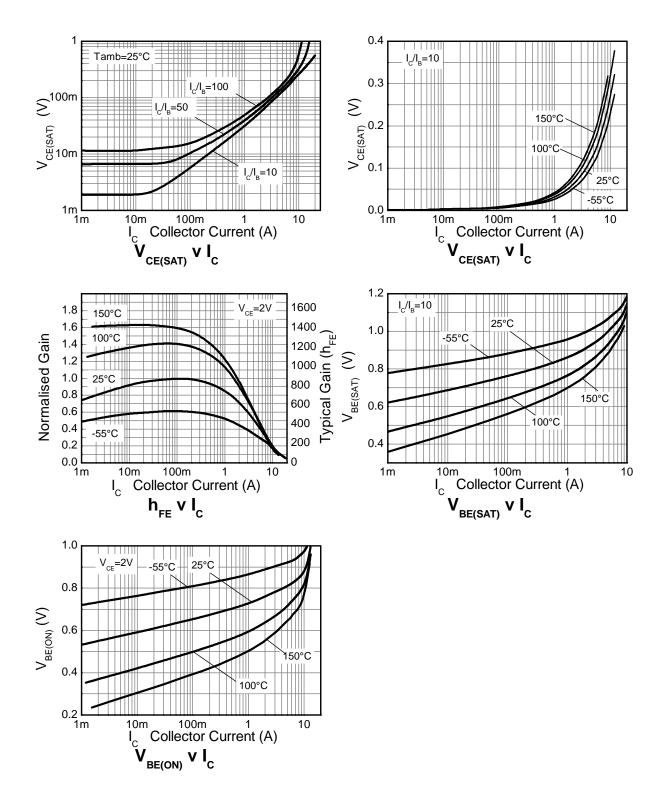
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS						
Collector-Base Breakdown Voltage	BV <sub>CBO</sub>	-12	-35	_	V	$I_C = -100\mu A, I_E = 0$
Collector-Emitter Breakdown Voltage (Note 13)	BV <sub>CEO</sub>	-12	-25	_	V	$I_C = -10 \text{mA}, I_B = 0$
Emitter-Base Breakdown Voltage	BV <sub>EBO</sub>	-7	-8.4	_	V	$I_E = -100\mu A, I_C = 0$
Collector Cut-Off Current	I <sub>CBO</sub>		< -1	-50 -0.5	nΑ μΑ	V <sub>CB</sub> = -12V V <sub>CB</sub> = -12V, T <sub>A</sub> = +100°C
Collector Cut-Off Current	I <sub>EBO</sub>	_	< -1	-50	nA	V <sub>EB</sub> = -5.6V
ON CHARACTERISTICS (Note 13)						
DC Current Gain	h <sub>FE</sub>	500 290 75	800 450 100	1500 — —	_	I <sub>C</sub> = -10mA, V <sub>CE</sub> = -2V I <sub>C</sub> = -1.0A, V <sub>CE</sub> = -2V I <sub>C</sub> = -3.5A, V <sub>CE</sub> = -2V
Collector-Emitter Saturation Voltage	V <sub>CE(sat)</sub>		-55 -170 -220 -150	-70 -265 -360 -200	mV	I <sub>C</sub> = -1.0A, I <sub>B</sub> = -100mA I <sub>C</sub> = -1.0A, I <sub>B</sub> = -10mA I <sub>C</sub> = -2.0A, I <sub>B</sub> = -40mA I <sub>C</sub> = -3.5A, I <sub>B</sub> = -350mA
Base-Emitter Saturation Voltage	V <sub>BE(sat)</sub>	_	-955	-1,050	mV	$I_C = -3.5A$ , $I_B = -350mA$
Base-Emitter Turn-On Voltage	V <sub>BE(on)</sub>	_	-830	-900	mV	I <sub>C</sub> = -3.5A, V <sub>CE</sub> = -2V
SMALL SIGNAL CHARACTERISTICS						
Output Capacitance	C <sub>obo</sub>	_	17	25	рF	$V_{CB} = -10V, f = 1.0MHz$
Current Gain-Bandwidth Product	f⊤		310	_	MHz	$V_{CE} = -10V$ , $I_{C} = -50mA$ , $f = 100MHz$
Delay Time	t <sub>d</sub>	-	41	_	ns	
Rise Time	t <sub>r</sub>	_	62	_	ns	$V_{CC} = -10V, I_C = -1A,$
Storage Time	ts	_	179	_	ns	$I_{B1} = -I_{B2} = -10 \text{mA}$
Fall Time	t <sub>f</sub>	_	65	_	ns	

13. Measured under pulsed conditions. Pulse width  $\leq$  300 $\mu$ s. Duty cycle  $\leq$  2%.

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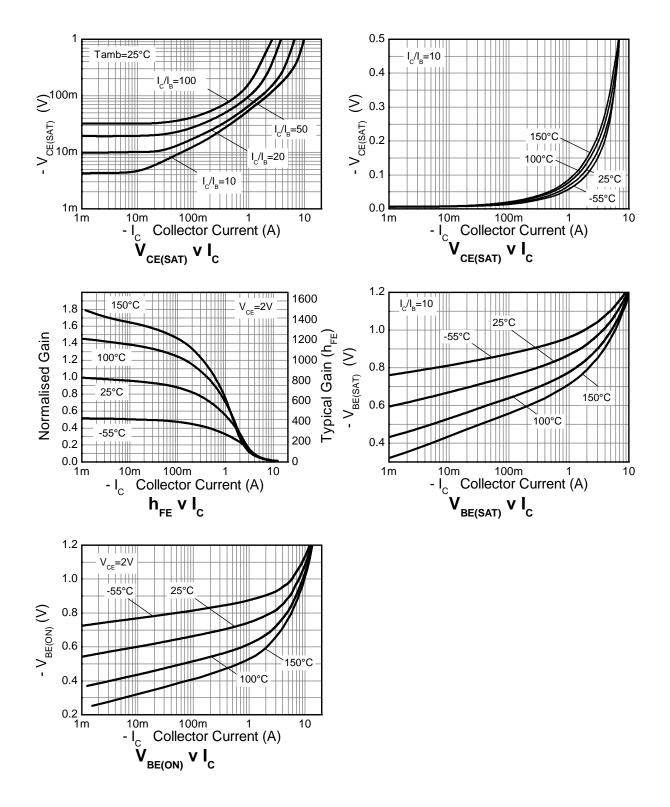


### Typical Electrical Characteristics - Q1 (NPN Transistor) (@TA = +25°C, unless otherwise specified.)





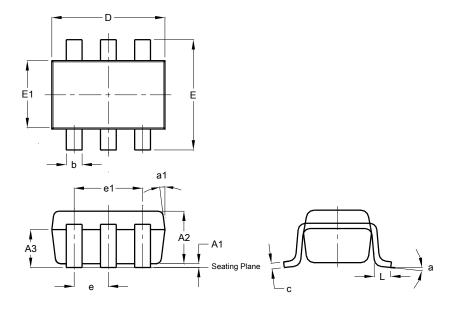
## Typical Electrical Characteristics - Q2 (PNP Transistor) (@TA = +25°C, unless otherwise specified.)





# **Package Outline Dimensions**

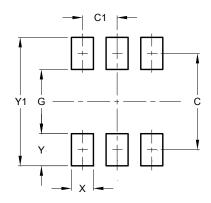
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.



SOT26						
Dim	Min	Max	Тур			
A1	0.013	0.10	0.05			
A2	1.00	1.30	1.10			
A3	0.70	0.80	0.75			
b	0.35	0.50	0.38			
С	0.10	0.20	0.15			
D	2.90	3.10	3.00			
е	-	-	0.95			
e1	-	-	1.90			
Е	2.70	3.00	2.80			
E1	1.50	1.70	1.60			
L	0.35	0.55	0.40			
а	-	-	8°			
a1	-	-	7°			
All	Dimen	sions	in mm			

# **Suggested Pad Layout**

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
С	2.40
C1	0.95
G	1.60
X	0.55
Υ	0.80
Y1	3 20



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