



Product Summary

BV _{DSS}	R _{DS(ON)} Max	I _D Max T _A = +25°C
	$27m\Omega @ V_{GS} = 4.5V$	5.6
20V	$35m\Omega @ V_{GS} = 2.5V$	4.9

Description and Applications

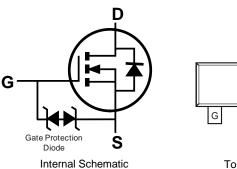
This MOSFET is designed to minimize the on-state resistance (R_{DS(ON)}) yet maintain superior switching performance, which makes it ideal for high-efficiency power management applications.

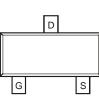
- Backlighting
- **Power Management Functions**
- **DC-DC** Converters
- Motor Control

ESD protected Gate



Top View





Top View

Ordering Information (Note 4)

Part Number	Case	Packaging
DMN2025U-7	SOT23	3000/Tape & Reel

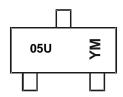
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. Notes:

2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



05U = Product Type Marking Code YM = Date Code Marking Y or \overline{Y} = Year (ex: E = 2017) M = Month (ex: 9 = September)

Date Code Key

Year	2017		2018	2019		2020	2021		2022	2023		2024
Code	E		F	G		Н			J	K		L
Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code		-	-		-	-	_	-		-		_

N-CHANNEL ENHANCEMENT MODE MOSFET

Features and Benefits

- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- **ESD Protected Gate**
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

Mechanical Data

- Case: SOT23 •
- Case Material: Molded Plastic, "Green" Molding Compound. . UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish-Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 @3
- Terminals Connections: See Diagram Below
- Weight: 0.009 grams (Approximate)



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Drain-Source Voltage		V _{DSS}	20	V
Gate-Source Voltage		V _{GSS}	±12	V
Continuous Drain Current (Note 6) $V_{GS} = 4.5V$ State $T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$		ID	5.6 4.9	А
Maximum Continuous Body Diode Forward Curre	ent (Note 6)	ls	1.4	А
Pulsed Drain Current (10µs Pulse, Duty Cycle =	1%)	I _{DM}	40	A

Thermal Characteristics

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)		PD	0.8	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	R _{OJA}	162	°C/W
Total Power Dissipation (Note 6)		PD	1.3	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	R _{OJA}	98	°C/W
Operating and Storage Temperature Range		T _{J,} T _{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)	Symbol	141111	Ч	IVIAN	Unit	Test condition
Drain-Source Breakdown Voltage	BV _{DSS}	20	_	_	V	$V_{GS} = 0V, I_{D} = 250\mu A$
Zero Gate Voltage Drain Current $T_J = +25^{\circ}C$	IDSS	_	_	1.0	μA	$V_{DS} = 20V, V_{GS} = 0V$
Gate-Source Leakage	Igss	_	_	±10	μA	$V_{GS} = \pm 10V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V _{GS(TH)}	0.5	_	0.9	V	$V_{DS} = V_{GS}, I_D = 250 \mu A$
			20	27		$V_{GS} = 4.5 V, I_D = 6.5 A$
Static Drain-Source On-Resistance	R _{DS(ON)}	—	26.7	35	mΩ	V _{GS} = 2.5V, I _D = 5.5A
			49.1	65		V _{GS} = 1.8V, I _D = 3.5A
Diode Forward Voltage	V _{SD}	—	0.8	1.2	V	$V_{GS} = 0V, I_D = 5A$
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	Ciss	—	485	—	pF	
Output Capacitance	Coss	_	91	_	pF	V _{DS} = 10V, V _{GS} = 0V - f = 1.0MHz
Reverse Transfer Capacitance	C _{rss}	_	77	—	pF	T = T.00012
Gate Resistance	Rg	_	3.1	—	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1MHz$
Total Gate Charge	Qg	_	5.9	—	nC	
Gate-Source Charge	Q _{gs}	_	12	—	nC	$V_{GS} = 4.5V, V_{DS} = 10V, I_D = 6.5A$
Gate-Drain Charge	Q _{gd}	_	0.8	—	nC	
Turn-On Delay Time	t _{D(ON)}	_	3.4	—	ns	
Turn-On Rise Time	t _R	_	5.4	—	ns	V _{DS} = 10V, V _{GS} = 4.5V,
Turn-Off Delay Time	t _{D(OFF)}	_	18	—	ns	$R_L = 10\Omega, R_g = 6\Omega, I_D = 1A$
Turn-Off Fall Time	t _F	—	9.3	—	ns	
Reverse Recovery Time	t _{RR}	_	7.7	—	ns	I _F = 1.0A, di/dt = 100A/µs
Reverse Recovery Charge	Q _{RR}	_	1.5	—	nC	I _F = 1.0A, di/dt = 100A/µs

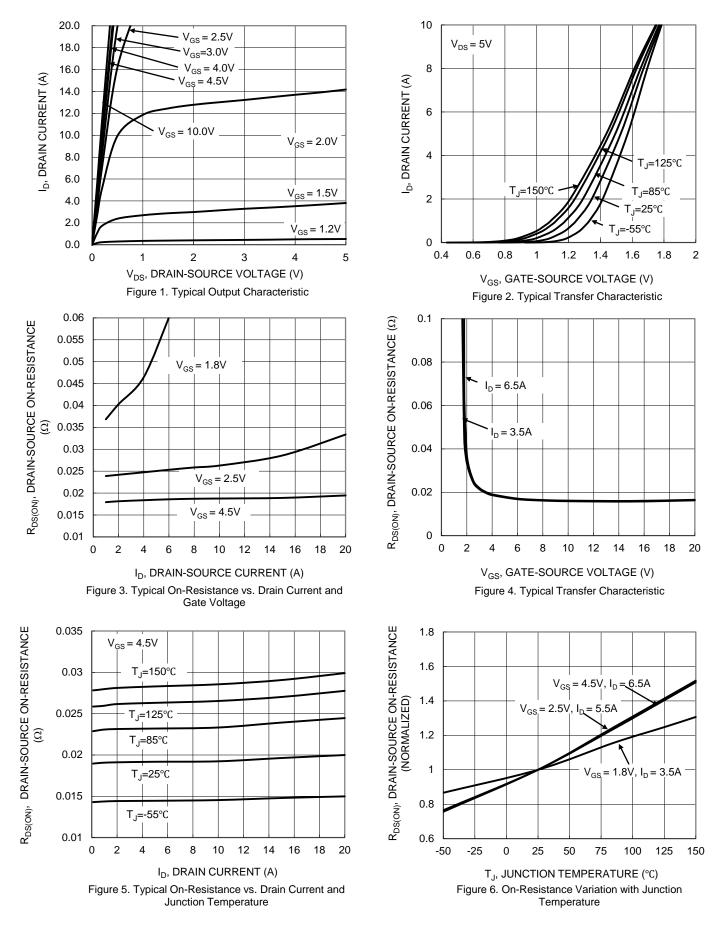
Notes:

5. Device mounted on FR-4 substrate PCB, 2oz copper, with minimum recommended pad layout.

Device mounted on FR-4 substrate PCB, 202 copper, with 1inch square copper plate.
Short duration pulse test used to minimize self-heating effect.

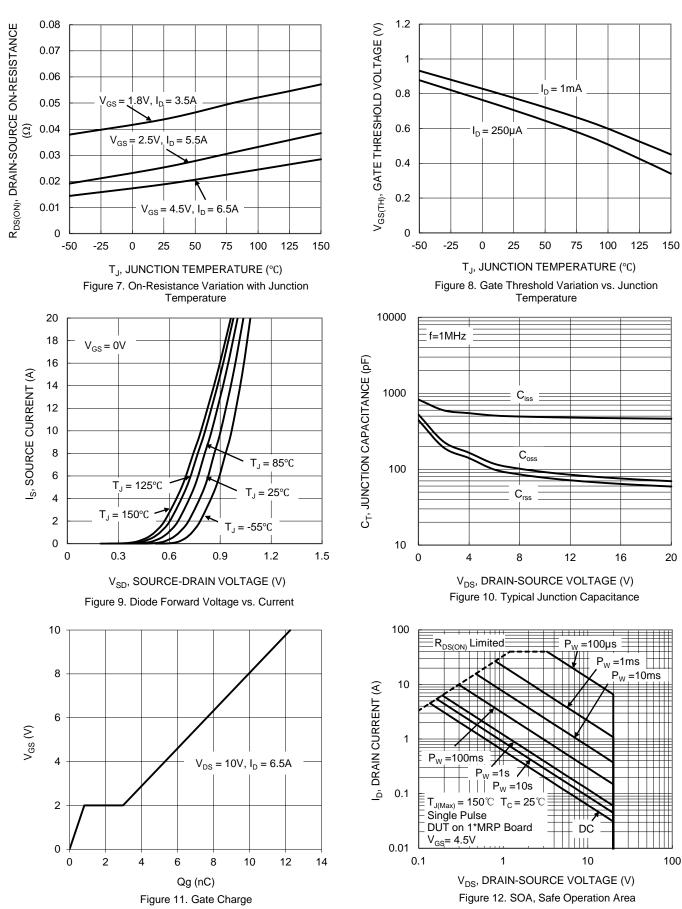
8. Guaranteed by design. Not subject to product testing.



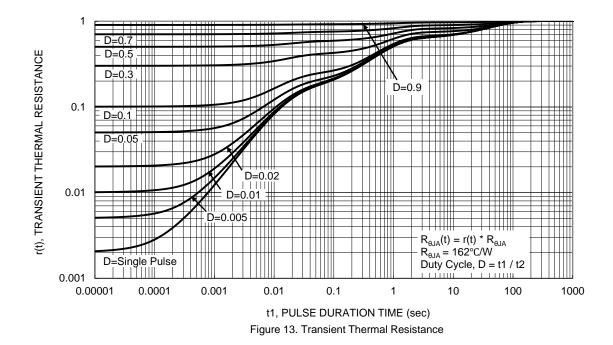




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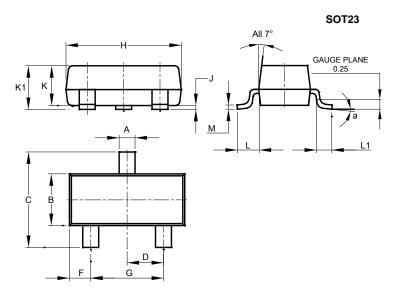






Package Outline Dimensions

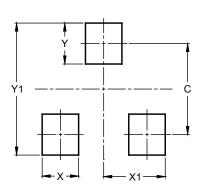
Please see http://www.diodes.com/package-outlines.html for the latest version.



	SOT23							
Dim	Min	Max	Тур					
Α	0.37	0.51	0.40					
В	1.20	1.40	1.30					
С	2.30	2.50	2.40					
D	0.89	1.03	0.915					
F	0.45	0.60	0.535					
G	1.78	2.05	1.83					
Н	2.80	3.00	2.90					
J	0.013	0.10	0.05					
K	0.890	1.00	0.975					
K1	0.903	1.10	1.025					
L	0.45	0.61	0.55					
L1	0.25	0.55	0.40					
Μ	0.085	0.150	0.110					
а	0°	8°	_					
All	Dimens	ions in	mm					

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.



SOT23

Dimensions	Value (in mm)
С	2.0
Х	0.8
X1	1.35
Y	0.9
Y1	2.9



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