

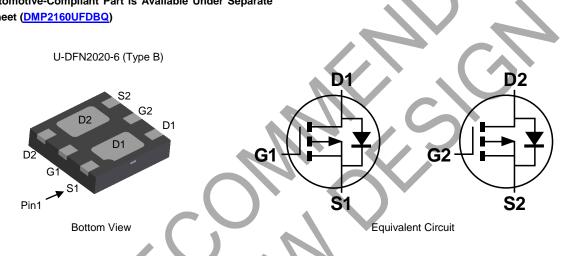
## DUAL P-CHANNEL ENHANCEMENT MODE MOSFET

#### **Features**

- Low On-Resistance
  - $70m\Omega @V_{GS} = -4.5V$
  - $85m\Omega @V_{GS} = -2.5V$
  - $86m\Omega$  (Typ) @V<sub>GS</sub> = -1.8V
- Low Gate Threshold Voltage, -0.9V Max
- Fast Switching Speed
- Low Input/Output Leakage
- Low Profile, 0.5mm Max Height
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- An Automotive-Compliant Part is Available Under Separate Datasheet (DMP2160UFDBQ)

# **Mechanical Data**

- Case: U-DFN2020-6 (Type B)
- Case Material: Molded Plastic, "Green" Molding Compound; UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram
- Terminals: Finish NiPdAu Annealed over Copper Leadframe; Solderable per MIL-STD-202, Method 208 @4
- Weight: 0.0065 grams (Approximate)



#### Ordering Information (Note 4)

	Part Number	Case	Packaging				
	DMP2160UFDB-7	U-DFN2020-6 (Type B)	3,000/Tape & Reel				
	DMP2160UFDB-7R	U-DFN2020-6 (Type B)	3,000/Tape & Reel				
Notes:	Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.						

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

2. See http://www.diodes.com/quality/lead\_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

## **Marking Information**

#### U-DFN2020-6 (Type B)



P2 = Marking Code YM = Date Marking Y = Year (ex: E = 2017)M = Month (ex: 9 = September)Dot Denotes Pin 1

Date Code Key												
Year	2008	2009		2015	5 201	6 20	)17	2018	2019	2020	2021	2022
Code	V	W		С	D		E	F	G	Н		J
Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	Ν	D



# Maximum Ratings (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Drain-Source Voltage	V <sub>DSS</sub>	-20	V
Gate-Source Voltage	V <sub>GSS</sub>	±12	V
Drain Current (Note 5)	ID	-3.8	А
Pulsed Drain Current (Note 6)	I <sub>DM</sub>	-13	A

## Thermal Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 5)	PD	1.4	W
Thermal Resistance, Junction to Ambient	$R_{ heta JA}$	89	°C/W
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	<b>℃</b>

## Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	-20	—		V	$V_{GS} = 0V, I_D = -250\mu A$
Zero Gate Voltage Drain Current	IDSS	-	—	-1	μA	$V_{DS} = -20V, V_{GS} = 0V$
Gate-Source Leakage	I <sub>GSS</sub>		I	±100 ±800	nA	$V_{GS} = \pm 8V, V_{DS} = 0V$ $V_{GS} = \pm 12V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)				V		
Gate Threshold Voltage	V <sub>GS(TH)</sub>	-0.45	)	-0.9	V	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$
		-	54	70		$V_{GS} = -4.5V, I_D = -2.8A$
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>	—	68	85	mΩ	$V_{GS} = -2.5V, I_D = -2.0A$
		—	86	—		$V_{GS} = -1.8V, I_D = -1.0A$
Forward Transfer Admittance	Y <sub>fs</sub>		8		S	V <sub>DS</sub> = -5V, I <sub>D</sub> = -2.8A
Diode Forward Voltage (Note 7)	V <sub>SD</sub>		-0.7	-1.2	V	$V_{GS} = 0V, I_{S} = -1.6A$
DYNAMIC CHARACTERISTICS						
Input Capacitance	Ciss		536		pF	
Output Capacitance			68	_	pF	V <sub>DS</sub> = -10V, V <sub>GS</sub> = 0V f = 1.0MHz
Reverse Transfer Capacitance	Crss		59		pF	
Gate Resistance	Rg		34	_	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1MHz$
Total Gate Charge			6.5		nC	
Gate-Source Charge			0.8		nC	V <sub>GS</sub> = -4.5V, V <sub>DD</sub> = -10V, I <sub>D</sub> = -1.5A
Gate-Drain Charge	Q <sub>gd</sub>		1.4		nC	ID = -1.5A
Turn-On Delay Time	t <sub>D(ON)</sub>		11.51	—	ns	
Turn-On Rise Time			12.09	-	ns	$V_{GEN} = -4.5V, V_{DD} = -10V,$
Turn-Off Delay Time		_	55.34	_	ns	$R_L = 10\Omega, R_G = 6\Omega$
Turn-Off Fall Time	t⊨	—	27.54	—	ns	

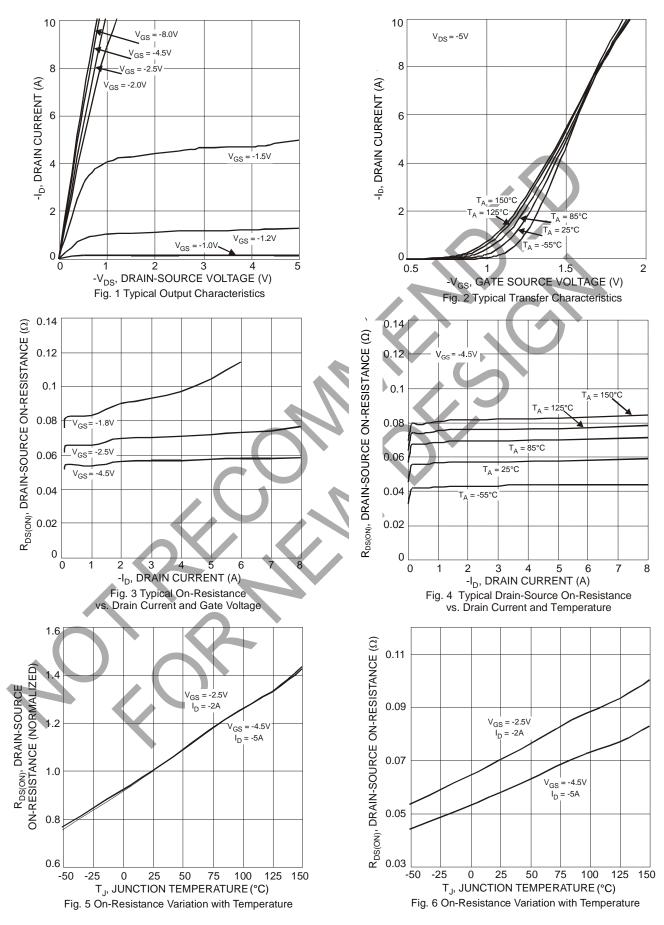
Notes:

Device mounted on FR-4 PCB, on minimum recommended, 2oz Copper pad layout.
Repetitive rating, pulse width limited by junction temperature.
Short duration pulse test used to minimize self-heating effect.



#### NOT RECOMMENDED FOR NEW DESIGN USE <u>DMP2065UFDB</u>



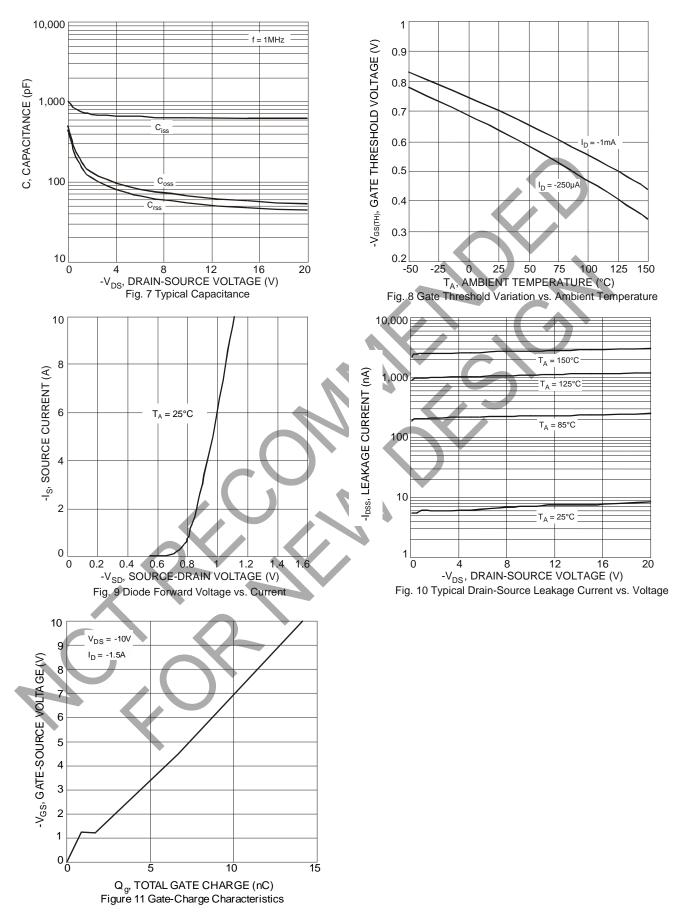


DMP2160UFDB Document number: DS31643 Rev. 10 - 3



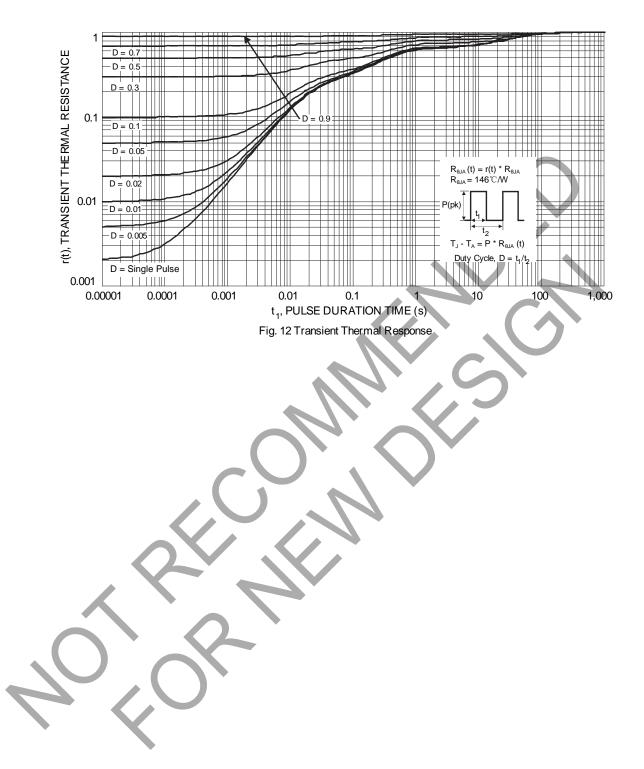
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## DMP2160UFDB





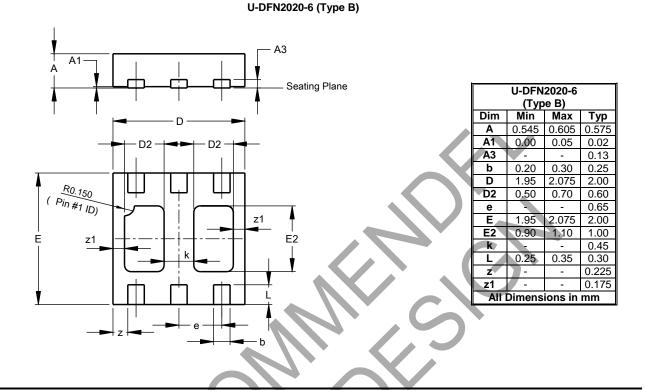
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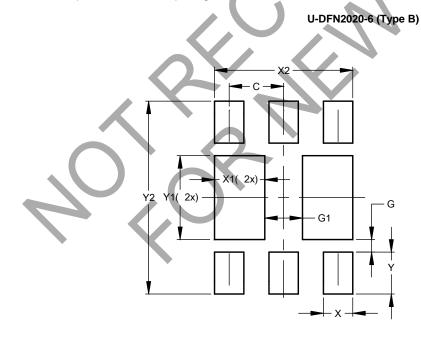
## Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.



## **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.



Dimensions	Value (in mm)				
С	0.650				
G	0.150				
G1	0.450				
Х	0.350				
X1	0.600 1.650				
X2					
Ŷ	0.500				
Y1	1.000				
Y2	2.300				



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