



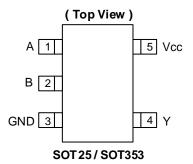
SINGLE 2-INPUT POSITIVE NAND GATE

Description

The 74AHC1G00Q is an automotive compliant single, two-input positive NAND gate with a standard push-pull output. The device is designed for operation with a power supply range of 2.0V to 5.5V. The gate performs the positive Boolean function:

$$Y = \overline{A \cdot B}$$
 or $Y = \overline{A} + \overline{B}$

Pin Assignments



Features

- Grade 1 Ambient Temperature Operation: -40°C to +125°C
- Supply Voltage Range from 2.0V to 5.5V
- ±8mA Output Drive at 4.5V
- CMOS Low-Power Consumption
- Schmitt Trigger Action at All Inputs Make the Circuit Tolerant for Slower Input Rise and Fall Time
- Inputs not Limited by Vcc
- Balanced Propagation Delays
- Balanced Drive Capability
- ESD Protection Tested per AEC-Q100
- Exceeds 2000-V Human Body Model (AEC-Q100-002)
- Exceeds 1000-V Charged Device Model (AEC-Q100-011)
- Latch-Up Exceeds 100mA (AEC-Q100-004)
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- The 74AHC1G00Q is suitable for automotive applications requiring specific change control; this part is AEC-Q100 qualified, PPAP capable, and manufactured in IATF 16949 certified facilities.

https://www.diodes.com/quality/product-definitions/

Applications

- General Purpose Logic
- Wide Array of Products, such as:
 - Automotive Applications within Grade 1 Temperature Range
 - Industrial Computing/Controls/Automation
 - High Reliability Networking/Communications
 - Industrial/Agricultural Equipment

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

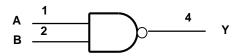
74AHC1G00Q Document number: DS41446 Rev. 1 - 2



Pin Descriptions

Pin Name	Description
Α	Data Input
В	Data Input
GND	Ground
Y	Data Output
Vcc	Supply Voltage

Logic Diagram



Function Table

Inp	Output	
Α	В	Υ
Н	Н	L
L	Х	Н
Х	L	Н

Absolute Maximum Ratings (Notes 4 & 5)

Symbol	Description	Rating	Unit
ESD HBM	Human Body Model ESD Protection	2	kV
ESD CDM	Charged Device Model ESD Protection	1	kV
Vcc	Supply Voltage Range	-0.5 to 6.5	V
Vı	Input Voltage Range	-0.5 to 6.5	V
Vo	Voltage Applied to Output in High or Low State	-0.5 to Vcc + 0.5	V
lıĸ	Input Clamp Current V _I < 0	-20	mA
Іок	Output Clamp Current (Vo < 0 or Vo > Vcc)	±20	mA
lo	Continuous Output Current (Vo = 0 to Vcc)	±25	mA
Icc	Continuous Current Through Vcc	75	mA
Ignd	Continuous Current Through GND	-75	mA
TJ	Operating Junction Temperature	-40 to +150	°C
Tstg	Storage Temperature	-65 to +150	°C
PD	Total Power Dissipation (Note 6)	250	mW

Notes:

- 4. Stresses beyond the absolute maximum can result in immediate failure or reduced reliability. These are stress values and device operation should be within recommend values.
- 5. Forcing the maximum allowed voltage could cause a condition exceeding the maximum current or conversely forcing the maximum current could cause a condition exceeding the maximum voltage. The ratings of both current and voltage must be maintained within the controlled range.
- 6. This will need to be derated at higher operating temperatures to prevent exceeding maximum T_J. Refer to package thermal characteristics section.



Recommended Operating Conditions (Note 7)

Symbol		Parameter	Min	Max	Unit	
Vcc	Operating Voltage	_	2	5.5	V	
		Vcc = 2V	1.5	_		
VIH	High-Level Input Voltage	Vcc = 3V	2.1	_	V	
		Vcc = 5.5V	3.85	_		
		Vcc = 2V	_	0.5		
VIL	Low-Level Input Voltage	Vcc = 3V	_	0.9	V	
		Vcc = 5.5V	_	1.65		
Vı	Input Voltage		0	5.5	V	
Vo	Output Voltage		0	Vcc	V	
		Vcc = 2V	_	-50	μA	
Іон	High-Level Output Current	$VCC = 3.3V \pm 0.3V$	_	-4	1	
		$V_{CC} = 5V \pm 0.5V$	_	-8	mA	
		Vcc = 2V	_	50	μA	
loL	Low-Level Output Current	$V_{CC} = 3.3V \pm 0.3V$	_	4	A	
		$V_{CC} = 5V \pm 0.5V$	_	8	mA	
A4/A)/	Input Transition Rise or Fall	$V_{CC} = 3.3V \pm 0.3V$	_	100		
Δt/ΔV	Rate	$V_{CC} = 5V \pm 0.5V$		20	ns/V	
TA	Ambient Temperature	_	-40	+125	°C	

Note:

Electrical Characteristics (All typical values are at $V_{CC} = 3.3V$, $T_A = +25$ °C)

Comple al	Danamatan	Took Conditions	V		+25°C		-40°C to	o +85°C	-40°C to	+125°C	l lmit	
Symbol	Parameter	Test Conditions	Vcc	Min	Тур	Max	Min	Max	Min	Max	Unit	
		M. Maran Mar	2V	1.9	2	_	1.9	_	1.9	_		
		VI = VIH OF VIL	3V	2.9	3	_	2.9	_	2.9	_		
	High Level	Іон = -50μΑ	4.5V	4.4	4.5	_	4.4	_	4.4	_		
Vон	Output Voltage	$V_I = V_{IH} \text{ or } V_{IL}$ $I_{OH} = -4mA$	3V	2.58	1	_	2.48	_	2.40	_	V	
		VI = VIH OR VIL IOH = -8mA	4.5V	3.94		_	3.80	_	3.70	_		
			\\\.\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\	2V	_	_	0.1	_	0.1	_	0.1	
		VI = VIH Or VIL	3V	_	_	0.1	_	0.1	_	0.1		
	I am I am I Ontant	$I_{OL} = 50\mu A$	4.5V	_	ı	0.1	_	0.1	1	0.1		
Vol	Low Level Output Voltage	VI = VIH OR VIL IOL = 4mA	3V	_	1	0.36	_	0.44	ı	0.55	V	
		$V_I = V_{IH} \text{ or } V_{IL}$ $I_{OL} = 8mA$	4.5V	_	1	0.36	_	0.44	1	0.55		
lı	Input Current	Vı = 5.5V or GND	0 to 5.5V	_	_	±0.1	_	±1	_	±2	μΑ	
Icc	Supply Current	V _I = 5.5V or GND I _O = 0	5.5V	_		1	_	10	_	40	μΑ	
Сі	Input Capacitance	V _I = V _{CC} or GND	5.5V	_	1.5	10	_	10	_	10	pF	

^{7.} Unused inputs should be held at V_{CC} or Ground.



Package Characteristics

Symbol	Parameter	Package	Test Conditions	Min	Тур	Max	Unit
0	Thermal Resistance	SOT25	Note 0	1	184	1	9044
θ JA	Junction-to-Ambient	SOT353	Note 8	-	385	1	°C/W
0	Thermal Resistance	SOT25	Nata 0	_	62	_	0044
θυς	Junction-to-Case	SOT353	Note 8		164	1	°C/W

Note: 8. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.

Switching Characteristics

 $Vcc = 3.3V \pm 0.3V$ (See Figure 1)

Parameter	From	То			+25°C		-40°C t	o +85°C	-40°C to	+125°C	Unit
	(Input)	(Output)	Conditions	Min	Тур	Max	Min	Max	Min	Max	
4	A == D	V	C _L = 15pF	1.0	4.5	7.9	1.0	9.5	1.0	10.5	ns
tPD	A or B	Y	C _L = 50pF	1.0	6.5	11.4	1.0	13.0	1.0	14.5	ns

Vcc = 5V ± 0.5V (See Figure 1)

Parameter	From	То	Test		+25°C		-40°C t	o +85°C	-40°C to	+125°C	Unit
	(Input)	(Output)	Conditions	Min	Тур	Max	Min	Max	Min	Max	
4	A = = D	V	C _L = 15pF	1.0	3.5	5.5	1.0	6.5	1.0	7.0	ns
tpD	A or B	Y	C _L = 50pF	1.0	4.9	7.5	1.0	8.5	1.0	9.5	ns

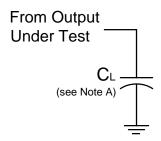
Operating Characteristics

 $T_A = +25$ °C

	Parameter	Test Conditions	Vcc = 5V	Unit
raidilletei		rest conditions	Тур	Oilit
C _{PD}	Power Dissipation Capacitance	f = 1MHz No Load	18	pF



Measurement Information



Vcc	In	puts	V _M	CL	
VCC	Vı	t _R /t _F	▼ IWI	J.	
3.3V±0.3V	Vcc	≤3ns	Vcc/2	15pF	
5V±0.5V	Vcc	≤3ns	Vcc/2	15pF	
3.3V±0.3V	Vcc	≤3ns	Vcc/2	50pF	
5V±0.5V	Vcc	≤3ns	Vcc/2	50pF	

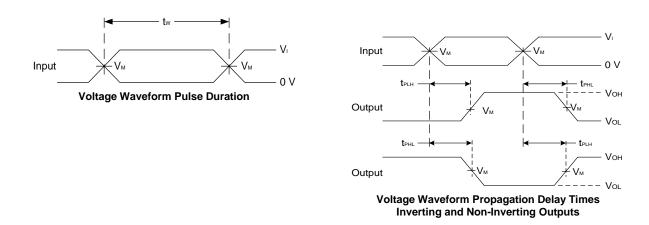


Figure 1. Load Circuit and Voltage Waveforms

Notes:

- A. Includes test lead and test apparatus capacitance.

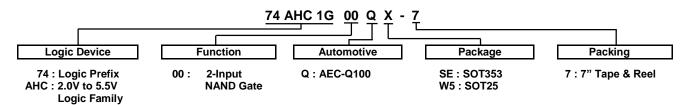
 B. All pulses are supplied at pulse repetition rate ≤ 1MHz.
- C. Inputs are measured separately one transition per measurement.

74AHC1G00Q Document number: DS41446 Rev. 1 - 2



1G: One Gate

Ordering Information (Note 9)



Part Number	Package	Package	Package Size	7" Tape	and Reel
l art Number	Code	(Notes 10 & 11)	i ackage Size	Quantity	Part Number Suffix
74AHC1G00QSE-7	SE	SOT353	2.15 mm \times 2.1 mm \times 1.1 mm 0.65 mm lead pitch	3000/Tape & Reel	-7
74AHC1G00QW5-7	W5	SOT25	3.0 mm \times 2.8 mm \times 1.2 mm 0.95 mm lead pitch	3000/Tape & Reel	-7

Notes:

9. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

10. Pad layout as shown in Diodes Incorporated's package outline PDFs, which can be found on our website at http://www.diodes.com/package-outlines.html.

11. The taping orientation is located on our website at https://www.diodes.com/assets/Packaging-Support-Docs/ap02007.pdf.

Marking Information

(Top View)

XXX: Identification Code Year 0~9

Week: A~Z 1~26 week a~z 27~52 week

z represents week 52 and 53

X : A~ Z: Internal Code

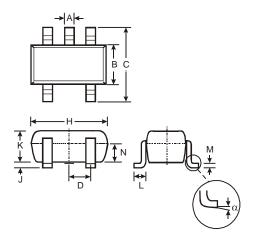
SOT25 / SOT353

Part Number	Package	Identification Code
74AHC1G00QW5-7	SOT25	YRQ
74AHC1G00QSE-7	SOT353	YRQ



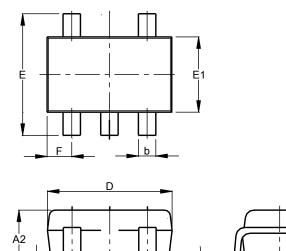
Package Outline Dimensions

(1) Package Type: SOT25



SOT25					
Dim	Min	Max	Тур		
Α	0.35	0.50	0.38		
В	1.50	1.70	1.60		
С	2.70	3.00	2.80		
D	-	-	0.95		
H	2.90	3.10	3.00		
J	0.013	0.10	0.05		
K	1.00	1.30	1.10		
L	0.35	0.55	0.40		
M	0.10	0.20	0.15		
N	0.70	0.80	0.75		
α	0°	8°	-		
All Dimensions in mm					

(2) Package Type: SOT353



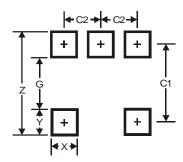
SOT353					
Dim	Min	Max	Тур		
A1	0.00	0.10	0.05		
A2	0.90	1.00	0.95		
b	0.10	0.30	0.25		
С	0.10	0.22	0.11		
D	1.80	2.20	2.15		
Е	2.00	2.20	2.10		
E1	1.15	1.35	1.30		
е	0.650 BSC				
F	0.40	0.45	0.425		
L	0.25	0.40	0.30		
а	0°	8°			
All Dimensions in mm					



Suggested Pad Layout

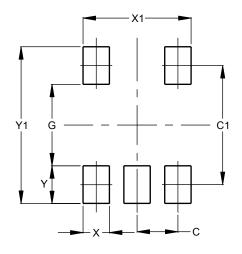
Please see http://www.diodes.com/package-outlines.html for the latest version.

(1) Package Type: SOT25



Dimensions	Value	
Z	3.20	
G	1.60	
Х	0.55	
Y	0.80	
C1	2.40	
C2	0.95	

(2) Package Type: SOT353



Dimensions	Value (in mm)		
С	0.650		
C1	1.900		
G	1.300		
Х	0.420		
X1	1.720		
Υ	0.600		
Y1	2.500		

Mechanical Data

SOT25

- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Plated Leads, Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.0158 grams (Approximate)

SOT353

- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Plated Leads, Solderable per MIL-STD-202, Method 208 @3
- Weight: 0.0064 grams (Approximate)



IMPORTANT NOTICE

DIODES INCORPORATED MAKES NO WARRANTY OF ANY KIND, EXPRESS OR IMPLIED, WITH REGARDS TO THIS DOCUMENT, INCLUDING, BUT NOT LIMITED TO, THE IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A PARTICULAR PURPOSE (AND THEIR EQUIVALENTS UNDER THE LAWS OF ANY JURISDICTION).

Diodes Incorporated and its subsidiaries reserve the right to make modifications, enhancements, improvements, corrections or other changes without further notice to this document and any product described herein. Diodes Incorporated does not assume any liability arising out of the application or use of this document or any product described herein; neither does Diodes Incorporated convey any license under its patent or trademark rights, nor the rights of others. Any Customer or user of this document or products described herein in such applications shall assume all risks of such use and will agree to hold Diodes Incorporated and all the companies whose products are represented on Diodes Incorporated website, harmless against all damages.

Diodes Incorporated does not warrant or accept any liability whatsoever in respect of any products purchased through unauthorized sales channel. Should Customers purchase or use Diodes Incorporated products for any unintended or unauthorized application, Customers shall indemnify and hold Diodes Incorporated and its representatives harmless against all claims, damages, expenses, and attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized application.

Products described herein may be covered by one or more United States, international or foreign patents pending. Product names and markings noted herein may also be covered by one or more United States, international or foreign trademarks.

This document is written in English but may be translated into multiple languages for reference. Only the English version of this document is the final and determinative format released by Diodes Incorporated.

LIFE SUPPORT

Diodes Incorporated products are specifically not authorized for use as critical components in life support devices or systems without the express written approval of the Chief Executive Officer of Diodes Incorporated. As used herein:

- A. Life support devices or systems are devices or systems which:
 - 1. are intended to implant into the body, or
 - 2. support or sustain life and whose failure to perform when properly used in accordance with instructions for use provided in the labeling can be reasonably expected to result in significant injury to the user.
- B. A critical component is any component in a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or to affect its safety or effectiveness.

Customers represent that they have all necessary expertise in the safety and regulatory ramifications of their life support devices or systems, and acknowledge and agree that they are solely responsible for all legal, regulatory and safety-related requirements concerning their products and any use of Diodes Incorporated products in such safety-critical, life support devices or systems, notwithstanding any devices- or systems-related information or support that may be provided by Diodes Incorporated. Further, Customers must fully indemnify Diodes Incorporated and its representatives against any damages arising out of the use of Diodes Incorporated products in such safety-critical, life support devices or systems.

Copyright © 2020, Diodes Incorporated

www.diodes.com

74AHC1G00Q 9 of 9
Document number: DS41446 Rev. 1 - 2
Downloaded From Oneyac.com

单击下面可查看定价,库存,交付和生命周期等信息

>>Diodes Incorporated(达迩科技(美台))