

COMPLEMENTARY PAIR ENHANCEMENT MODE MOSFET

Product Summary

Device	V _{(BR)DSS}	R _{DS(ON)} max	I _D max T _A = +25°C
01	30V	$27m\Omega @ V_{GS} = 10V$	7.2A
Q1		35mΩ @ V _{GS} = 4.5V	6.0A
Q2	Q2 -30V -	25mΩ @ V _{GS} = -10V	-7.6A
		41mΩ @ V _{GS} = -4.5V	-6.2A

Description

This new generation MOSFET is designed to minimize the on-state resistance ($R_{DS(ON)}$) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

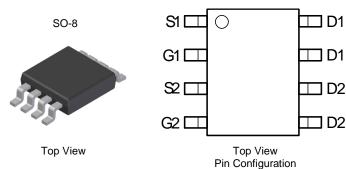
- DC-DC Converters
- Power Management Functions
- Backlighting

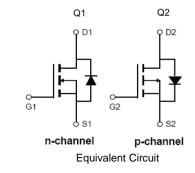


- Low Input Capacitance
- Low On-Resistance
- Fast Switching Speed
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- PPAP Capable (Note 4)

Mechanical Data

- Case: SO-8
- Case Material: Molded Plastic, "Green" Molding Compound; UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram
- Terminals: Finish Tin Finish Annealed over Copper Leadframe; Solderable per MIL-STD-202, Method 208⁽³⁾
- Weight: 0.074 grams (Approximate)





Ordering Information (Note 5)

Part Number	Case	Packaging
DMC3028LSDXQ-13	SO-8	2,500/Tape & Reel

Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

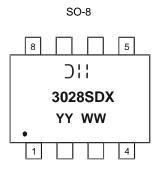
2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. Automotive products are AEC-Q101 qualified and are PPAP capable. Automotive, AEC-Q101 and standard products are electrically and thermally the same, except where specified. For more information, please refer to http://www.diodes.com/quality/product_grade_definitions/.

5. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information



⇒ Hanufacturer's Marking
3028SDX = Product Type Marking Code
YYWW = Date Code Marking
YY = Year (ex: 13 = 2013)
WW = Week (01 - 53)



Maximum Ratings – Q1 and Q2 ($@T_A = +25^{\circ}C$, unless otherwise specified.)

Characteristic	Symbol	Q1	Q2	Units		
Drain-Source Voltage	V _{DSS}	30	-30	V		
Gate-Source Voltage	V _{GSS}	±20	±20	V		
Continuous Droin Current (Note 6) \/ 10\/	Steady State	$T_{A} = +25^{\circ}C$ $T_{A} = +70^{\circ}C$	ID	5.5 4.1	-5.8 -4.3	А
Continuous Drain Current (Note 6) V _{GS} =10V	t<10s	$T_{A} = +25^{\circ}C$ $T_{A} = +70^{\circ}C$	ID	7.2 5.7	-7.6 -6.1	А
Maximum Body Diode Forward Current (Note 6)	Is	2.2	-2.2	А		
Pulsed Drain Current (10µs pulse, duty cycle = 1%)			I _{DM}	40	-30	А

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units		
Total Power Dissignation (Note 6)	T _A = +25°C	P	1.2	W	
Total Power Dissipation (Note 6)	T _A = +70°C	PD	0.75		
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	Р	108	°C/W	
memai Resistance, Junction to Ambient (Note 6)	t<10s R _{0JA}		65	0/10	
Total Power Dissipation (Note 7)	T _A = +25°C	P	1.5	W	
	T _A = +70°C	PD	0.95		
Thermal Resistance, Junction to Ambient (Note 7)	Steady State	D.	85	°C/W	
Thermal Resistance, Junction to Ambient (Note 7)	t<10s	R _{0JA}	50		
Thermal Resistance, Junction to Case (Note 7)		Rejc	14.5		
Operating and Storage Temperature Range		T _{J,} T _{STG}	-55 to +150	°C	

Electrical Characteristics – Q1 (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 8)	Cymbol	WIIII	тур	Max	Onit	Test condition	
Drain-Source Breakdown Voltage	BV _{DSS}	30	_		V	$V_{GS} = 0V, I_{D} = 250 \mu A$	
Zero Gate Voltage Drain Current	I _{DSS}		_	1	μA	$V_{DS} = 24V, V_{GS} = 0V$	
Gate-Source Leakage	Igss	_	_	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 8)							
Gate Threshold Voltage	V _{GS(th)}	1	_	3	V	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	
Statia Dusia Course On Desistance			19	27		$V_{GS} = 10V, I_D = 6A$	
Static Drain-Source On-Resistance	R _{DS (ON)}		22	35	mΩ	V _{GS} = 4.5V, I _D = 5A	
Diode Forward Voltage	V _{SD}		0.7	1.2	V	$V_{GS} = 0V, I_{S} = 1.3A$	
DYNAMIC CHARACTERISTICS (Note 9)			•	•	•		
Input Capacitance	Ciss	_	641	_			
Output Capacitance	Coss		66	_	pF	V _{DS} = 15V, V _{GS} = 0V f = 1.0MHz	
Reverse Transfer Capacitance	C _{rss}		51	_			
Gate Resistance	R _G	_	2.2	_	Ω V _{DS} = 0V, V _{GS} = 0V, f = 1.0MHz		
Total Gate Charge (V _{GS} = 4.5V)	Qq		6	_			
Total Gate Charge (V _{GS} = 10V)	Q _q		13.2	_		V _{DS} = 15V, I _D = 10A	
Gate-Source Charge	Q _{gs}		1.7	_	nC		
Gate-Drain Charge	Q _{gd}		2.2				
Turn-On Delay Time	t _{D(on)}		3.3	_			
Turn-On Rise Time	tr		4.4	_	nS	$V_{GS} = 10V, V_{DD} = 15V, R_G = 6\Omega,$	
Turn-Off Delay Time	t _{D(off)}		22.3	_	105	$I_D = 1A$	
Turn-Off Fall Time	tf		5.3	—]		



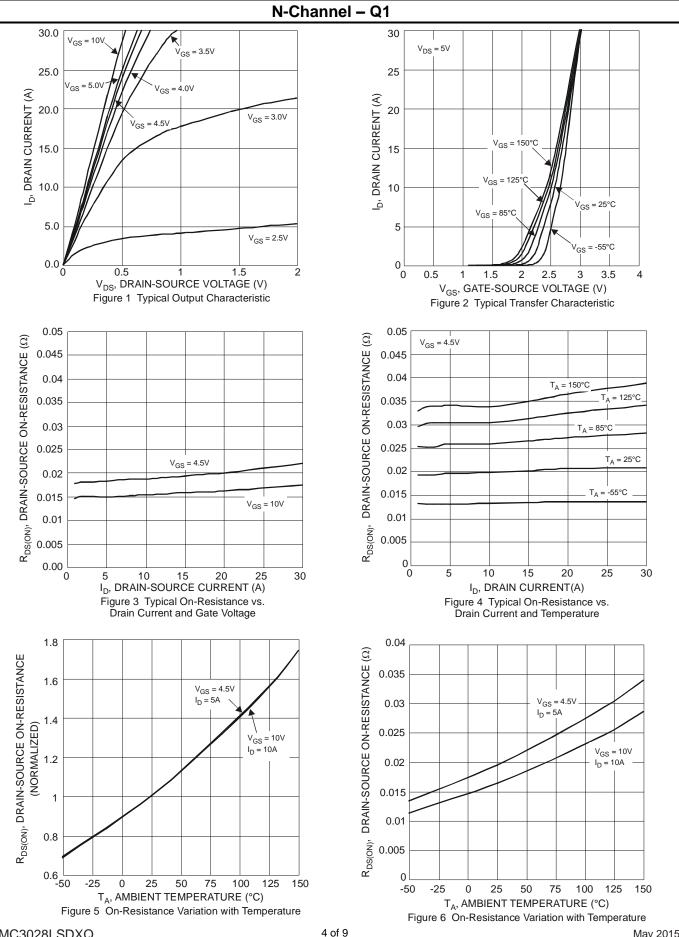
Electrical Characteristics – Q2 (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 8)							
Drain-Source Breakdown Voltage	BV _{DSS}	-30	_	_	V	$V_{GS} = 0V, I_D = -250\mu A$	
Zero Gate Voltage Drain Current	I _{DSS}	_	_	-1	μA	$V_{DS} = -24V, V_{GS} = 0V$	
Gate-Source Leakage	Igss	_	_	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 8)							
Gate Threshold Voltage	V _{GS(th)}	-1	_	-3	V	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	
Static Drain-Source On-Resistance			21	25	mΩ	$V_{GS} = -10V, I_D = -6A$	
Static Drain-Source On-Resistance	R _{DS (ON)}	_	29	41	11122	$V_{GS} = -4.5V, I_D = -5A$	
Diode Forward Voltage	V _{SD}	_	-0.7	-1.2	V	$V_{GS} = 0V, I_{S} = -1.3A$	
DYNAMIC CHARACTERISTICS (Note 9)							
Input Capacitance	C _{iss}	_	1241	_		V _{DS} = -15V, V _{GS} = 0V f = 1.0MHz	
Output Capacitance	C _{oss}	_	146	_	pF		
Reverse Transfer Capacitance	Crss	_	110	_			
Gate Resistance	R _G	_	14.8	_	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1.0MHz$	
Total Gate Charge (V _{GS} = -4.5V)	Qg	_	10.9	_			
Total Gate Charge (V _{GS} = -10V)	Qg	_	22	_	nC	V _{DS} = -15V, I _D = -7A	
Gate-Source Charge	Q _{gs}	_	3.5	_	IIC IIC		
Gate-Drain Charge	Q _{gd}		4.7	_			
Turn-On Delay Time	t _{D(on)}		9.7	_			
Turn-On Rise Time	tr		17.1		nS	$V_{GS} = -10V, V_{DD} = -15V, R_{GEN} = 6\Omega,$	
Turn-Off Delay Time	t _{D(off)}	_	60.5	_	105	I _D = -7A	
Turn-Off Fall Time	t _f		40.4	—	1		

6. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
7. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
8. Short duration pulse test used to minimize self-heating effect.
9. Guaranteed by design. Not subject to product testing. Notes:



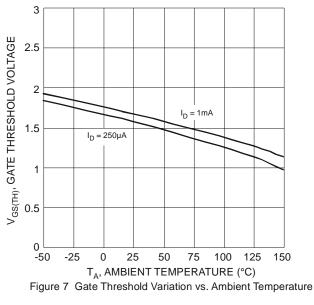
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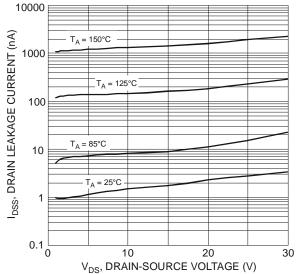


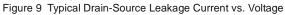
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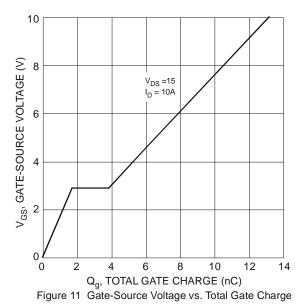
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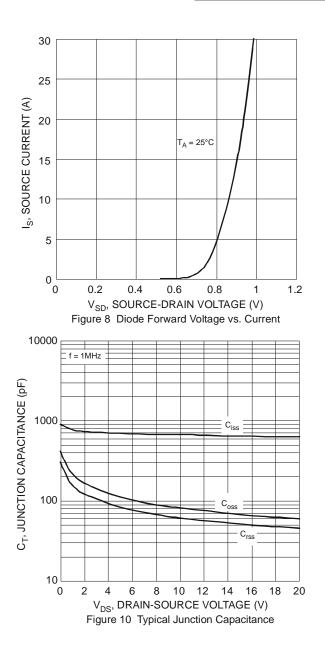






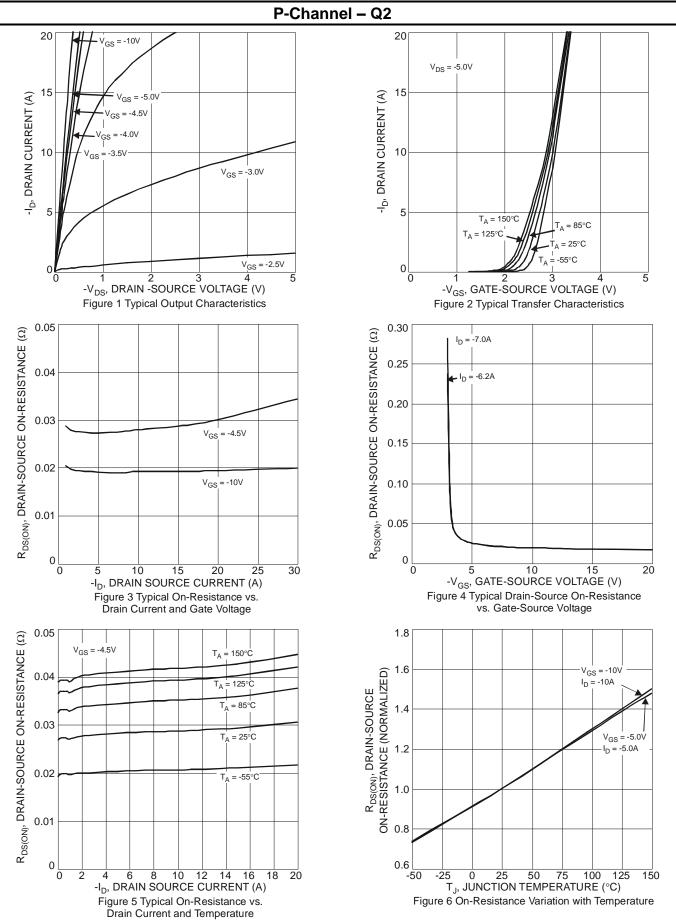








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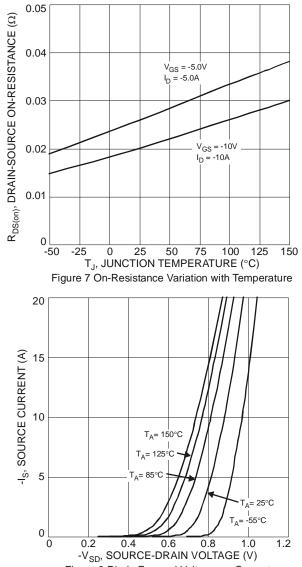
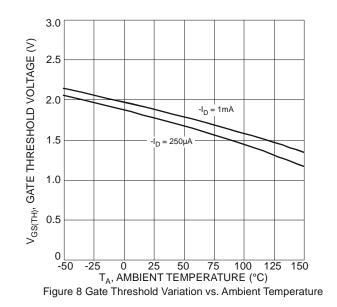


Figure 9 Diode Forward Voltage vs. Current

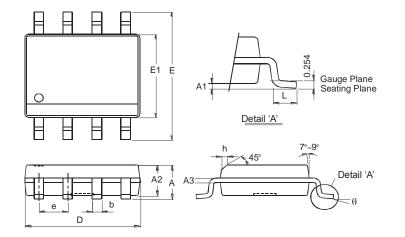


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Package Outline Dimensions

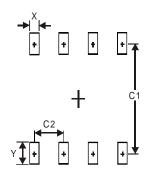
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.



SO-8						
Dim	Min Max					
Α	-	1.75				
A1	0.10	0.20				
A2	1.30	1.50				
A3	0.15	0.25				
b	0.3	0.5				
D	4.85	4.95				
Е	5.90	6.10				
E1	3.85	3.95				
е	1.27 Тур					
h	- 0.35					
L	0.62	0.82				
Θ	0° 8°					
All Dimensions in mm						

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
Х	0.60
Y	1.55
C1	5.4
C2	1.27



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