



SURFACE MOUNT FAST SWITCHING DIODE ARRAY

Features

- Fast Switching Speed
- Low Forward Voltage: Maximum of 0.72V at 5mA
- Low Reverse Current: Maximum of 100nA at 70V
- Fast Reverse Recovery: Maximum of 4ns
- Low Capacitance: Maximum of 3.5pF
- Small Surface Mount Package
- For General Purpose Switching Applications

SOT-363

TOP VIEW

- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

Mechanical Data

- Case: SOT353 or SOT363
 - Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Matte Tin Finish annealed over Alloy 42 leadframe (Lead Free Plating). Solderable per MIL-STD-202, Method 208 3
- Orientation: See Diagrams Below
- Weight: 0.006 grams (approximate)

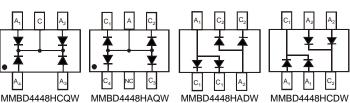
SOT353/SOT363

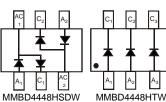


SOT-353

TOP VIEW







Ordering Information (Note 4)

Devel Neverlage	Qualification	0	Destanting
Part Number	Qualification	Case	Packaging
MMBD4448HADW-7-F	Commercial	SOT363	3000/Tape & Reel
MMBD4448HADWQ-7-F	Automotive	SOT363	3000/Tape & Reel
MMBD4448HAQW-7-F	Commercial	SOT363	3000/Tape & Reel
MMBD4448HCDW-7-F	Commercial	SOT363	3000/Tape & Reel
MMBD4448HCQW-7-F	Commercial	SOT353	3000/Tape & Reel
MMBD4448HSDW-7-F	Commercial	SOT363	3000/Tape & Reel
MMBD4448HTW-7-F	Commercial	SOT363	3000/Tape & Reel

Notes:

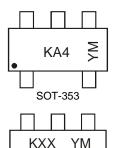
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

2. See http://www.diodes.com for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at http://www.diodes.com.

Marking Information

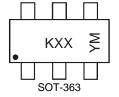


SOT-363

XXX

KA4 = Product Type Marking Code, KA4 = MMBD4448HCQW YM = Date Code Marking Y = Year (ex: Z = 2012) M = Month (ex: 9 = September)

KXX = Product Type Marking Code, ex. KA6 = MMBD4448HADW KA7 = MMBD4448HCDW KAB = MMBD4448HSDW YM = Date Code Marking Y = Year (ex: Z = 2012) M = Month (ex: 9 = September)



KXX = Product Type Marking Code, ex. KA5 = MMBD4448HAQW KAA = MMBD4448HTW YM = Date Code Marking Y = Year (ex: Z = 2012) M = Month (ex: 9 = September)

Date	Code	Key

MΥ

Year	2000	2001	2002	2003		2012	201	13 20 [.]	4	2015	2016	2017	2018	2019
Code	L	М	Ν	Р		Z	A	ι B		С	D	E	F	G
Month	Jan	Feb	Mar	Apr	M	ay	Jun	Jul	Au	ıg	Sep	Oct	Nov	Dec
Code	1	2	3	4	Ę	5	6	7	8	3	9	0	Ν	D

MMBD4448HCQW /AQW /ADW /CDW /SDW /TW

Document number: DS30153 Rev. 18 - 2

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Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Non-Repetitive Peak Reverse Voltage		V _{RM}	100	V
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage		V _{RRM} V _{RWM} V _R	80	V
RMS Reverse Voltage		V _{R(RMS)}	57	V
Forward Continuous Current (Note 5)		I _{FM}	500	mA
Non-Repetitive Peak Forward Surge Current	@ t = 1.0µs @ t = 1.0s	I _{FSM}	4.0 1.0	A

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 5)	PD	200	mW
Thermal Resistance Junction to Ambient Air (Note 5)	R _{0JA}	625	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-65 to +150	°C

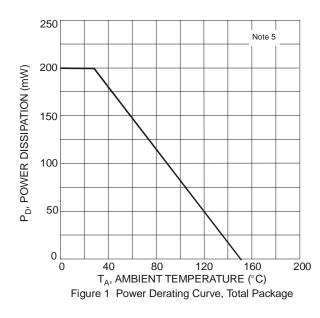
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

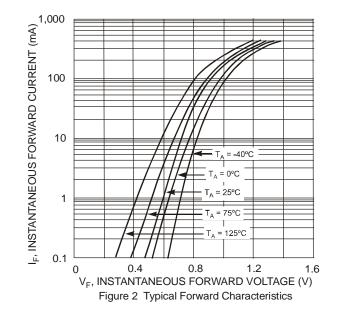
Characteristic	Symbol	Min	Max	Unit	Test Condition
Reverse Breakdown Voltage (Note 6)	V _{(BR)R}	80		V	I _R = 100μA
		0.62	0.72		I _F = 5.0mA
Forward Voltage	¥-	—	0.855	V	$I_F = 10 \text{mA}$
Forward voltage	VF		1.0	v	I _F = 100mA
		_	1.25		I _F = 150mA
			100	nA	V _R = 70V
Reverse Current (Note 6)		I _R —	50	μA	V _R = 75V, T _J = +150°C
Reverse Current (Note 6)	IR		30	μA	V _R = 25V, T _J = +150°C
			25	nA	V _R = 20V
Total Capacitance	CT		3.5	pF	$V_{R} = 6V, f = 1.0MHz$
Reverse Recovery Time	t _{rr}	_	4.0	ns	$V_{R} = 6V, I_{F} = 5mA$

Notes:

5. Device mounted on FR-4 PCB, 1 inch x 0.85 inch x 0.062 inch; pad layout as shown on Diodes Inc. suggested pad layout document AP02001, which can be found on our website at http://www.diodes.com.

6. Short duration pulse test used to minimize self-heating effect.

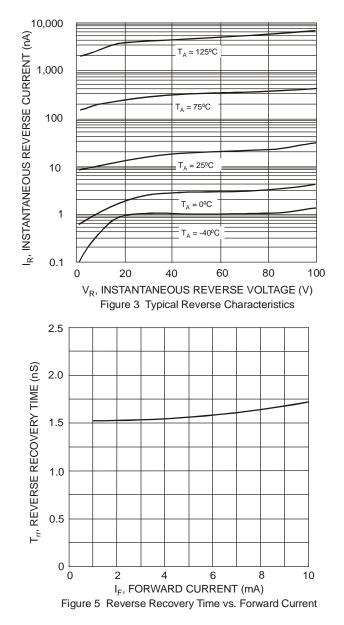




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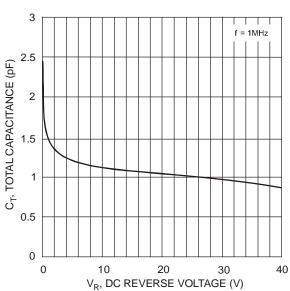
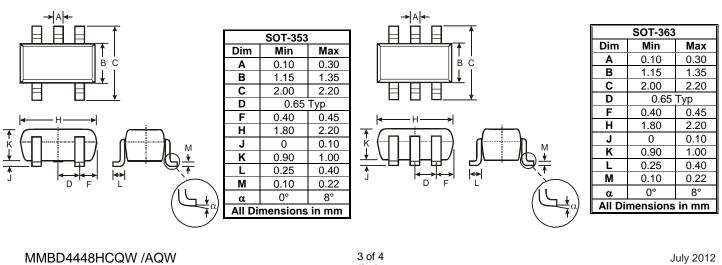


Figure 4 Total Capacitance vs. Reverse Voltage

Package Outline Dimensions

/ADW /CDW /SDW /TW Document number: DS30153 Rev. 18 - 2



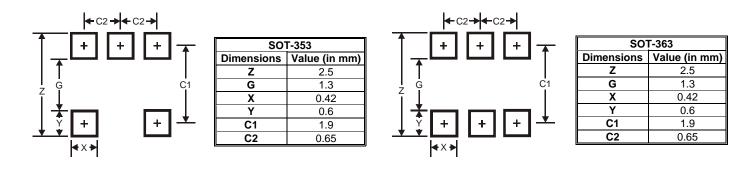
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Suggested Pad Layout



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