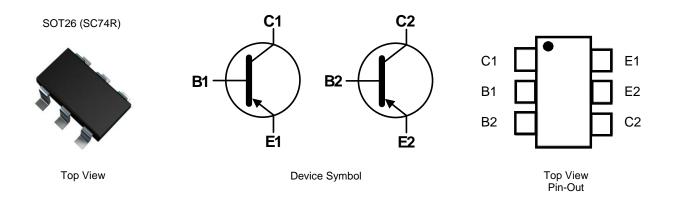


### **Features**

- BV<sub>CEO</sub> > -150V
- I<sub>C</sub> = -200mA High Collector Current
- Pair of PNP Transistors that are Intrinsically Matched (Note 1)
- 2% Matched Tolerance, hFE, VCE(SAT), VBE(SAT)
- Ideal for Medium Power Amplification and Switching
- Fully Internally Isolated in a Small Surface Mount Package
- **Epitaxial Planar Die Construction**
- Totally Lead-Free & Fully RoHS Compliant (Notes 2 & 3)
- Halogen and Antimony Free. "Green" Device (Note 4)
- Qualified to AEC-Q101 for High Reliability

## **Mechanical Data**

- Case: SOT26 (SC74R)
- Case Material: Molded Plastic. "Green" Molding Compound. UL Flammability Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Plated Leads. Solderable per MIL-STD-202. Method 208 @3
- Weight: 0.018 grams (Approximate)



## Ordering Information (Note 5)

Part Number	Compliance	Marking	Reel Size (inches)	Tape Width (mm)	Quantity Per Reel
DMMT5401-7-F	AEC-Q101	K4S	7	8	3,000

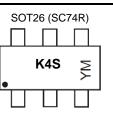
Intrinsically matched pair as this is built with adjacent die from the same wafer.
No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.

3. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

4. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

5. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

## Marking Information



K4S = Part Marking (See Ordering Information) YM = Date Code Marking Y = Year (ex: F = 2018)

M = Month (ex: 9 = September)

Date	Code	Key
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Notes:

Duie Coue Rey												
Year	2017		2018	2	2019	202	20	2021		2022	2	2023
Code	E		F		G	Н				J		K
Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



# Maximum Ratings (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	V <sub>CBO</sub>	-160	V
Collector-Emitter Voltage	V <sub>CEO</sub>	-150	V
Emitter-Base Voltage	V <sub>EBO</sub>	-5	V
Collector Current	lc	-200	mA

# Thermal Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Power Dissipation Total Device	(Notes 6 & 7)	PD	300	mW
Thermal Resistance, Junction to Ambient	(Note 6)	$R_{ heta JA}$	417	°C/W
Operating and Storage Temperature Range		TJ, T <sub>STG</sub>	-55 to +150	°C

# Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 8)	Symbol	IVIIII	тур	WIAN	Onit	
Collector-Base Breakdown Voltage	BV <sub>CBO</sub>	-160			V	I <sub>C</sub> = -100μA, I <sub>E</sub> = 0
Collector-Emitter Breakdown Voltage	BV <sub>CEO</sub>	-150			V	$I_{\rm C} = -1 {\rm mA},  I_{\rm B} = 0$
Emitter-Base Breakdown Voltage	BV <sub>EBO</sub>	-5		_	V	$I_{\rm E} = -10\mu A, I_{\rm C} = 0$
Collector-Base Cutoff Current	I <sub>CBO</sub>	_		-50	nA µA	$V_{CB} = -120V, I_E = 0$ $V_{CB} = -120V, I_E = 0, T_A = +100^{\circ}C$
Emitter-Base Cutoff Current	I <sub>EBO</sub>			-50	nA	$V_{EB} = -3V, I_{B} = 0$
ON CHARACTERISTICS (Note 8)						
DC Current Gain (Note 9)	h <sub>FE</sub>	50 60 50	_	240		Ic = -1mA, V <sub>CE</sub> = -5V Ic = -10mA, V <sub>CE</sub> = -5V Ic = -50mA, V <sub>CE</sub> = -5V
Collector-Emitter Saturation Voltage	V <sub>CE(SAT)</sub>	_		-0.2 -0.5	V	I <sub>C</sub> = -10mA, I <sub>B</sub> = -1mA I <sub>C</sub> = -50mA, I <sub>B</sub> = -5mA
Base-Emitter Saturation Voltage	V <sub>BE(SAT)</sub>	_	_	-1	V	I <sub>C</sub> = -10mA, I <sub>B</sub> = -1mA I <sub>C</sub> = -50mA, I <sub>B</sub> = -5mA
SMALL SIGNAL CHARACTERISTICS						
Current Gain-Bandwidth Product	f <sub>T</sub>	100		300	MHz	$V_{CE} = -10V, I_{C} = -10mA, f = 100MHz$
Output Capacitance	C <sub>OBO</sub>			6	pF	$V_{CB} = -10V, f = 1.0MHz, I_E = 0mA$
Small Signal Current Gain	h <sub>fe</sub>	40		260		$V_{CE} = -10V, I_{C} = -1mA, f = 1.0kHz$
Noise Figure	NF		_	8	dB	$\label{eq:Vce} \begin{split} V_{CE} &= -5V, \ I_C = -200 \mu A, \ R_S = 10\Omega, \\ f &= 1.0 kHz \end{split}$

6. For a device mounted on minimum recommended pad layout with 1oz copper that is on a single-sided 1.6mm FR-4 PCB; the device is measured under still air conditions whilst operating in a steady-state.

7. Maximum combined dissipation.

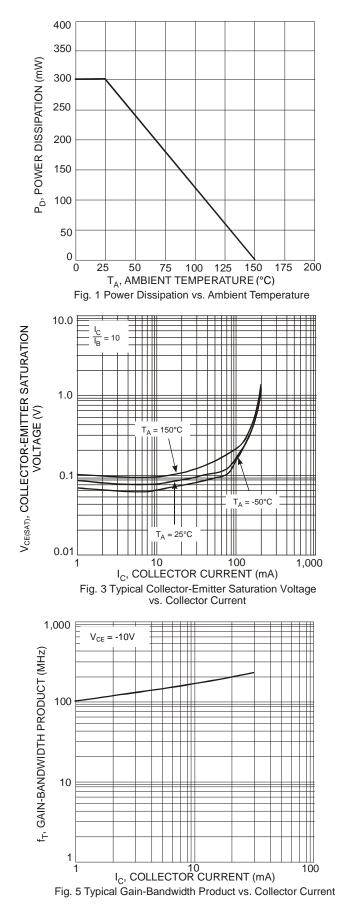
Notes:

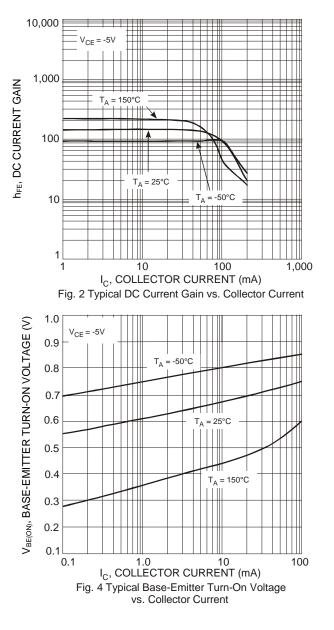
8. Short duration pulse test used to minimize self-heating effect.

The DC Current Gain, h<sub>FE</sub>, (matched at I<sub>C</sub> = -10mA and V<sub>CE</sub> = -5V) Collector Emitter Saturation Voltage, V<sub>CE(SAT)</sub>, and Base Emitter Saturation Voltage, V<sub>BE(SAT)</sub> are matched with typical matched tolerances of 1% and maximum of 2%.







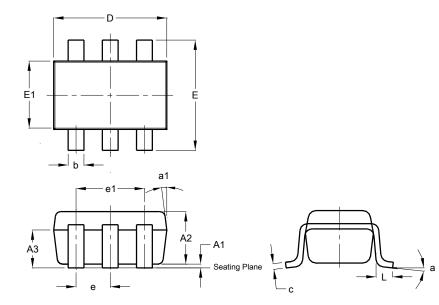




## **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT26 (SC74R)

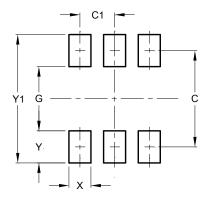


	SOT26 (SC74R)						
Dim	Min	Max	Тур				
A1	0.013	0.10	0.05				
A2	1.00	1.30	1.10				
A3	0.70	0.80	0.75				
b	0.35	0.50	0.38				
С	0.10	0.20	0.15				
D	2.90	3.10	3.00				
е	-	-	0.95				
e1	-	-	1.90				
Е	2.70	3.00	2.80				
E1	1.50	1.70	1.60				
L	0.35	0.55	0.40				
а	-	-	8°				
a1	-	-	7°				
All	Dimen	sions	in mm				

# Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT26 (SC74R)



Dimensions	Value (in mm)
С	2.40
C1	0.95
G	1.60
Х	0.55
Y	0.80
Y1	3.20



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