



## **Product Summary**

| V <sub>(BR)DSS</sub> | Max R <sub>DS(on)</sub>     | Max I <sub>D</sub><br>T <sub>A</sub> = 25°C<br>(Note 5) |
|----------------------|-----------------------------|---|
| 400)/                | $250m\Omega @ V_{GS} = 10V$ | 1.9A  |
| 100V                 | $300m\Omega @ V_{GS} = 6V$  | 1.68A   |

## **Description and Applications**

This MOSFET features a unique structure, combining the benefits of low on-resistance and fast switching, making it ideal for highefficiency, power management applications.

- DC DC Converters
- Power Management Functions
- Disconnect Switches
- Motor Control

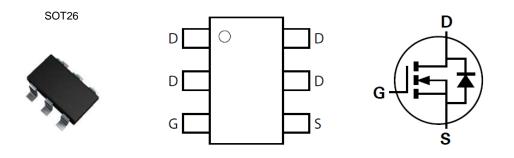
### **100V N-CHANNEL ENHANCEMENT MODE MOSFET**

### **Features and Benefits**

- Low On-Resistance
- Fast Switching Speed
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

### **Mechanical Data**

- Case: SOT26
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208(2)
- Weight: 0.015 grams (Approximate)



Top View

Pinout Top-view

Device symbol

# Ordering Information (Note 4)

| Part Number   | Reel Size (inch) | Tape Width (mm) | Quantity Per Reel |
|---------------|------------------|-----------------|-------------------|
| ZXMN10A08E6TA | 7                | 8               | 3000              |
| ZXMN10A08E6TC | 13               | 8               | 10,000            |

Notes:

No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
See http://www.diodes.com/quality/lead\_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green"

and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at http://www.diodes.com.

### **Marking Information**

| Γ | 7 |    | 26       |          | ] |
|---|---|----|----------|----------|---|
| ٠ | 1 | 0A | 8        | NN/      |   |
| _ |   | _  | <b>—</b> | <b>—</b> |   |

10A8 = Product Type Marking Code YM = Date Code Marking Y or  $\overline{Y}$  = Year (ex: C = 2015) M or  $\overline{M}$  = Month (ex: 9 = September)

### Date Code Kev

| Balo boao | Roy  |     |     |      |      |      |      |     |      |     |      |      |      |
|-----------|------|-----|-----|------|------|------|------|-----|------|-----|------|------|------|
| Year      | 2015 | 2   | 016 | 2017 | 2018 | 2019 | 2020 | 202 | 1 20 | 22  | 2023 | 2024 | 2025 |
| Code      | С    |     | D   | Е    | F    | G    | Н    |     |      | J   | K    | L    | М    |
| Mont      | h    | Jan | Feb | Mar  | Apr  | Мау  | Jun  | Jul | Aug  | Sep | Oct  | Nov  | Dec  |
| Code      | •    | 1   | 2   | 3    | 4    | 5    | 6    | 7   | 8    | 9   | 0    | N    | D    |



### Maximum Ratings (@T<sub>A</sub> = +25°C, unless otherwise specified.)

|  | Characteristic |                                | Symbol           | Value | Unit |
|--|----------------|--------------------------------|------------------|-------|------|
| Drain-Source Voltage                   |                |                                | V <sub>DSS</sub> | 100   | V    |
| Gate-Source Voltage                    |                |                                | V <sub>GS</sub>  | ±20   | V    |
| Continuous Drain Current               |                | Note 5)                        | - I <sub>D</sub> | 1.9   |      |
|  | $V_{GS} = 10V$ | T <sub>A</sub> =+70°C (Note 5) |                  | 1.5   | ٨    |
|  |                | (Note 4)                       |                  | 1.5   | A    |
|  |                | (Note 7)                       |                  | 3.5   |      |
| Pulsed Drain Current                   |                | (Note 6)                       | I <sub>DM</sub>  | 8.6   | А    |
| Continuous Source Current (Body Diode) |                | (Note 5)                       | Is               | 2.5   | А    |
| Pulsed Source Current (Bod             | y Diode)       | (Note 6)                       | Ism              | 8.6   | А    |

### Thermal Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

| Characteristic                                     |          | Symbol               | Value       | Unit           |
|--|----------|----------------------|-------------|----------------|
|  | (Note 4) |                      | 1.1         |                |
| Power Dissipation                                  | (Note 5) | PD                   | 1.7         | W              |
|  | (Note 7) |                      | 6.3         |                |
| The model Designation and the string to Analytical | (Note 4) | _                    | 114         | ~ <b>O</b> .M/ |
| Thermal Resistance, Junction to Ambient            | (Note 5) | R <sub>0JA</sub>     | 73.5        | °C/W           |
| Thermal Resistance, Junction to Leads              | (Note 7) | R <sub>θJL</sub>     | 19.7        | °C/W           |
| Operating and Storage Temperature Range            |          | TJ, T <sub>STG</sub> | -55 to +150 | ٥°             |

Notes:

4. For a device surface mounted on 25mm x 25mm FR4 PCB with high coverage of single sided 1oz copper, in still air conditions. 5. For a device surface mounted on FR4 PCB measured at t ≤ 5 sec.

6. Repetitive rating 25mm x 25mm FR4 PCB, D = 0.02, pulse width 300µs - pulse width limited by maximum junction temperature.

7. Thermal resistance from junction to solder-point (at the end of the drain lead).

#### 10 1.2 Max Power Dissipation (W) R 1.0 Drain Current (A) Limited 0.8 0.6 100ms 0.4 10ms 1ms 0.2 Single Pulse \_º10m T<sub>amb</sub>=25°C 100µs 0.0 V<sub>DS</sub> Drain-Source Voltage (V) 80 100 120 140 160 20 40 60 100m 0 Temperature (°C) Safe Operating Area **Derating Curve** 120 =25°C Single Pulse 100 Thermal Resistance (°C/W) 100 T\_\_\_=25°C Maximum Power (W) 80 D=0.5 60 10 40 Single Pulse D=0.2 20 D=0.05 D=0.1 100µ 1m 10m 100m 10 100 1k 100µ 1m 10m 100m 10 100 1k 1 1 Pulse Width (s) Pulse Width (s) **Transient Thermal Impedance Pulse Power Dissipation**

### **Thermal Characteristics**



### Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

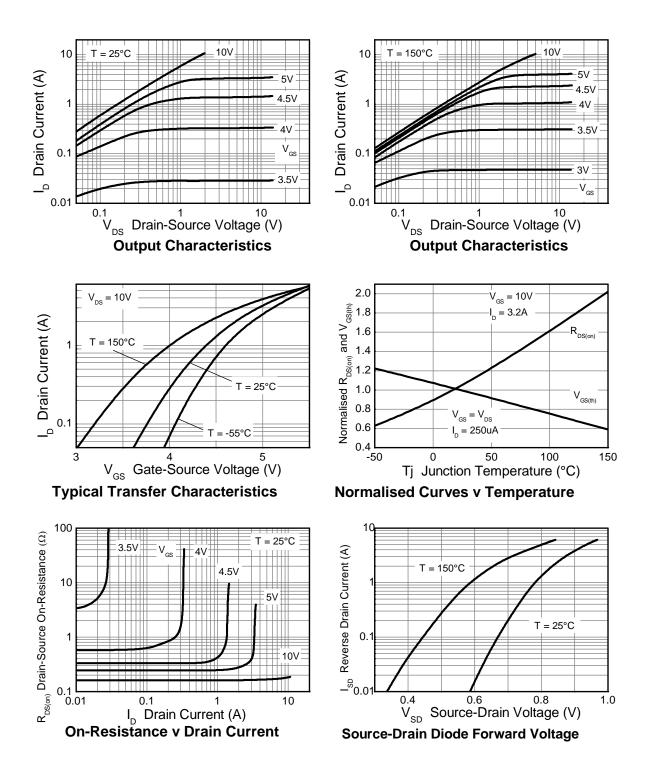
| Characteristic                             | Symbol               | Min | Тур  | Max  | Unit | Test Condition  |  |
|--|----------------------|-----|------|------|------|---|--|
| OFF CHARACTERISTICS                        |                      |     | •    | r    |      |   |  |
| Drain-Source Breakdown Voltage             | BV <sub>DSS</sub>    | 100 | —    | —    | V    | $I_D = 250 \mu A, V_{GS} = 0 V$                         |  |
| Zero Gate Voltage Drain Current            | I <sub>DSS</sub>     | _   | —    | 0.5  | μΑ   | $V_{DS} = 100V, V_{GS} = 0V$                            |  |
| Gate-Source Leakage                        | IGSS                 | _   | _    | 100  | nA   | $V_{GS} = \pm 20V, V_{DS} = 0V$                         |  |
| ON CHARACTERISTICS                         |                      |     |      |      |      |   |  |
| Gate Threshold Voltage                     | V <sub>GS(th)</sub>  | 2.0 | _    | 4.0  | V    | $I_{D} = 250 \mu A, V_{DS} = V_{GS}$                    |  |
| Statia Drain Source On Desistance (Nate 9) |                      |     |      | 0.25 | Ω    | $V_{GS} = 10V, I_D = 3.2A$                              |  |
| Static Drain-Source On-Resistance (Note 8) | R <sub>DS (ON)</sub> | _   | _    | 0.30 | Ω    | $V_{GS} = 6V, I_D = 2.6A$                               |  |
| Forward Transconductance (Notes 8 & 10)    |                      | _   | 5.0  | _    | S    | V <sub>DS</sub> = 15V, I <sub>D</sub> = 3.2A            |  |
| Diode Forward Voltage (Note 8)             | V <sub>SD</sub>      | _   | 0.87 | 0.95 | V    | I <sub>S</sub> = 3.2A, V <sub>GS</sub> = 0V             |  |
| Reverse Recovery Time (Note 10)            | trr                  |     | 27   | _    | ns   |   |  |
| Reverse Recovery Charge (Note 10)          | Qrr                  | _   | 32   | _    | nC   | I <sub>S</sub> = 1.2A, di/dt = 100A/μs                  |  |
| DYNAMIC CHARACTERISTICS (Note 10)          |                      |     |      |      |      |   |  |
| Input Capacitance                          | Ciss                 | _   | 405  | _    | pF   |   |  |
| Output Capacitance                         | C <sub>oss</sub>     | _   | 28.2 | _    | pF   | V <sub>DS</sub> = 50V, V <sub>GS</sub> = 0V<br>f = 1MHz |  |
| Reverse Transfer Capacitance               | Crss                 | _   | 14.2 | _    | pF   |   |  |
| Gate Charge (Note 9)                       | Qg                   | _   | 4.2  | _    | nC   | $V_{GS} = 5V, V_{DS} = 50V$<br>$I_D = 1.2A$             |  |
| Total Gate Charge (Note 9)                 | Qq                   |     | 7.7  | —    | nC   |   |  |
| Gate-Source Charge (Note 9)                | Q <sub>gs</sub>      |     | 1.8  |      | nC   | $V_{GS} = 10V, V_{DS} = 50V$                            |  |
| Gate-Drain Charge (Note 9)                 | Q <sub>gd</sub>      |     | 2.1  | _    | nC   | $I_{\rm D} = 1.2 {\rm A}$                               |  |
| Turn-On Delay Time (Note 9)                | t <sub>d(on)</sub>   | _   | 3.4  | _    | ns   |   |  |
| Turn-On Rise Time (Note 9)                 | tr                   | _   | 2.2  | _    | ns   | $V_{DD} = 30V, V_{GS} = 10V$                            |  |
| Turn-Off Delay Time (Note 9)               | t <sub>d(off)</sub>  | _   | 8    | _    | ns   | $I_D = 1.2A, R_G \cong 6.0\Omega$                       |  |
| Turn-Off Fall Time (Note 9)                | tf                   | _   | 3.2  |      | ns   | 1   |  |

Notes:

8. Measured under pulsed conditions. Width ≤300µs. Duty cycle ≤2%.
9. Switching characteristics are independent of operating junction temperature.
10. For design aid only, not subject to production testing.

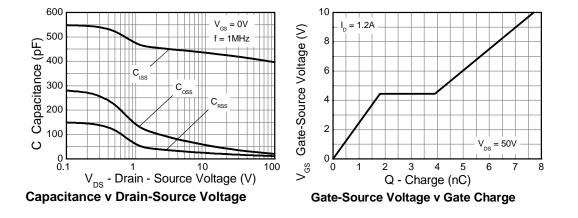


# **Typical Characteristics**

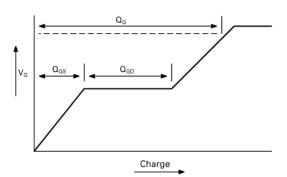




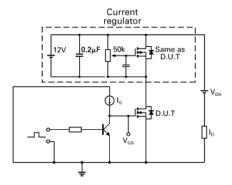
# Typical Characteristics (continued)



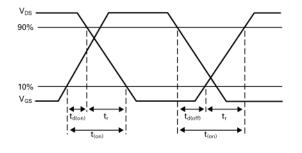
# **Test Circuits**



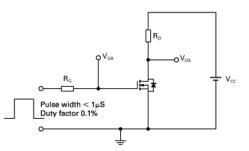
Basic gate charge waveform



Gate charge test circuit



Switching time waveforms

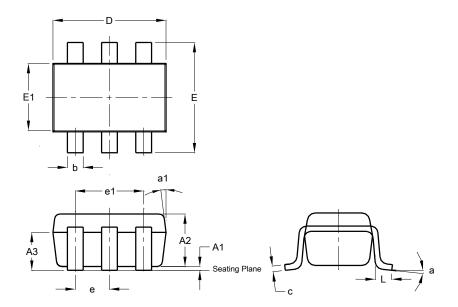


Switching time test circuit



# **Package Outline Dimensions**

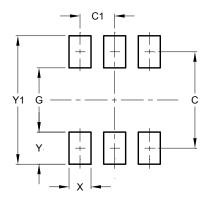
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.



| SOT26 |       |         |       |  |  |  |  |
|-------|-------|---------|-------|--|--|--|--|
| Dim   | Min   | Max     | Тур   |  |  |  |  |
| A1    | 0.013 | 0.10    | 0.05  |  |  |  |  |
| A2    | 1.00  | 1.30    | 1.10  |  |  |  |  |
| A3    | 0.70  | 0.80    | 0.75  |  |  |  |  |
| b     | 0.35  | 0.50    | 0.38  |  |  |  |  |
| С     | 0.10  | 0.20    | 0.15  |  |  |  |  |
| D     | 2.90  | 3.10    | 3.00  |  |  |  |  |
| е     | -     | -       | 0.95  |  |  |  |  |
| e1    | -     | -       | 1.90  |  |  |  |  |
| E     | 2.70  | 3.00    | 2.80  |  |  |  |  |
| E1    | 1.50  | 1.70    | 1.60  |  |  |  |  |
| L     | 0.35  | 0.55    | 0.40  |  |  |  |  |
| а     | -     | -       | 8°    |  |  |  |  |
| a1    | -     | -       | 7°    |  |  |  |  |
| All   | Dimen | sions i | in mm |  |  |  |  |

# Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



| Dimensions | Value (in mm) |
|------------|---------------|
| С          | 2.40          |
| C1         | 0.95          |
| G          | 1.60          |
| Х          | 0.55          |
| Y          | 0.80          |
| Y1         | 3.20          |



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