



P-CHANNEL ENHANCEMENT MODE FIELD EFFECT TRANSISTOR

Features

- Low On-Resistance
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- ESD Protected Gate
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e.: parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please refer to the related automotive grade (Qsuffix) part. A listing can be found at https://www.diodes.com/products/automotive/automotive-products/.
- This part is qualified to JEDEC standards (as references in AEC-Q) for High Reliability.

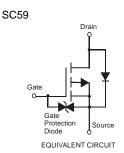
https://www.diodes.com/quality/product-definitions/

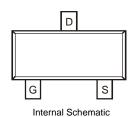
Mechanical Data

- Package: SC59
- Package Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 ³
- Weight: 0.014 grams (Approximate)









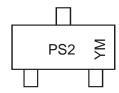
Ordering Information (Note 4)

Part Number	Package	Packing		
Fait Number	Fackage	Qty.	Carrier	
DMP3030SN-7	SC59	3000	Tape & Reel	

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See http://www.diodes.com/quality/lead_free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



PS2 = Product Type Marking Code YM = Date Code Marking Y or \overline{Y} = Year (ex: I = 2021) M = Month (ex: 9 = September)

Date Code Key

Year	2006		2021	2022	2023	2024	2025	2026	2027	2028	2029	2030
Code	Т		I	J	K	L	М	N	0	Р	R	S
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D

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Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Drain-Source Voltage	VDSS	-30	V
Gate-Source Voltage	Vgss	±20	V
Drain Current (Note 5) Steady State	lD	-0.7	Α
Pulsed Drain Current (Note 6)	I _{DM}	-2.8	А

Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 5)	PD	500	mW
Thermal Resistance, Junction to Ambient	R _θ JA	250	°C/W
Operating and Storage Temperature Range	TJ, TSTG	-65 to +150	°C

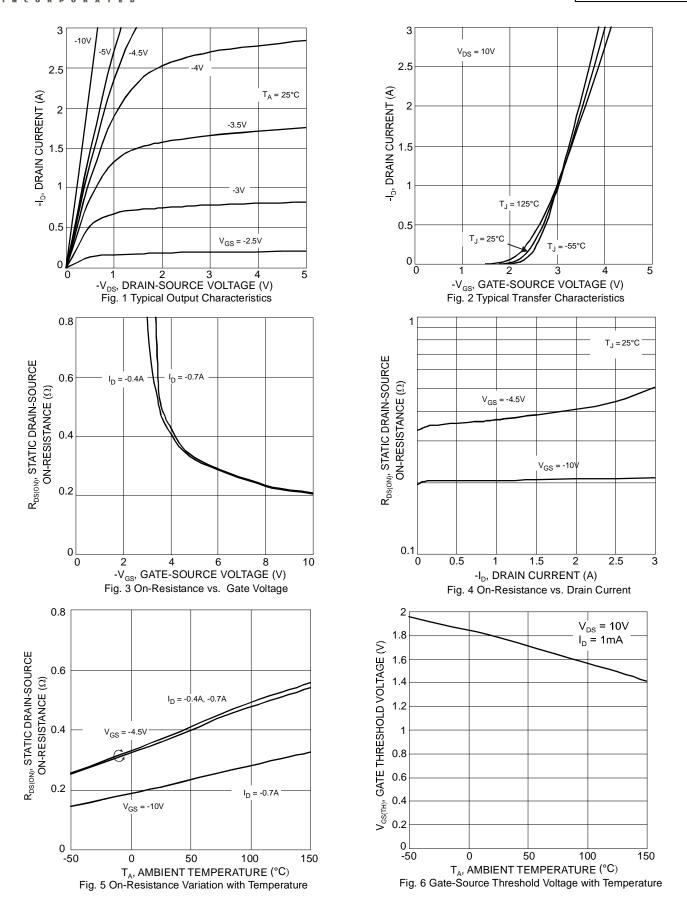
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BV _{DSS}	-30			V	$V_{GS} = 0V, I_D = -250\mu A$	
Zero Gate Voltage Drain Current	IDSS	_		-10	μΑ	$V_{DS} = -30V$, $V_{GS} = 0V$	
Gate-Body Leakage	Igss	_	_	±10	μΑ	$V_{GS} = \pm 20V$, $V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)	,						
Gate Threshold Voltage	V _{GS(TH)}	-1.0	_	-3.0	V	$V_{DS} = -10V, I_{D} = -1.0mA$	
Static Drain-Source On-Resistance	Dagger	_	0.20 0.35	0.25 0.45	Ω	$V_{GS} = -10V, I_{D} = -0.4A$	
Static Dialif-Source Off-Resistance	RDS(ON)					$V_{GS} = -4.5V$, $I_{D} = -0.4A$	
Forward Transfer Admittance	Y _F s	_	1	_	S	$V_{DS} = -10V, I_{D} = -0.4A$	
Diode Forward Voltage (Note 7)	VsD	_	-0.8	-1.1	V	$V_{GS} = 0V, I_{S} = -0.7A$	
DYNAMIC CHARACTERISTICS						•	
Input Capacitance	Ciss	_	160	_	рF	V _{DS} = -10V, V _{GS} = 0V f = 1.0MHz	
Output Capacitance	Coss	_	120	_	рF		
Reverse Transfer Capacitance	Crss	_	50	_	pF		
SWITCHING CHARACTERISTICS							
Turn-On Delay Time	tD(ON)	_	10	_	ns		
Turn-Off Delay Time	tD(OFF)	_	25	_	ns	$V_{DD} = -10V$, $I_{D} = -0.4A$, $V_{GS} = -5.0V$, $R_{GEN} = 50\Omega$	
Turn-On Rise Time	t _R		25	_	ns		
Turn-Off Fall Time	tF		40	_	ns		

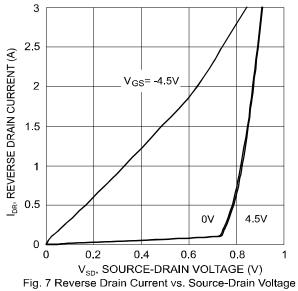
- Notes: 5. Device mounted on FR-4 PCB.
 - 6. Pulse width $\leq 10 \mu S$, Duty Cycle $\leq 1\%$.
 - 7. Short duration pulse test used to minimize self-heating effect.

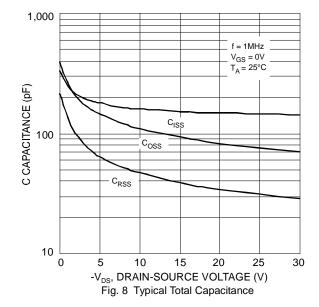
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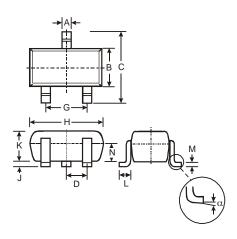




Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

SC59

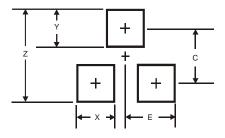


SC59							
Dim	Min	Max	Тур				
Α	0.35	0.50	0.38				
В	1.50	1.70	1.60				
С	2.70	3.00	2.80				
D	-	-	0.95				
G	-	-	1.90				
Н	2.90	3.10	3.00				
J	0.013	0.10	0.05				
K	1.00	1.30	1.10				
L	0.35	0.55	0.40				
М	0.10	0.20	0.15				
N	0.70	0.80	0.75				
а	0°	8°	-				
All Dimensions in mm							

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SC59



Dimensions	Value in mm
Z	3.4
X	0.8
Y	1.0
С	2.4
E	1.35



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