



8 CHANNEL LOW CAPACITANCE TVS DIODE ARRAY

Product Summary

V _{BR} (Min)	IPP (Max)	C _{I/O} (Typ)
5V	5.5A	0.6pF

Description

The DT1240A-08LP3810Q is a high-performance device suitable for protecting four high-speed I/Os. This device is assembled in U-DFN3810-9 (Type B) package and has high ESD surge capability and low capacitance.

Applications

Typically used at high-speed ports such as USB2.0, USB3.0, USB3.1, IEEE1394 (Firewire®, iLink), Serial ATA, DVI™, HDMI1.4™, HDMI2.0™ and PCI™.

Features

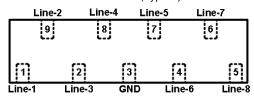
- Clamping Voltage: 8.2V at 10A 100ns, TLP; 7.5V at 5.5A (8μs/20μs)
- IEC 61000-4-2 (ESD): Air ±16kV, Contact ±14kV
- IEC 61000-4-5 (Lighting): 5.5A (8/20µs)
- 8 Channels of ESD Protection
- Low Channel Input Capacitance of 0.6pF Typical
- TLP Dynamic Resistance: 0.25Ω
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- The DT1240A-08LP3810Q is suitable for automotive applications requiring specific change control; this part is AEC-Q101 qualified, PPAP capable, and manufactured in IATF 16949 certified facilities.

https://www.diodes.com/quality/product-definitions/

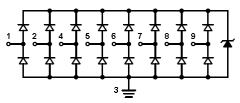
Mechanical Data

- Case: U-DFN3810-9
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Schematic
- Terminals: Finish NiPdAu, Solderable per MIL-STD-202, Method 208 (e4)
- Weight: 0.005 grams (Approximate)

U-DFN3810-9 (Type B)



Pin Description (Top View)



Device Schematic

Ordering Information (Note 4)

DT1240A-08LP3810Q-7 Automotive MW4 7 8 5,000/Tape & Re	Part Number	Compliance	Marking	Reel Size (inches)	Tape Width (mm)	Quantity
	11 11 12 10 10 10 10 10 10 10 10 10 10 10 10 10	Automotive	MW4	7	8	5,000/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/

Marking Information

MW4 YM

MW4 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: I = 2021) M = Month (ex: 9 = September)

Date Code Key

Date Code Ney												
Year	2019	2020	2021	2022	2023	2024	2025	2026	2027	2028	2029	2030
Code	G	Н		J	K	L	М	N	0	Р	R	S
		1	1	1	1				1	1	I	I
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec

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Maximum Ratings (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	Conditions
Peak Pulse Current, per IEC 61000-4-5	I _{PP}	5.5	Α	I/O to V _{SS} , 8/20µs
Peak Pulse Power, per IEC 61000-4-5	P _{PP}	55	W	I/O to Vss, 8/20µs
ESD Protection – Contact Discharge, per IEC 61000-4-2	Vesd_contact	±14	kV	I/O to Vss
ESD Protection – Air Discharge, per IEC 61000-4-2	Vesd_air	±16	kV	I/O to Vss
Operating Temperature	Top	-55 to +85	°C	_
Storage Temperature	T _{STG}	-55 to +150	°C	_

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation Typical (Note 5)	PD	350	mW
Thermal Resistance, Junction to Ambient Typical (Note 5)	Reja	360	°C/W

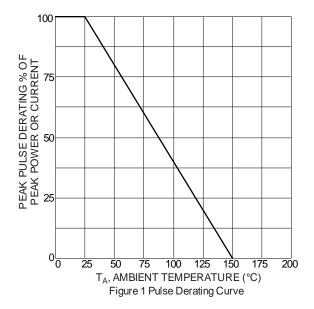
Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

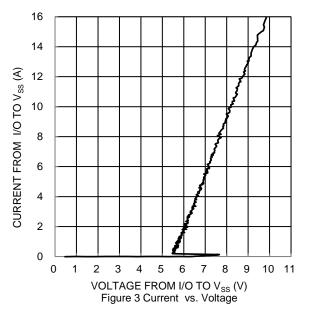
Characteristic	Symbol	Min	Тур	Max	Unit	Test Conditions
Reverse Working Voltage	V _{RWM}	_	_	3.3	V	_
Reverse Current	I _R	_	_	0.5	μA	$V_R = 3.3V$, I/O to V_{SS}
Reverse Breakdown Voltage	V _{BR}	5	_	_	V	I _R = 1mA, I/O to V _{SS}
Forward Clamping Voltage	VF	-1.0	-0.85	_	V	IF = -15mA, I/O to Vss
Reverse Clamping Voltage (Note 6)	Vc	_	7.5	10	V	$I_{PP} = 5.5A$, I/O to V_{SS} , 8/20 μ s
ESD Clamping Voltage	V _{ESD}	_	8.2	_	V	TLP, 10A, $t_P = 100$ ns, I/O to V_{SS}
Dynamic Reverse Resistance	R _{DIF-R}	_	0.25	_	Ω	TLP, 10A, t _P = 100ns, I/O to V _{SS}
Dynamic Forward Resistance	R _{DIF-F}	_	0.25	_	Ω	TLP, 10A, tp = 100ns, Vss to I/O
Channel Input Capacitance	Cı/o	_	0.6	0.7	pF	V _{I/O} = 1.65V, V _{SS} = 0V, f = 1MHz
Delta C _{I/O}	CI/OMAX-CI/OMIN	_	0.04	_	pF	CI/OMAX-CI/OMIN

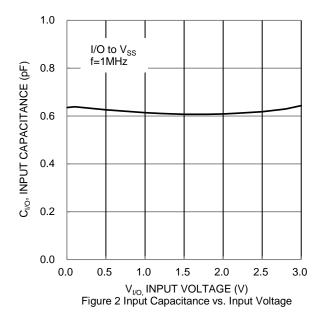
Notes:

- 5. Device mounted on FR-4 PCB pad layout (2oz copper) as shown on Diodes Incorporated's website at http://www.diodes.com/package-outlines.html.
- 6. Clamping voltage value is based on an 8x20µs peak pulse current (Ipp) waveform.







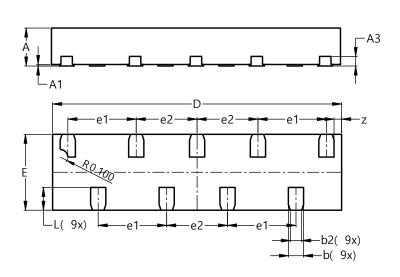




Package Outline Dimensions

 $Please see \ http://www.diodes.com/package-outlines.html \ for \ the \ latest \ version.$

U-DFN3810-9 (Type B)

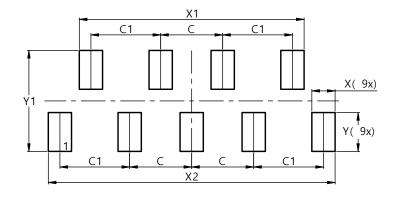


U-DFN3810-9 (Type B)						
Dim	Min	Max	Тур			
Α	0.45	0.55	0.50			
A1	0.00	0.05	0.02			
А3		_	0.127			
b	0.15	0.25	0.20			
b2	0.10	0.20	0.15			
D	3.75	3.85	3.80			
Е	0.95	1.05	1.00			
e1	_	_	0.90			
e2	_		0.80			
L	0.25	0.35	0.30			
Z			0.10			
All	Dimensi	ions in r	nm			

Suggested Pad Layout

 $\label{lem:please} Please see \ http://www.diodes.com/package-outlines.html \ for \ the \ latest \ version.$

U-DFN3810-9 (Type B)



Dimensions	Value (in mm)		
С	0.800		
C1	0.900		
X	0.300		
X1	2.900		
X2	3.700		
Y	0.500		
Y1	1.300		



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