



#### 24V ULTRA LOW CAPACITANCE BIDIRECTIONAL TVS DIODE

## **Product Summary**

V <sub>BR</sub> (Min)	IPP (Max)	Ст (Тур)
25V	1A	0.3pF

### Description

This new generation TVS is designed to protect sensitive electronics from the damage due to ESD. The combination of small size and high ESD surge capability makes it ideal for use in portable applications such as cellular phones, digital cameras, and MP3 players.

## **Applications**

- Cellular Handsets
- Portable Electronics
- · Computers and Peripheral

### **Features**

- Low Profile Package (0.53mm Max) and Ultra-Small PCB Footprint Area (1.08mm \* 0.68mm Max) Suitable for Compact Portable Electronics
- Provides ESD Protection per IEC 61000-4-2 Standard:
  Air ±14kV, Contact ±12kV
- 1 Channel of ESD Protection
- Low Channel Input Capacitance
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e.: parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please refer to the related automotive grade (Q-suffix) part. A listing can be found at

https://www.diodes.com/products/automotive/automotive-products/.

- This part is qualified to JEDEC standards (as references in AEC-Q) for High Reliability.
  - https://www.diodes.com/quality/product-definitions/
- An Automotive-Compliant Part is Available Under Separate Datasheet (<u>DESD24VF1BLQ</u>)

### **Mechanical Data**

- Case: X1-DFN1006-2
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: NiPdAu over Copper Leadframe. Solderable per MIL-STD-202, Method 208
- Weight: 0.001 grams (Approximate)

X1-DFN1006-2



**Bottom View** 



Device Schematic

## Ordering Information (Note 4)

Part Number	Compliance	Marking	Reel Size (inches)	Tape Width (mm)	Quantity per Reel
DESD24VF1BL-7B	Standard	Q6	7	8	10,000/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

# **Marking Information**



Q6 = Product Type Marking Code Bar Denotes Pin 1

DESD24VF1BL Document number: DS42366 Rev. 1 - 2



# **Maximum Ratings** (@ $T_A = +25$ °C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	Conditions
Peak Pulse Current	IPP	1	Α	8/20µs, per Figure 1
ESD Protection – Contact Discharge	VESD_Contact	±12	kV	IEC 61000-4-2 Standard
ESD Protection – Air Discharge	VESD_Air	±14	kV	IEC 61000-4-2 Standard

## **Thermal Characteristics**

Characteristic	Symbol	Value	Unit
Package Power Dissipation (Note 5)	P <sub>D</sub>	250	mW
Thermal Resistance, Junction to Ambient (Note 5)	R <sub>0</sub> JA	500	°C/W
Operating and Storage Temperature Range	TJ, TSTG	-65 to +150	°C

## **Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

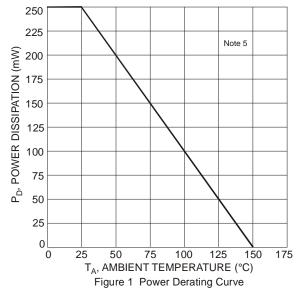
Characteristic	Symbol	Min	Тур	Max	Unit	Test Conditions
Reverse Working Voltage	VRWM	_	_	24	V	_
Reverse Current (Note 6)	IR	_	1	30	nA	V <sub>R</sub> = 24V
Reverse Breakdown Voltage	V <sub>BR</sub>	25	_	_	V	I <sub>R</sub> = 1mA
Reverse Clamping Voltage	V <sub>CL1</sub>	_	21	_	V	I <sub>TLP</sub> = 16A, t <sub>P</sub> = 100ns
Reverse Clamping Voltage	V <sub>CL2</sub>	_	_	23	V	IPP = 1A, tP = 8/20μs
Dynamic Resistance	RDYN	-	0.65	_	Ω	TLP, 10A, t <sub>P</sub> = 100ns
Capacitance	Ст		0.3	0.45	pF	V <sub>R</sub> = 0V, f = 1MHz

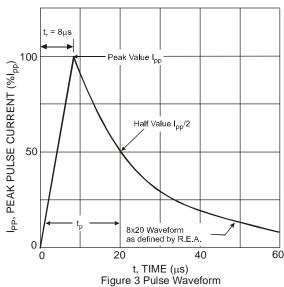
Notes:

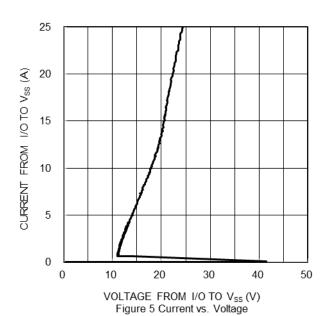
<sup>5.</sup> Device mounted on FR-4 PCB pad layout (2oz copper) as shown on Diodes Incorporated's suggested pad layout, which can be found on our website at http://www.diodes.com/package-outlines.html.

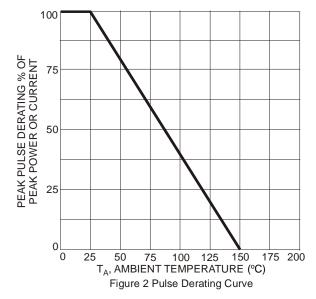
<sup>6.</sup> Short duration pulse test used to minimize self-heating effect.

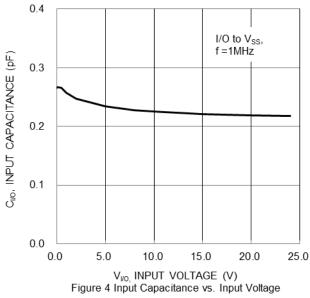












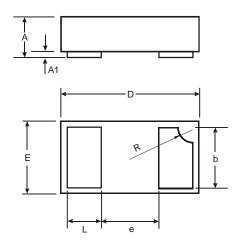
DESD24VF1BL Document number: DS42366 Rev. 1 - 2



# **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.

#### X1-DFN1006-2

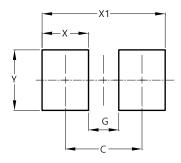


X1-DFN1006-2					
Dim	Min	Max	Тур		
Α	0.47	0.53	0.50		
A1	0	0.05	0.03		
b	0.45	0.55	0.50		
D	0.95	1.075	1.00		
Е	0.55	0.675	0.60		
е	-	-	0.40		
L	0.20	0.30	0.25		
R	0.05	0.15	0.10		
All Dimensions in mm					

# **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.

### X1-DFN1006-2



Dimensions	Value		
Difficilisions	(in mm)		
С	0.70		
G	0.30		
Х	0.40		
X1	1.10		
Υ	0.70		



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