



DMT64M1LCG

Equivalent Circuit

### **Product Summary**

BV <sub>DSS</sub>	R <sub>DS(ON)</sub> Max	I <sub>D</sub> Max Tc = +25°C
65V	5.4mΩ @ V <sub>GS</sub> = 10V	67.8A
057	7.3mΩ @ V <sub>GS</sub> = 4.5V	59.6A

# **Description and Applications**

This new generation N-Channel Enhancement Mode MOSFET is designed to minimize  $R_{DS(ON)}$  yet maintain superior switching performance. This device is ideal for use in notebook battery power management and load switch.

- Synchronous Rectifier
- Power Management Functions
- DC-DC Converters

### 65V N-CHANNEL ENHANCEMENT MODE MOSFET

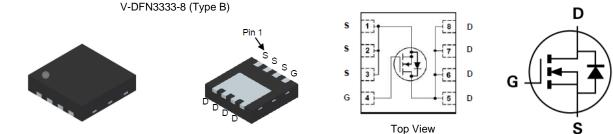
### **Features and Benefits**

- 100% Unclamped Inductive Switching (UIS) Test in Production Ensures More Reliable and Robust End Application
- High Conversion Efficiency
- Low RDS(ON) Minimizes On State Losses
- Low Input Capacitance
- Fast Switching Speed
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please <u>contact us</u> or your local Diodes representative. <u>https://www.diodes.com/quality/product-definitions/</u>

### **Mechanical Data**

- Case: V-DFN3333-8
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections Indicator: See Below Diagram
- Terminals: Finish—NiPdAu over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (4)
- Weight: 0.027 grams (Approximate)

Internal Schematic



Top View

### Ordering Information (Note 4)

Part Number	Case	Packaging
DMT64M1LCG-7	V-DFN3333-8 (Type B)	2,000/Tape & Reel
DMT64M1LCG-13	V-DFN3333-8 (Type B)	3,000/Tape & Reel

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.

2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Bottom View

# **Marking Information**

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ХМХ	
641	

- 641 = Product Type Marking Code
  - YWX = Date Code Marking
  - Y = Year (ex: 1 = 2021)

W = Week (ex: a = Week 27; z Represents Week 52 and 53)

X = Internal Code (ex: U = Monday)

Date Code Key
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Notes:

Year	2019	2020	2021	2022	2023	2024	2025	2026	2027	2028	2029	2030
Code	9	0	1	2	3	4	5	6	7	8	9	0
Week	1-26			27-52			53					
Code	A-Z				a	-Z				Z		

Intern	al Code	Sun	Mon	Tue	Wed	Thu	Fri	Sat
C	Code	Т	U	V	W	Х	Y	Z



### Maximum Ratings (@TA = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			Vdss	65	V
Gate-Source Voltage			V <sub>GSS</sub>	±20	V
Steady		T <sub>A</sub> = +25°C	1-	16.7	^
Continuous Drain Current, V <sub>GS</sub> = 10V (Note 6)	State	T <sub>A</sub> = +70°C	ID	13.4	A
Continuous Drain Current, V <sub>GS</sub> = 10V (Note 7) $\frac{T_C = +25^{\circ}C}{T_C = +70^{\circ}C}$			ID	67.8	
				54.2	A
Pulsed Drain Current (10µs Pulse, Duty Cycle =	1%)	•	I <sub>DM</sub>	260	А
Maximum Continuous Body Diode Forward Curre	ent (Note 7)		ls	65	Α
Pulsed Body Diode Forward Current (10µs Pulse	e, Duty Cycle	e = 1%)	lsм	260	A
Avalanche Current, L = 1mH		las	20.3	А	
Avalanche Energy, L = 1mH		Eas	206	mJ	
V <sub>DS</sub> Spike		t = 10µs	VSPIKE	65	V

# Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)	T <sub>A</sub> = +25°C	PD	1.2	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	Reja	101	°C/W
Total Power Dissipation (Note 6)	T <sub>A</sub> = +25°C	PD	2.6	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	R <sub>0JA</sub>	49	°C/W
Thermal Resistance, Junction to Case (Note 7)		Rejc	3.0	°C/W
Operating and Storage Temperature Range		TJ, TSTG	-55 to +150	°C

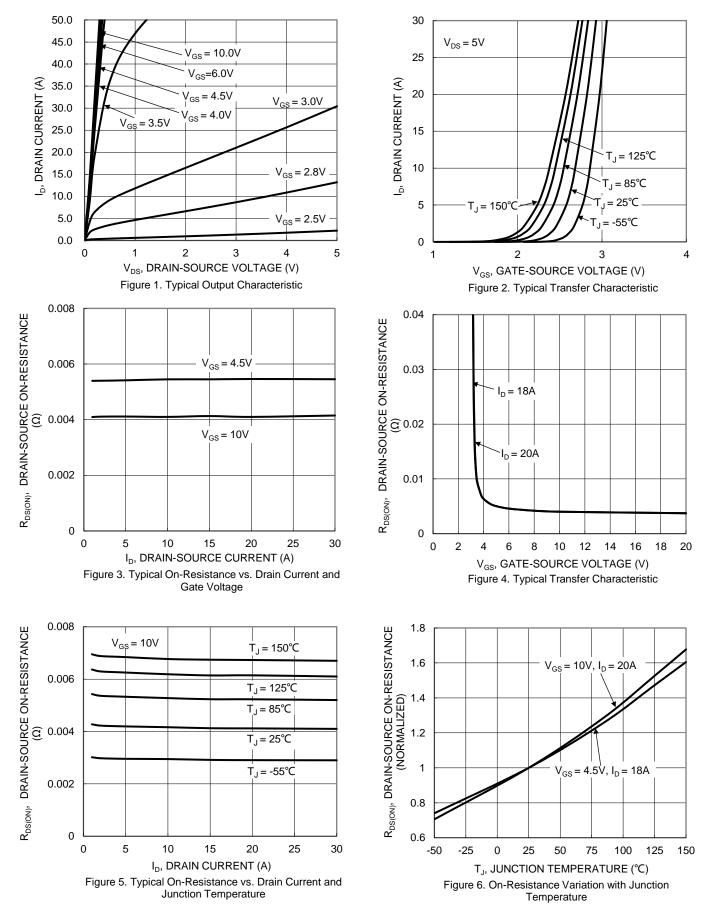
# Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 8)					•	
Drain-Source Breakdown Voltage	BVDSS	65	_	_	V	$V_{GS} = 0V$ , $I_D = 10mA$
Zero Gate Voltage Drain Current	IDSS	_	_	1	μA	$V_{DS} = 52V, V_{GS} = 0V$
Gate-Source Leakage	lgss	_	_	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 8)						-
Gate Threshold Voltage	Vgs(th)	1.3	—	2.5	V	$V_{DS} = V_{GS}$ , $I_D = 250 \mu A$
Static Drain-Source On-Resistance	Proven	_	4.1	5.4	mΩ	$V_{GS} = 10V, I_D = 20A$
	R <sub>DS(ON)</sub>	_	5.5	7.3	11152	VGS = 4.5V, ID = 18A
Diode Forward Voltage	Vsd	_	0.7	1.2	V	$V_{GS} = 0V$ , $I_S = 1A$
DYNAMIC CHARACTERISTICS (Note 9)				-		
Input Capacitance	Ciss		2626	_		V <sub>DS</sub> = 30V, V <sub>GS</sub> = 0V, f = 1MHz
Output Capacitance	Coss	_	905	—	pF	
Reverse Transfer Capacitance	Crss		91	_		
Gate Resistance	Rg	_	1.21	—	Ω	$V_{DS} = 0V$ , $V_{GS} = 0V$ , $f = 1MHz$
Total Gate Charge (V <sub>GS</sub> = 10V)	Qg	_	51.4	—		
Total Gate Charge (VGS = 4.5V)	Qg	_	28.9	—	nC	V <sub>DS</sub> = 30V, I <sub>D</sub> = 30A
Gate-Source Charge	Q <sub>gs</sub>	—	8.2	—		
Gate-Drain Charge	Q <sub>gd</sub>	_	14.4	_		
Turn-On Delay Time	td(ON)	_	11.5	_		
Turn-On Rise Time	tR	_	7.8	—	]	$V_{DD} = 30V, V_{GS} = 10V,$
Turn-Off Delay Time	tD(OFF)	_	35.1	_	ns	$I_D = 30A, R_g = 3.3\Omega$
Turn-Off Fall Time	tF		19.9			
Reverse Recovery Time	trr		44.8	_	ns	
Reverse Recovery Charge	Qrr		54.0		nC	IF = 30A, di/dt = 100A/μs

 Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
 Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
 Thermal resistance from junction to soldering point (on the exposed drain pad).
 Short duration pulse test used to minimize self-heating effect.
 Guaranteed by design. Not subject to product testing. Notes:



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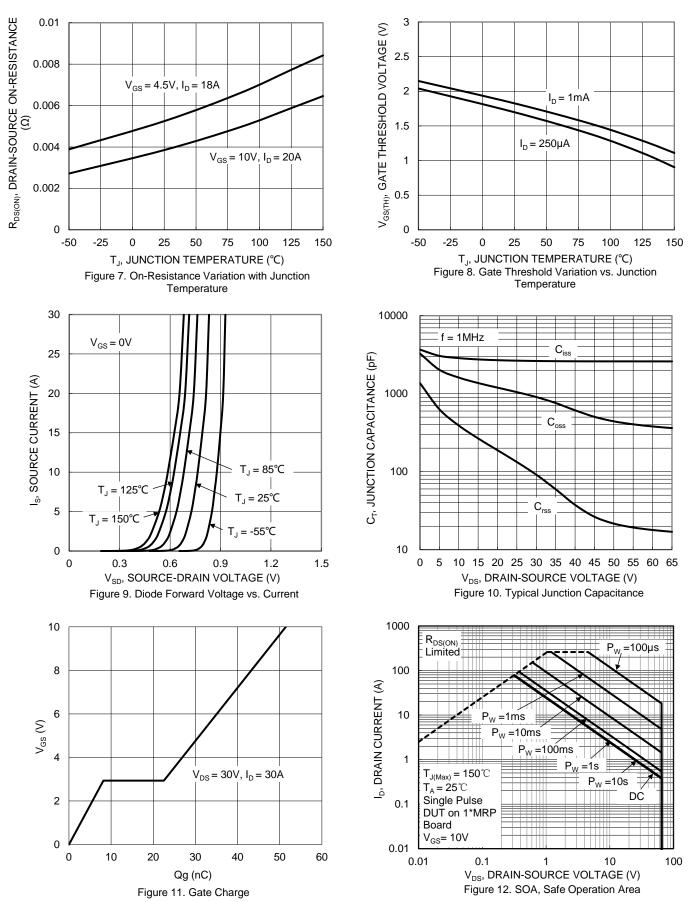


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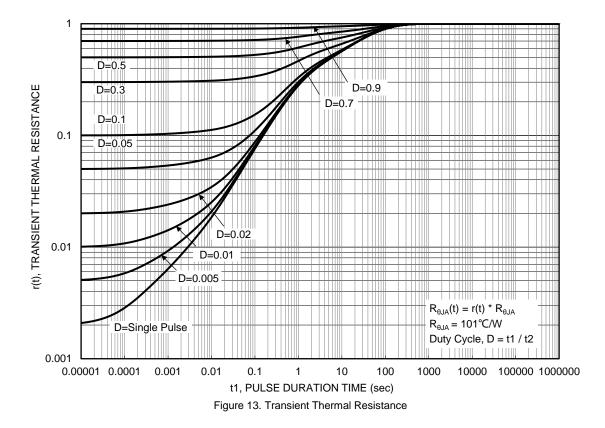
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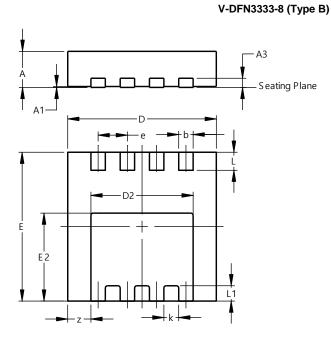






# **Package Outline Dimensions**

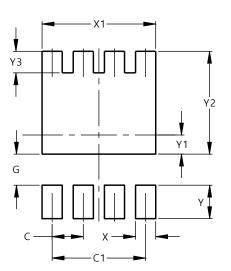
Please see http://www.diodes.com/package-outlines.html for the latest version.



	V-DFN3333-8 (Type B)							
Dim Min Max Typ								
Α	0.75	0.85	0.80					
A1	0.00	0.05	0.02					
A3			0.203					
b	0.27	0.37	0.32					
D	3.25	3.35	3.30					
D2	2.17	2.37	2.27					
Е	3.25	3.35	3.30					
E2	1.85	2.05	1.95					
е		1	0.65					
k			0.33					
L	0.35	0.45	0.40					
L1			0.34					
z			0.515					
All	Dimens	sions in	mm					

# **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.



Dimensions	Value (in mm)
С	0.650
C1	1.950
G	0.650
Х	0.420
X1	2.370
Y	0.700
Y1	0.400
Y2	2.150
Y3	0.450

#### V-DFN3333-8 (Type B)

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