

**50V NPN PRE-BIASED (R1=R2) SMALL SIGNAL TRANSISTOR IN DFN1006**

### Product Summary

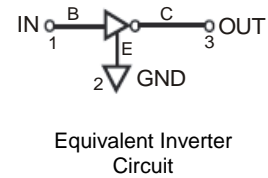
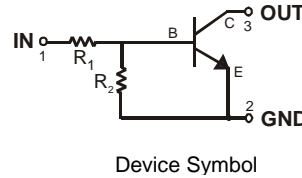
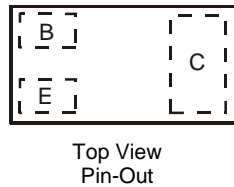
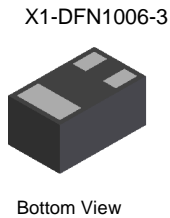
Part Number	R1(NOM)	R2(NOM)	Marking
DDTC144ELP	47kΩ	47kΩ	N6

### Features

- Epitaxial Planar Die Construction
- Complementary PNP Type Available (DDTA144ELP)
- Ultra-Small Leadless Surface Mount Package
- Ideally Suited for Automated Assembly Processes
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **Qualified to AEC-Q101 Standards for High Reliability**

### Mechanical Data

- Case: X1-DFN1006-3
- Case Material: Molded Plastic, "Green" Molding Compound.  
UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Marking Information
- Terminals: Finish — NiPdAu  
Solderable per MIL-STD-202, Method 208 ④
- Weight: 0.001 grams (Approximate)



### Ordering Information (Note 4)

Part Number	Marking	Reel size (inches)	Tape width (mm)	Quantity per reel
DDTC144ELP-7	N6	7	8	3,000
DDTC144ELP-7B	N6	7	8	10,000

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
  2. See [http://www.diodes.com/quality/lead\\_free.html](http://www.diodes.com/quality/lead_free.html) for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
  3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
  4. For packaging details, go to our website at <http://www.diodes.com/products/packages.html>.

### Marking Information

<b>DDTC144ELP-7</b>	<p>Top View Dot Denotes Collector Side</p> <p>Top View Bar Denotes Base and Emitter Side</p>	<p>From date code 1527 (YYWW), this changed to:</p> <p>Top View Bar Denotes Base and Emitter Side</p>
<b>DDTC144ELP-7B</b>	<p>Top View Bar Denotes Base and Emitter Side</p> <p>N6 = Part Marking Code</p>	

**Absolute Maximum Ratings** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Supply Voltage	V <sub>CC</sub>	50	V
Input Voltage	V <sub>IN</sub>	-10 to +40	V
Output Current	I <sub>C(MAX)</sub>	100	mA

**Thermal Characteristics**

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 5)	P <sub>D</sub>	250	mW
Power Deration above 25°C	P <sub>der</sub>	2	mW/°C
Thermal Resistance, Junction to Ambient Air (Note 5)	R <sub>θJA</sub>	500	°C/W
Operation and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C

**Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
Collector-Base Breakdown Voltage	BV <sub>CB0</sub>	50	—	—	V	I <sub>C</sub> = 50μA, I <sub>E</sub> = 0
Collector-Emitter Breakdown Voltage (Note 6)	BV <sub>CEO</sub>	50	—	—	V	I <sub>C</sub> = 1.0mA, I <sub>B</sub> = 0
Collector-Base Cut Off Current	I <sub>CB0</sub>	—	—	0.5	μA	V <sub>CB</sub> = 50V, I <sub>E</sub> = 0
Input Voltage (Note 6)	V <sub>I(OFF)</sub>	0.5	1.2	—	V	V <sub>CE</sub> = 5V, I <sub>O</sub> = 100μA
	V <sub>I(ON)</sub>	—	1.6	3		V <sub>CE</sub> = 0.3V, I <sub>O</sub> = 2mA
Output Voltage (Note 6)	V <sub>O(ON)</sub>	—	—	0.3	V	I <sub>O</sub> /I <sub>I</sub> = 10mA/0.5mA
Input Current	I <sub>I</sub>	—	—	0.18	mA	V <sub>I</sub> = 5V
Output Current	I <sub>O(OFF)</sub>	—	—	0.5	μA	V <sub>CC</sub> = 50V, V <sub>I</sub> = 0V
DC Current Gain (Note 6)	G <sub>1</sub>	68	—	—	—	V <sub>O</sub> = 5V, I <sub>O</sub> = 5mA
Input Resistance	R <sub>1</sub>	32.9	47	61.1	kΩ	—
Resistance Ratio	R <sub>2</sub> /R <sub>1</sub>	0.8	1	1.2	—	—
Transition Frequency (Note 7)	f <sub>T</sub>	—	250	—	MHz	V <sub>CE</sub> = 10V, I <sub>E</sub> = 5mA, f = 100MHz

- Notes:
5. For the device mounted on minimum recommended pad layout 1oz copper that is on a single-sided 1.6mm FR4 PCB; device is measured under still air conditions whilst operating in steady state condition. The entire exposed collector pad is attached to the heatsink.
  6. Measured under pulsed conditions. Pulse width ≤ 300μs. Duty cycle ≤ 2%.
  7. Characteristics of transistor only.

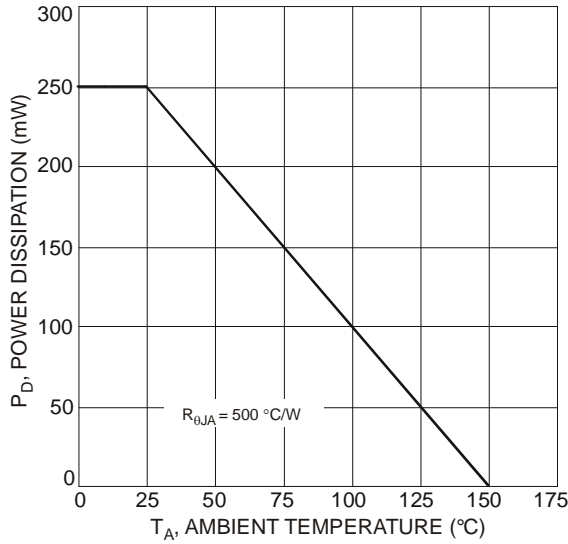


Fig. 1 Power Dissipation vs. Ambient Temperature

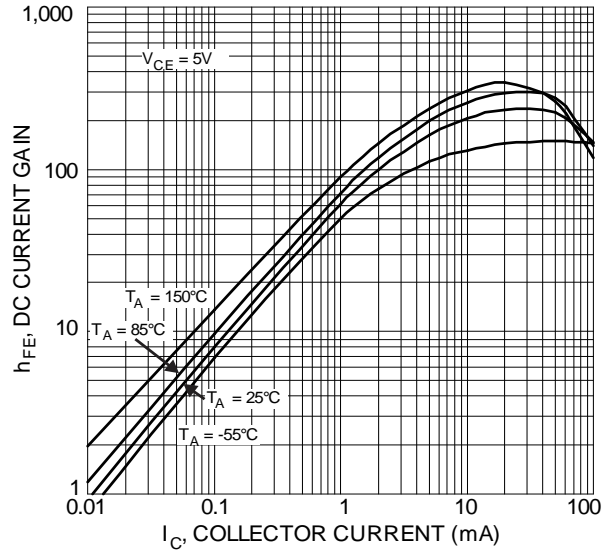


Fig. 2 Typical DC Current Gain vs. Collector Current

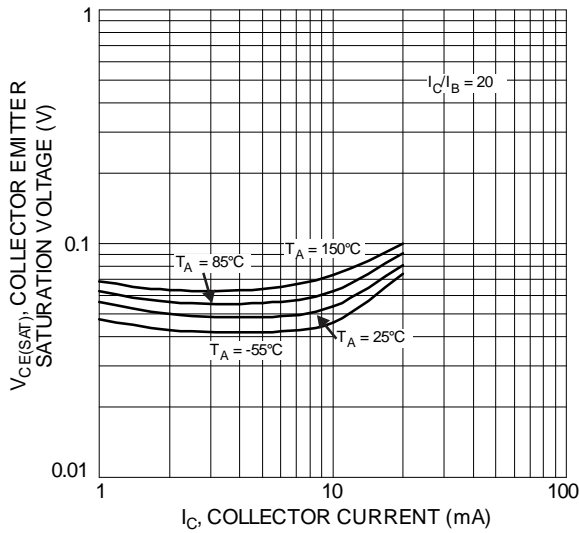


Fig. 3 Typical Collector Emitter Saturation Voltage vs. Collector Current

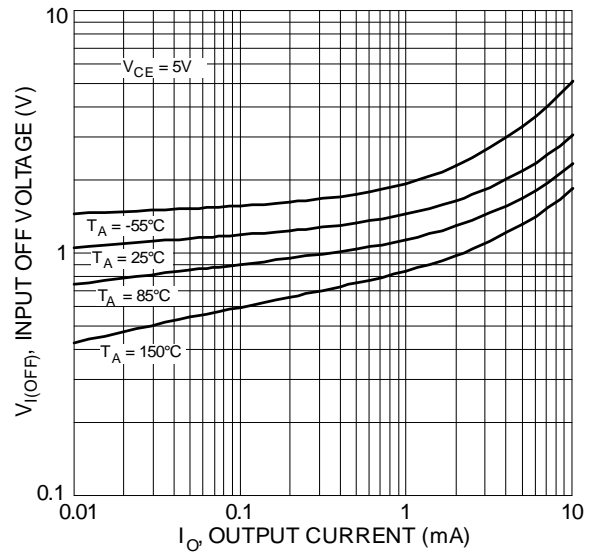


Fig. 4 Typical Input Off Voltage vs. Output Current

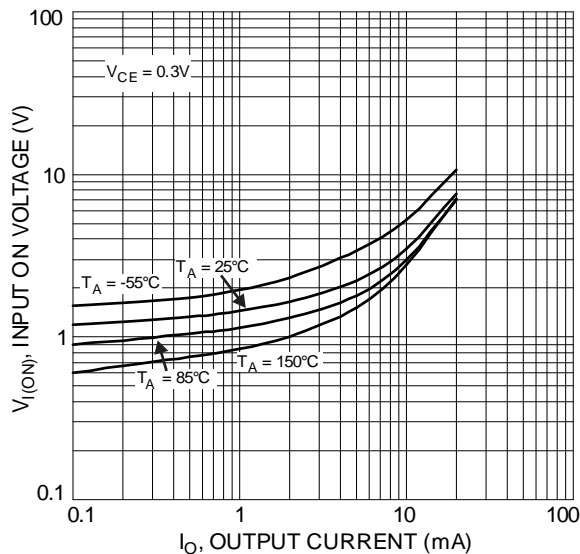
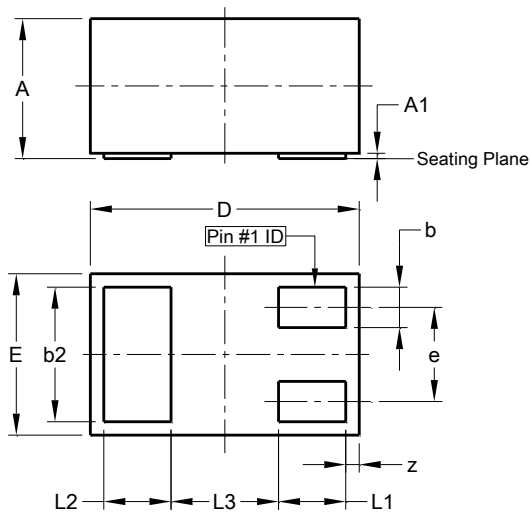


Fig. 5 Typical Input ON Voltage vs. Output Current

**Package Outline Dimensions**

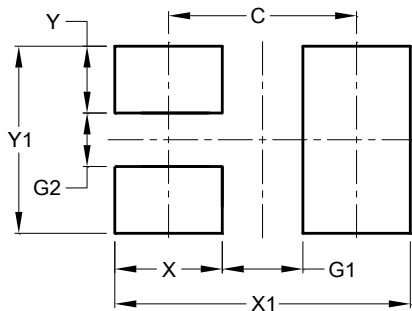
Please see <http://www.diodes.com/package-outlines.html> for the latest version.



X1-DFN1006-3			
Dim	Min	Max	Typ
A	0.47	0.53	0.50
A1	0.00	0.05	0.03
b	0.10	0.20	0.15
b2	0.45	0.55	0.50
D	0.95	1.075	1.00
E	0.55	0.675	0.60
e	-	-	0.35
L1	0.20	0.30	0.25
L2	0.20	0.30	0.25
L3	-	-	0.40
z	0.02	0.08	0.05
<b>All Dimensions in mm</b>			

**Suggested Pad Layout**

Please see <http://www.diodes.com/package-outlines.html> for the latest version.



Dimensions	Value (in mm)
C	0.70
G1	0.30
G2	0.20
X	0.40
X1	1.10
Y	0.25
Y1	0.70

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