



NPN PRE-BIASED 500 MA SURFACE MOUNT TRANSISTOR

Features

- **Epitaxial Planar Die Construction**
- Complementary PNP Types Available (DDTB)
- **Built-In Biasing Resistors**
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please contact us or your local Diodes representative. https://www.diodes.com/quality/product-definitions/

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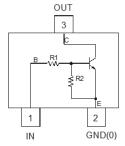
- Case: SOT23
- Case Material: Molded Plastic, "Green" Molding Compound; UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Plated Leads; Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.008 grams (Approximate)

P/N	R1 (NOM)	R2 (NOM)
DDTD122LC	0.22kΩ	10kΩ
DDTD142JC	0.47kΩ	10kΩ
DDTD122TC	0.22kΩ	OPEN
DDTD142TC	0.47kΩ	OPEN

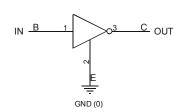
SOT23



Top View



Device Schematic



Equivalent Inverter Circuit

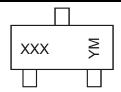
Ordering Information (Note 4)

Product	Status	Compliance	Marking	Reel size (inches)	Tape width (mm)	Quantity per reel
DDTD122LC -7-F	Obsolete	Standard	N75	7	8	3,000
DDTD142JC -7-F	Active	Standard	N76	7	8	3,000
DDTD122TC -7-F	Obsolete	Standard	N77	7	8	3,000
DDTD142TC -7-F	Obsolete	Standard	N78	7	8	3,000

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



XXX = Product Type Marking Code, See Table Above YM = Date Code Marking Y = Year ex: I = 2021 M = Month ex: 9 = September

Data Cada Ka

Date Code Key												
Year	2021	2022	2023	2024	2025	2026	2027	2028	2029	2030	2031	2032
Code	I	J	K	L	M	N	0	Р	R	S	Т	U
	1	1	1	1	1	1	1		_	_		_
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code								_	_	_	N	_

DDTD (LO-R1) C



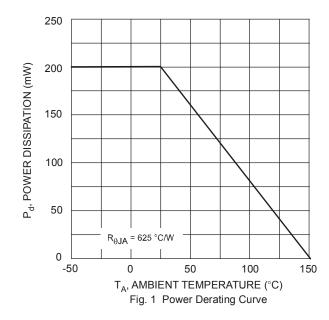
Absolute Maximum Ratings (@ T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Supply Voltage <pin: (2)="" (3)="" to=""></pin:>	V _{CC}	50	V	
Input Voltage <pin: (1)="" (2)<="" td="" to=""><td>DDTD122LC DDTD142JC</td><td>V_{IN}</td><td>-5 to +6 -5 to +6</td><td>V</td></pin:>	DDTD122LC DDTD142JC	V _{IN}	-5 to +6 -5 to +6	V
Input Voltage <pin: (1)<="" (2)="" td="" to=""><td>V_{EBO} (MAX)</td><td>5</td><td>V</td></pin:>	V _{EBO} (MAX)	5	V	
Output Current	Ic	500	mA	

Thermal Characteristics (@ T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 5)	P_{D}	200	mW
Thermal Resistance, Junction to Ambient Air (Note 5)	$R_{ heta JA}$	625	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

Note: 5. Mounted on FR4 PC board with recommended pad layout.





Electrical Characteristics - R1, R2 Types (@ T_A = +25°C, unless otherwise specified.)

Characteristic	Characteristic			Тур	Max	Unit	Test Condition
Input Voltage	DDTD122LC DDTD142JC	$V_{I(off)}$	0.3 0.3	_	_	٧	V _{CC} = 5V, I _O = 100μA
	DDTD122LC DDTD142JC	V _{I(on)}			2.0 2.0	>	V _O = 0.3V, I _O = 20mA V _O = 0.3V, I _O = 20mA
Output Voltage		V _{O(on)}		_	0.3V	٧	$I_{O}/I_{I} = 50$ mA/2.5mA
Input Current	DDTD122LC DDTD142JC	l _l			28 13	mA	V _I = 5V
Output Current		I _{O(off)}	_		0.5	μА	V _{CC} = 50V, V _I = 0V
DC Current Gain	DDTD122LC DDTD142JC	G _l	56 56				V _O = 5V, I _O = 50mA
Gain-Bandwidth Product (Note 6)		f⊤	_	200	_	MHz	V _{CE} = 10V, I _E = 5mA, f = 100MHz

Electrical Characteristics - R1- Only, R2- Only Types (@ T_A = +25°C, unless otherwise specified.)

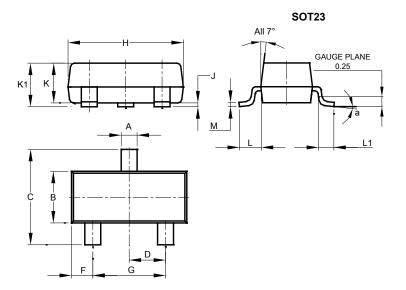
Characteristic		Symbol	Min	Тур	Max	Unit	Test Condition
Collector-Base Breakdown Voltage		BV_CBO	50	_	_	V	I _C = 50μA
Collector-Emitter Breakdown Voltage		BV _{CEO}	40	_	_	V	I _C = 1mA
Emitter-Base Breakdown Voltage	DDTD122TC DDTD142TC	BV _{EBO}	5			٧	I _E = 50μA I _E = 50μA
Collector Cut-Off Current		I _{CBO}	_	_	0.5	μΑ	V _{CB} = 50V
Emitter Cut-Off Current	DDTD122TC DDTD142TC	I _{EBO}			0.5 0.5	μΑ	V _{EB} = 4V
Collector-Emitter Saturation Voltage		V _{CE(sat)}	_	_	0.3	V	$I_C = 50$ mA, $I_B = 2.5$ mA
DC Current Transfer Ratio	DDTD122TC DDTD142TC	h _{FE}	100 100	250 250	600 600		I _C = 5mA, V _{CE} = 5V
Gain-Bandwidth Product (Note 6)		f⊤	_	200	_	MHz	V _{CE} = 10V, I _E = -5mA, f = 100MHz

Note: 6. Transistor – For Reference Only



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

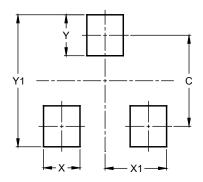


SOT23								
Dim	Dim Min Max Typ							
Α	0.37	0.51	0.40					
В	1.20	1.40	1.30					
С	2.30	2.50	2.40					
D	0.89	1.03	0.915					
F	0.45	0.60	0.535					
G	1.78	2.05	1.83					
Н	2.80	3.00	2.90					
J	0.013	0.10	0.05					
K	0.890	1.00	0.975					
K1	0.903	1.10	1.025					
٦	0.45	0.61	0.55					
L1	0.25	0.55	0.40					
М	0.085	0.150	0.110					
а	0°	8°						
All	Dimens	ions in	mm					

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT23



Dimensions	Value (in mm)
С	2.0
Х	0.8
X1	1.35
Y	0.9
Y1	2.9



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