



DDTC (R1 = R2 SERIES) CA

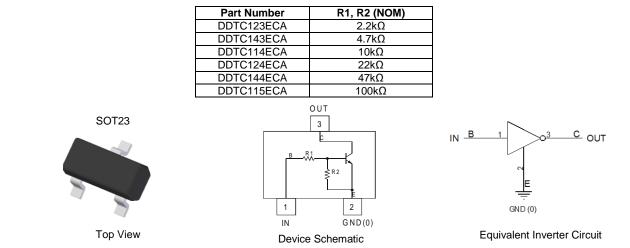
NPN PRE-BIASED SMALL SIGNAL SURFACE MOUNT TRANSISTOR

Features

- **Epitaxial Planar Die Construction**
- Complementary PNP Types Available (DDTA)
- Built-In Biasing Resistors, R1 = R2
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- **PPAP Capable (Note 4)**

Mechanical Data

- Case: SOT23
- Case Material: Molded Plastic, "Green" Molding Compound UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Plated Leads, Solderable per MIL-STD-202, Method 208 @3
- Weight: 0.008 grams (Approximate)



Ordering Information (Notes 4, 5 & 6)

Part Number	Status	Compliance	Marking	Reel Size (inches)	Tape Width (mm)	Quantity Per Reel
DDTC123ECA-7-F	Active	AEC-Q101	N04	7	8	3,000
DDTC123ECAQ-7-F	Active	Automotive	N04	7	8	3,000
DDTC143ECA-7-F	Active	AEC-Q101	N08	7	8	3,000
DDTC143ECA-13-F	Active	AEC-Q101	N08	13	8	10,000
DDTC114ECA-7-F	Active	AEC-Q101	N13	7	8	3,000
DDTC114ECAQ-7-F	NRND (Use ADTC114ECAQ)	Automotive	N13	7	8	3,000
DDTC114ECAQ-13-F	NRND (Use ADTC114ECAQ)	Automotive	N13	13	8	10,000
DDTC124ECA-7-F	Active	AEC-Q101	N17	7	8	3,000
DDTC144ECA-7-F	Active	AEC-Q101	N20	7	8	3,000
DDTC144ECAQ-7-F	Active	Automotive	N20	7	8	3,000
DDTC144ECAQ-13-F	Active	Automotive	N20	13	8	10,000
DDTC115ECA-7-F	Active	AEC-Q101	N24	7	8	3,000

2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. Automotive products are AEC-Q101 qualified and are PPAP capable. Automotive, AEC-Q101 and standard products are electrically and thermally the same, except where specified. For more information, please refer to https://www.diodes.com/quality/.

5. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

6. NRND = Not Recommended for New Design.

Marking Information

Data Codo Kov

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NXX	(ΥM	

NXX = Product Type Marking Code, See Ordering Information

YM = Date Code Marking

Y = Year (ex: F = 2018)

M = Month (ex: 9 = September)

Dale Coue Key																
Year	2018	2019	2020	2021	2022	2023	2024	2025	2026	2027	2028	2029	2030	2031	2032	2033
Code	F	G	Н	Ι	J	К	L	М	N	0	Р	Q	R	S	Т	U
Manth		-	- 1-	Max	A			l	L.I			0	0-1			D
Month	Jan	F	eb	Mar	Apr	IV	lay	Jun	Jul	A	Jg	Sep	Oct	N	ov	Dec
Code	1		2	3	4		5	6	7	8	3	9	0	1	N	D



Absolute Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Ch	aracteristic	Symbol	Value	Unit
Supply Voltage <pin: (2)="" (3)="" to=""></pin:>		Vcc	50	V
Input Voltage <pin: (1)="" (2)="" to=""></pin:>	DDTC123ECA DDTC143ECA DDTC114ECA DDTC124ECA DDTC124ECA DDTC144ECA DDTC115ECA	V _{IN}	-10 to +12 -10 to +30 -10 to +40 -10 to +40 -10 to +40 -10 to +40	V
Output Current	DDTC123ECA DDTC143ECA DDTC114ECA DDTC124ECA DDTC124ECA DDTC144ECA DDTC115ECA	lo	100 100 50 30 30 20	mA
Output Current	•	I _C (Max)	100	mA

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 7)	PD	200	mW
Thermal Resistance, Junction to Ambient Air (Note 7)	R _{0JA}	625	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

Note: 7. Mounted on FR4 PC Board with minimum recommended pad layout

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Chara	cteristic	Symbol	Min	Тур	Max	Unit	Test Condition
		V _{I(off)}	0.5	1.1	_		$V_{CC} = 5V, I_{O} = 100\mu A$
Input Voltage	VI(on)		1.9	3	V	$V_0 = 0.3V$, $I_0 = 20mA$, DDTC123ECA $V_0 = 0.3V$, $I_0 = 20mA$, DDTC143ECA $V_0 = 0.3V$, $I_0 = 10mA$, DDTC114ECA $V_0 = 0.3V$, $I_0 = 5mA$, DDTC124ECA $V_0 = 0.3V$, $I_0 = 2mA$, DDTC144ECA $V_0 = 0.3V$, $I_0 = 1mA$, DDTC115ECA	
Output Voltage		V _{O(on)}		0.1	0.3	V	$\begin{split} & _O/I_I = 10 \text{mA}/0.5 \text{mA}, \text{DDTC123ECA} \\ & _O/I_I = 10 \text{mA}/0.5 \text{mA}, \text{DDTC143ECA} \\ & _O/I_I = 10 \text{mA}/0.5 \text{mA}, \text{DDTC114ECA} \\ & _O/I_I = 10 \text{mA}/0.5 \text{mA}, \text{DDTC124ECA} \\ & _O/I_I = 10 \text{mA}/0.5 \text{mA}, \text{DDTC144ECA} \\ & _O/I_I = 5 \text{mA}/0.25 \text{mA}, \text{DDTC115ECA} \end{split}$
Input Current	DDTC123ECA DDTC143ECA DDTC114ECA DDTC124ECA DDTC124ECA DDTC144ECA DDTC115ECA	lı	_	_	3.8 1.8 0.88 0.36 0.18 0.15	mA	V ₁ = 5V
Output Current		I _{O(off)}	_		0.5	μA	$V_{CC} = 50V, V_{I} = 0V$
DC Current Gain	DDTC123ECA DDTC143ECA DDTC114ECA DDTC114ECAQ DDTC124ECA DDTC124ECA DDTC144ECAQ DDTC144ECAQ DDTC115ECA	Gı	20 20 30 35 56 68 80 82				$\begin{array}{l} V_{O}=5V,\ I_{O}=20mA\\ V_{O}=5V,\ I_{O}=10mA\\ V_{O}=5V,\ I_{O}=5mA\\ \end{array}$
Input Resistor Tolerance		ΔR_1	-30		+30	%	
Resistance Ratio Tolerance	9	$\Delta R_2/R_1$	0.8	1	1.2	%	_
Gain-Bandwidth Product (N	lote 8)	f _T		250		MHz	$V_{CE} = 10V$, $I_E = 5mA$, f = 100MHz

Note: 8. Transistor - For Reference Only



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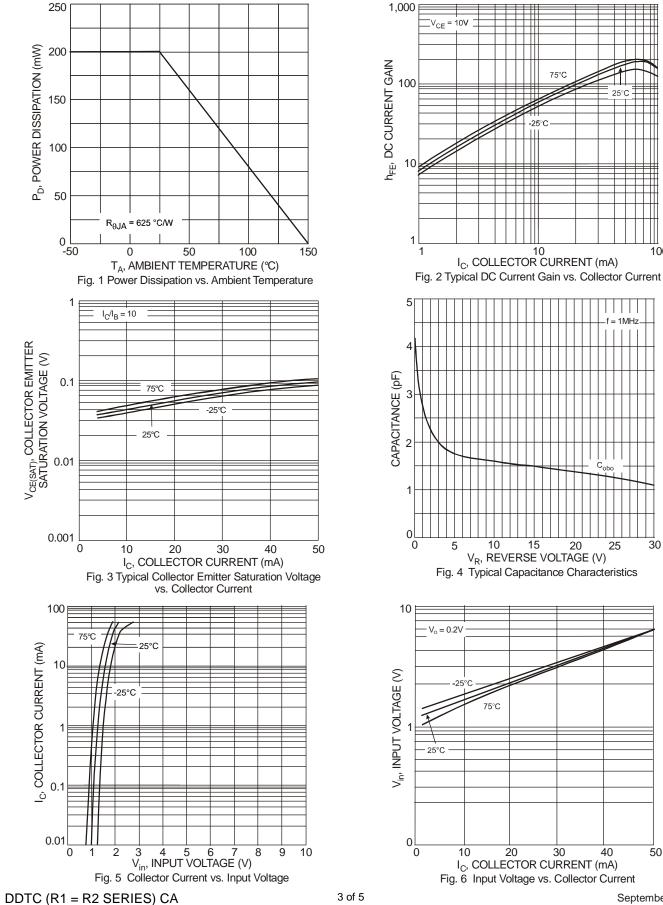
25°C

25

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100

Typical Characteristics – DDTC143ECA (@T_A = +25°C, unless otherwise specified.)



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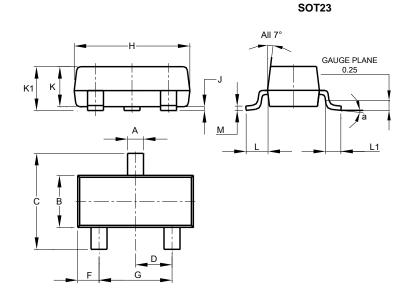
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Package Outline Dimensions

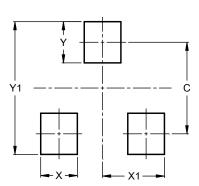
Please see http://www.diodes.com/package-outlines.html for the latest version.



SOT23							
Dim	Min	Max	Тур				
Α	0.37	0.51	0.40				
В	1.20	1.40	1.30				
С	2.30	2.50	2.40				
D	0.89	1.03	0.915				
F	0.45	0.60	0.535				
G	1.78	2.05	1.83				
н	2.80	3.00	2.90				
J	0.013	0.10	0.05				
К	0.890	1.00	0.975				
K1	0.903	1.10	1.025				
L	0.45	0.61	0.55				
L1	0.25	0.55	0.40				
М	0.085	0.150	0.110				
а	0°	8°					
All	Dimens	ions in	mm				

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.



SOT23

Dimensions	Value (in mm)
С	2.0
Х	0.8
X1	1.35
Y	0.9
Y1	2.9



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