

Description

The DIODESTM AP22916 is a small, low leakage, single P-channel power MOSFET designed for low-power consumption and loadswitching applications. This power MOSFET has a typical $R_{DS(ON)}$ of 60m Ω at 5V, allowing increased load current handling capacity with a low forward voltage drop. Multiple voltages correspond to different time options to support various system load conditions. The trigger of the load switch ON pin can be controlled to be enabled or disabled by an external low voltage digital signal for sequence control application. Smart pull down feature is built in the ON pin. Once the enable voltage is higher than V_{IH} , it will disconnect to avoid power loss. V_{IN} and V_{OUT} are isolated during OFF state with the TRCB (true reverse current blocking) feature.

The AP22916 load switch is designed to operate from 1.3V to 5.5V, making it ideal for 1.3V, 1.8V, 2.5V, 3.6V, and 5V systems. The typical quiescent supply current is only 0.5μ A.

The AP22916 is available in the wafer-level chip-scale 4-pin, X1-WLB0808-4 0.78mm x 0.78mm x 0.455mm, 0.4mm pitch package. The device is characterized for operation over a temperature range of -40°C to +85°C.

Features

- Wide Input Voltage Range: 1.3V to 5.5V
 - Low On-Resistance
 - 280mΩ Typical @1.3V
 - 135mΩ Typical @1.8V
 - 65mΩ Typical @3.6V
 - 54mΩ Typical @5.0V
- Continuous Current Capability up to 2A
- True Reverse Current Blocking (TRCB)
- Discharging Resistor on V_{OUT} When Disabled
- Ultra-Low Quiescent Current 0.5µA
- Active-High Control Pin
 - Minimum 1.0V V_{IH} of ON
- ESD Protection:
 - Human Body Model: 2kV
 - Charged Device Model: 1kV
- Package:

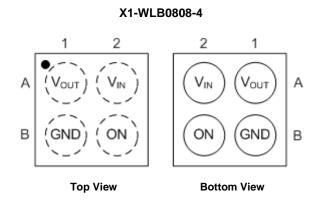
Notes:

- X1-WLB0808-4 with Backside Laminate
- 0.78mm x 0.78mm, 0.4mm Ball Pitch
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/104/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please <u>contact us</u> or your local Diodes representative. <u>https://www.diodes.com/quality/product-definitions/</u>

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.

- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

Pin Assignments



Applications

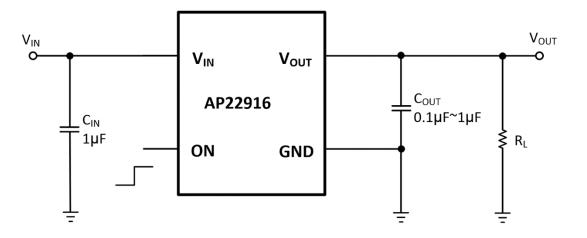
- Mobile devices and smart phones
- Portable media devices
- Wearable devices
- Advanced notebooks, UMPC, and MID
- Portable medical devices
- GPS and navigation equipment

Part Comparison Table

Version	Timing	Output Discharge	Enable
AP22916B	Fast	Yes	Active High
AP22916C	Slow	Yes	Active High
AP22916D	Fast	No	Active High
AP22916E	Slow	No	Active High



Typical Applications Circuit

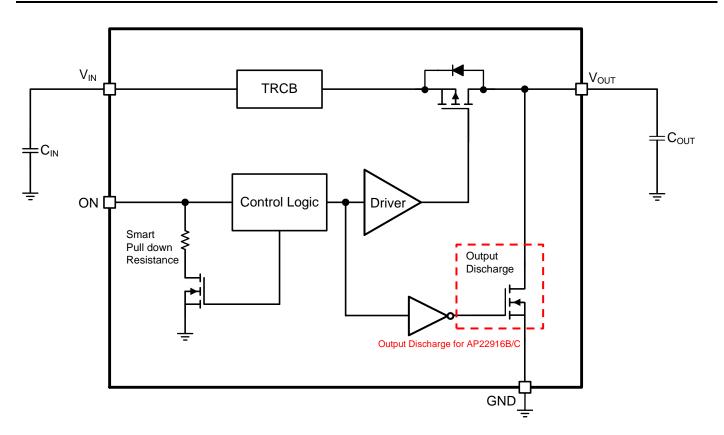


Pin Descriptions

Pin Name	Pin Number	Function
V _{OUT}		Voltage output pin. This is the pin to the P-channel MOSFET drain connection. Bypass to ground through a 0.1μ F or 1μ F capacitor.
V _{IN}	Δ.2	Voltage input pin. This is the pin to the P-channel MOSFET source. Bypass to ground through a $1\mu\text{F}$ capacitor.
GND	B1	Ground
ON	B2	Enable input



Functional Block Diagram





Symbol	Parameter	Ratings	Unit
ESD HBM	Human Body Model ESD Protection	2	kV
ESD CDM	Charged Device Model ESD Protection	1	kV
V _{IN}	Input Voltage	-0.3 to 6	V
V _{OUT}	Output Voltage	-0.3 to 6	V
V _{ON}	ON Voltage	-0.3 to 6	V
I _{LOAD}	Maximum Continuous Load Current	2	А
I _{LOAD}	Maximum Pulse Load Current, Pulse <300µs, 2% Duty Cycle	2.5	А
TJ	Maximum Junction Temperature	+125	°C
T _{ST}	Storage Temperature Range	-65 to +150	°C
PD	Power Dissipation	510	mW
$R_{ extsf{ heta}JA}$	Thermal Resistance, Junction to Ambient (Note 4)	195	°C/W
R _{θJC}	Thermal Resistance, Junction to Case (Note 5)	38	°C/W
T _{LEAD}	Maximum Lead temperature (10-s soldering time)	260	°C

Absolute Maximum Ratings (@ T_A = +25°C, unless otherwise specified.)

Notes: 4. The JEDEC high-K (2s2p) board used to derive this data was a 3 inch x 3 inch, multilayer board with 1oz internal power and ground planes with 2oz copper traces on top and bottom of the board.

5. Thermal resistance from junction to case.

Caution: Stresses greater than the 'Absolute Maximum Ratings' specified above, may cause permanent damage to the device. These are stress ratings only; functional operation of the device at these or any other conditions exceeding those indicated in this specification is not implied. Device reliability may be affected by exposure to absolute maximum rating conditions for extended periods of time.

Recommended Operating Conditions (@ T_A = +25°C, unless otherwise specified.)

Symbol	Parameter	Min	Мах	Unit
V _{IN}	Input Voltage	1.3	5.5	V
V _{ON}	ON Voltage Range	0	5.5	V
V _{OUT}	Output Voltage	1.3	5.5	V
	Output Current while Vin≧1.5V	0	2.0	А
I _{OUT}	Output Current while Vin≦1.5V	0	1.0	А
V _{IH}	ON High-Level Input Voltage	1.0	5.5	V
VIL	ON Low-Level Input Voltage	0	0.35	V
T _A	Operating Ambient Temperature	-40	+85	°C



Electrical Characteristics ($T_A = -40^{\circ}C$ to $+85^{\circ}C$, $V_{IN} = 1.3$ to 5.5V, $V_{ON} = V_{IN}$ (Enabled), $V_{ON} = 0V$ (Disabled), $C_{IN} = 1\mu$ F, $C_{OUT} = 0.1\mu$ F, unless otherwise specified. Typical values are at 25°C) (Note 6)

Symbol	Parameters	Test Co	Test Conditions		Тур	Max	Unit
lα	Input Quiescent Current	I _{OUT} = 0mA, V _{ON} Enat	I _{OUT} = 0mA, V _{ON} Enabled		0.3	0.5	μA
		$R_{L} = 1M\Omega, V_{ON}$	+25°C	_	40	_	
	Innut Chutdaure Current	Disabled, V _{IN} =5.0V	-40°C to +85°C	_	—	225	
I _{SHDN}	Input Shutdown Current	$R_L = 1M\Omega, V_{ON}$	+25°C	_	5	_	nA
		Disabled, V _{IN} =1.8V	-40°C to +85°C	_	_	20	
			+25°C	_	54	60	
		$V_{IN} = 5.0V$	-40°C to +85°C	_	_	70	mΩ
			-40°C to +105°C	_	—	75	
		V _{IN} = 3.6V	+25°C	_	65	75	
			-40°C to +85°C	_	_	85	
р	Switch On-resistance, I _{OUT} = 200mA		-40°C to +105°C	_	_	90	
R _{DS(ON)}		V _{IN} = 1.8V	+25°C	_	135	150	
			-40°C to +85°C	_	—	165	
			-40°C to +105°C	_	—	180	
			+25°C	_	280	310	
	V _{IN} = 1.3V	-40°C to +85°C	_	—	320		
			-40°C to +105°C	_	_	350	
R _{ON}	Smart Pull Down Resistance	V _{ON} Disabled		_	750	_	kΩ
V _{RCB}	TRCB Trigger Voltage	V _{ON} Enabled, V _{OUT} > V	IN	_	25	_	mV
I _{RCB}	TRCB Activation Current	V _{IN} =3.3V, V _{ON} Enabled	V _{IN} =3.3V, V _{ON} Enabled, V _{OUT} > V _{IN}		-650		mA
t _{RCB}	TRCB Response Time	V _{on} Enabled, V _{out} > V _{IN} + 200mV		_	10		μs
I _{IN_RCB}	TRCB Reverse Leakage Current (Current from V _{IN})	V_{ON} Enabled, V_{OUT} - $V_{IN} > V_{RCB}$		-300	—	_	nA
R _{DIS}	Output Discharge On Resistance	V_{ON} Disabled, $I_{OUT} = 2$	ImA	_	150		Ω

Note: 6. Specifications are over -40°C to +85°C and are guaranteed by characterization and design.



Timing Characteristics (The typical characteristics in the following table applies over the entire recommended power supply voltage range of 1.3V to 5.5V at 25°C with a load of $C_{OUT} = 0.1 \mu$ F, R_L = 10 Ω , unless otherwise specified.) (Note 7)

Symbol	Parameters	Test Conditions	Min	Тур	Max	Unit
AP22916B						
		$V_{IN} = 5.0V$	—	85	—	
	ton Output Turn-on	$V_{IN} = 3.6V$	_	110		
t _{ON}		V _{IN} = 1.8V	_	250		μs
		V _{IN} = 1.3V	_	480		
	t _R Output Rise Time	V _{IN} = 5.0V	_	42		
		V _{IN} = 3.6V	_	52	_	μs
τ _R		V _{IN} = 1.8V	_	95	_	
		V _{IN} = 1.3V	_	180	—	
		V _{IN} = 5.0V	_	90		
0.5		$V_{IN} = 3.6V$	_	52		mV/µs
SRON	Slew Rate	V _{IN} = 1.8V	_	13		
		V _{IN} = 1.3V	_	5		
		V _{IN} = 5.0V	_	6.4		
		$V_{IN} = 3.6V$	_	8		
t _{OFF}	Output Turn-off Time	V _{IN} = 1.8V	_	16		μs
		V _{IN} = 1.3V	_	25	—	
		$C_{OUT} = 0.1 \mu F, R_L = 10 \Omega$	—	2.3	—	
t _F	Output Fall Time	$C_{OUT} = 1\mu F, R_L = Open$	_	357	_	μs

Note: 7. Rise and fall time of the control signal are less than 100ns.

Symbol	Parameters	Test Conditions	Min	Тур	Max	Unit
AP22916C						
		$V_{IN} = 5.0V$	_	1400	—	<u> </u>
	Output Turn on	$V_{IN} = 3.6V$		1700	_	
t _{ON}	t _{on} Output Turn-on	$V_{IN} = 1.8V$	_	3800	_	μs
		V _{IN} = 1.3V		6800	_	
		$V_{IN} = 5.0V$		750	_	
		$V_{IN} = 3.6V$	_	900	_	μs
t _R	t _R Output Rise Time	$V_{IN} = 1.8V$		1500	_	
		V _{IN} = 1.3V		2800	_	
		V _{IN} = 5.0V		5	_	− mV/µs
00		$V_{IN} = 3.6V$		3.2	_	
SRON	Slew Rate	V _{IN} = 1.8V		1	_	
		V _{IN} = 1.3V		0.4		
		V _{IN} = 5.0V		7.1	_	
		$V_{IN} = 3.6V$		8	_	- μs
t _{OFF}	Output Turn-off Time	V _{IN} = 1.8V		16		
		V _{IN} = 1.3V		25	_	
		$C_{OUT} = 0.1 \mu F, R_L = 10 \Omega$	_	2.3	_	
t _F	Output Fall Time	$C_{OUT} = 10\mu F, R_L = Open$		4490	_	μs

Note: 7. Rise and fall time of the control signal are less than 100ns.



Timing Characteristics (The typical characteristics in the following table applies over the entire recommended power supply voltage range of 1.3V to 5.5V at 25°C with a load of $C_{OUT} = 0.1 \mu$ F, R_L = 10 Ω , unless otherwise specified.) (Note 7) (continued)

Symbol	Parameters	Test Conditions	Min	Тур	Мах	Unit
AP22916D					•	
		$V_{IN} = 5.0V$		85		
		$V_{IN} = 3.6V$		110		
t _{on}	Output Turn-on	$V_{IN} = 1.8V$		250		μs
		V _{IN} = 1.3V		480		
		$V_{IN} = 5.0V$		50		
	Output Rise Time	$V_{IN} = 3.6V$		60		
t _R		V _{IN} = 1.8V		110		μs
		V _{IN} = 1.3V		210		
		$V_{IN} = 5.0V$		90		
CD		$V_{IN} = 3.6V$		55		
SR _{ON}	Slew Rate	V _{IN} = 1.8V		15		mV/µs
		V _{IN} = 1.3V		5		
		$V_{IN} = 5.0V$		9		
		$V_{IN} = 3.6V$		12		
t _{OFF}	Output Turn-off Time	V _{IN} = 1.8V		18		μs
		V _{IN} =1.3V		35		1
t _F	Output Fall Time	$C_{OUT} = 0.1 \mu F, R_L = 10 \Omega$		13		μs

Note: 7. Rise and fall time of the control signal are less than 100ns.

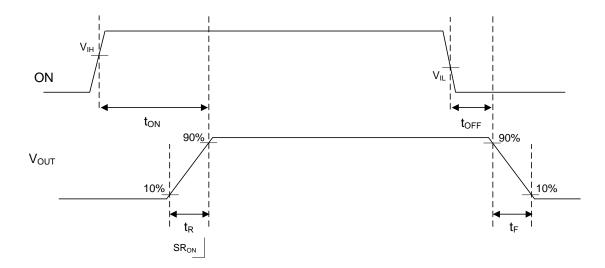
Symbol	Parameters	Test Conditions	Min	Тур	Max	Unit
AP22916E						
		$V_{IN} = 5.0V$		1300		
		$V_{IN} = 3.6V$		1700		1
t _{ON}	Output Turn-on	V _{IN} = 1.8V		3950		μs
		V _{IN} = 1.3V		7200		
	t _R Output Rise Time	$V_{IN} = 5.0V$		750		
		$V_{IN} = 3.6V$		930		μs
τ _R		V _{IN} = 1.8V		1750		
		V _{IN} = 1.3V		3300		
		$V_{IN} = 5.0V$		5		
0.0		$V_{IN} = 3.6V$		3		
SR _{ON}	Slew Rate	V _{IN} = 1.8V		0.8		mV/µs
		V _{IN} = 1.3V		0.3		
		$V_{IN} = 5.0V$		8		
		$V_{IN} = 3.6V$		10		1
t _{OFF}	Output Turn-off Time	V _{IN} = 1.8V		15		μs
		V _{IN} = 1.3V		35		1
t _F	Output Fall Time	$C_{OUT} = 0.1 \mu F, R_L = 10 \Omega$		13		μs

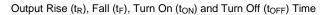
Note: 7. Rise and fall time of the control signal are less than 100ns.



Timing Characteristics (The typical characteristics in the following table applies over the entire recommended power supply voltage range of 1.3V to 5.5V at 25°C with a load of $C_{OUT} = 0.1 \mu$ F, R_L = 10 Ω , unless otherwise specified.) (Note 7) (continued)

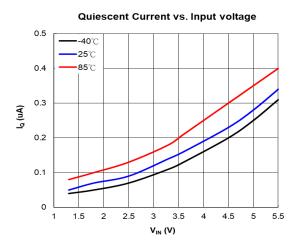
Timing for Power-Up and Power-Down Operation



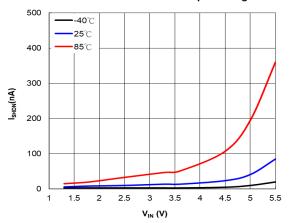


Note: 7. Rise and fall time of the control signal are less than 100ns.

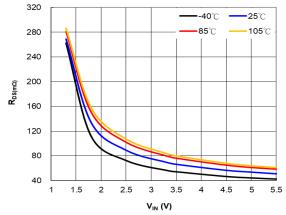




Shutdown Current vs. Input voltage

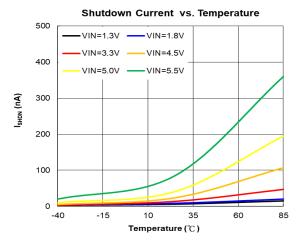


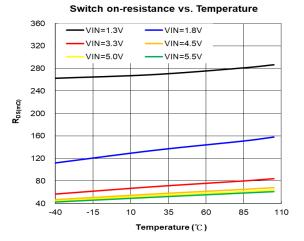
Switch on-resistance vs. Input voltage



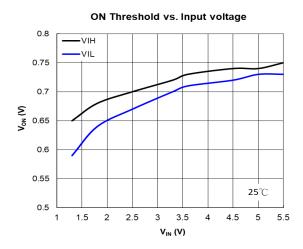
0.5 -VIN=1.3V ----VIN=1.8V VIN=3.3V -----VIN=4.5V 0.4 VIN=5.0V ----VIN=5.5V 0.3 l_a (uA) 0.2 0.1 0 -40 -15 10 35 60 85 Temperature (°C)

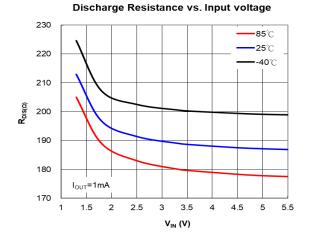
Quiescent Current vs. Temperature



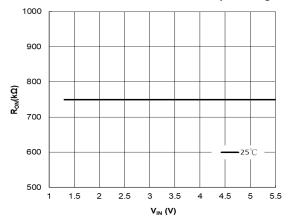




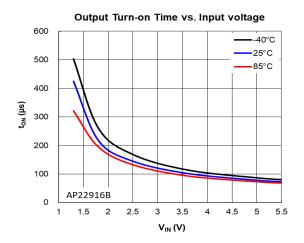




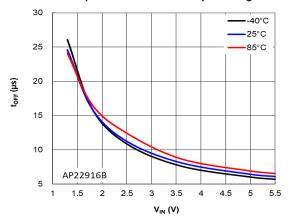
Smart Pull Down Resistance vs. Input voltage

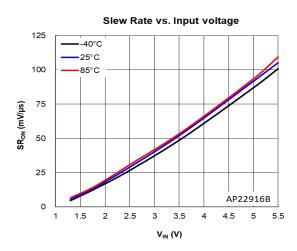




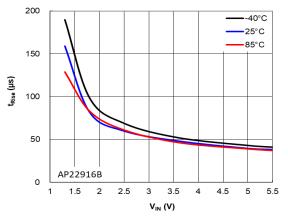


Output Turn-off Time vs. Input voltage





Output Rise Time vs. Input voltage

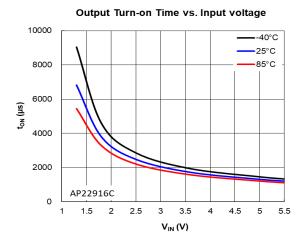


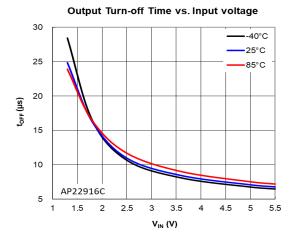
15 -40°C 25°C 12 85°C t_{Fall} (µs) 9 6 3 AP22916B 0 1.5 2 2.5 3 3.5 4 4.5 5 5.5 1 V_{IN} (V)

Output Fall Time vs. Input voltage

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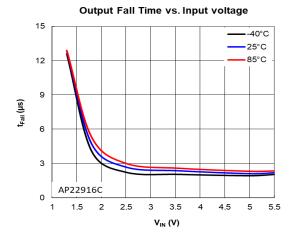






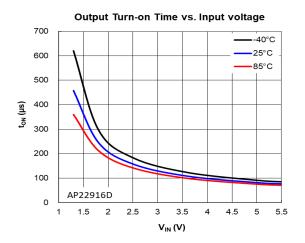
Slew Rate vs. Input voltage 7 -40°C 6 25°C 85°C 5 SR_{ov} (mV/µs) 4 з 2 1 AP22916C 0 1 1.5 2 2.5 3 3.5 4 4.5 5 5.5 V_{IN} (V)

4000 **-**-40°C 25°C 3500 -85°C 3000 t_{Rise} (µs) 2500 2000 1500 1000 AP22916C 500 1 1.5 2 2.5 3 3.5 4 4.5 5 5.5 V_{IN} (V)

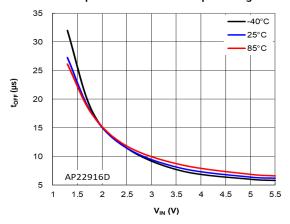


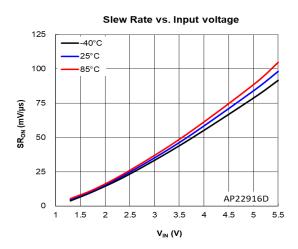
Output Rise Time vs. Input voltage



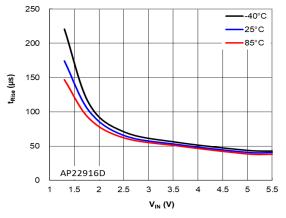


Output Turn-off Time vs. Input voltage





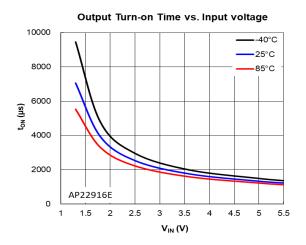
Output Rise Time vs. Input voltage

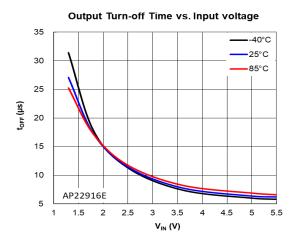


15 -40°C 25°C 12 85°C t_{Fall} (µs) 9 6 3 AP22916D 0 1 1.5 2 2.5 3 3.5 4 4.5 5 5.5 V_{IN} (V)

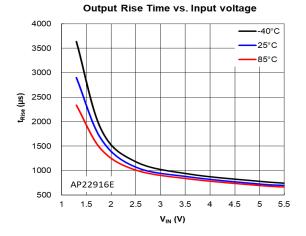
Output Fall Time vs. Input voltage

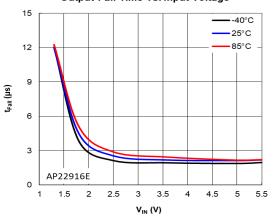






Slew Rate vs. Input voltage 7 -40°C 25°C 6 85°C 5 SR_{oN} (mV/µs) 4 з 2 1 AP22916E 0 1 1.5 2 2.5 3 3.5 4 4.5 5 5.5



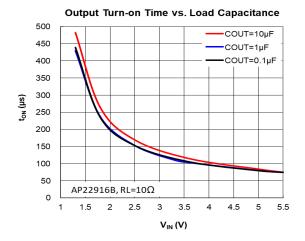


Output Fall Time vs. Input voltage

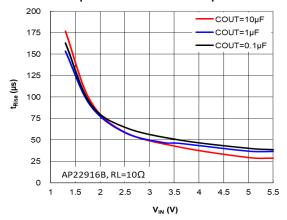
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Output Rise Time vs. Load Capacitance

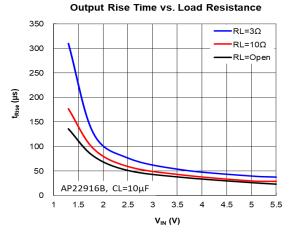


Slew Rate vs. Load Capacitance 150 COUT=10µF COUT=1µF 125 COUT=0.1µF 100 SR_{ov} (mV/µs) 75 50 25 AP22916B, RL=10Ω 0 1.5 2 2.5 3 3.5 4 4.5 5 5.5 1 V_{IN} (V)

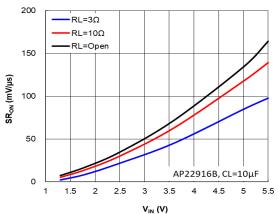
700 RL=3Ω RL=10Ω 600 RL=Open 500 t_{on} (µs) 400 300 200 100 AP22916B, CL=10µF 0 1.5 2 2.5 1 з 3.5 4 4.5 5 5.5

V_{IN} (V)

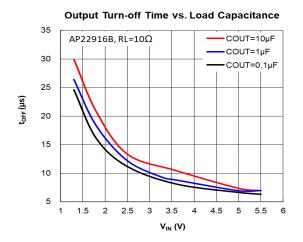
Output Turn-on Time vs. Load Resistance



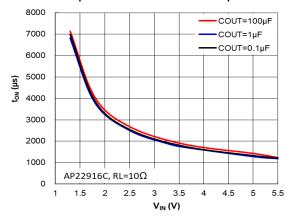
Slew Rate vs. Load Resistance

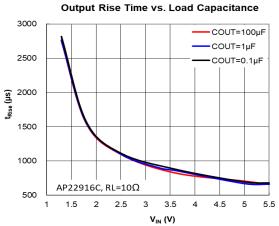






Output Turn-on Time vs. Load Capacitance





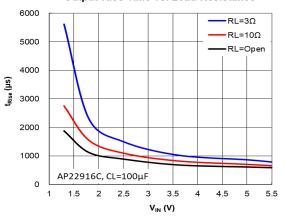
120 AP22916B, CL=10µF -RL=3Ω RL=10Ω 100 -RL=Open 80 t_{oFF} (µs) 60 40 20 0 2 2.5 3 4 4.5 5 5.5 1 1.5 3.5 V_{IN} (V)

Output Turn-off Time vs. Load Resistance

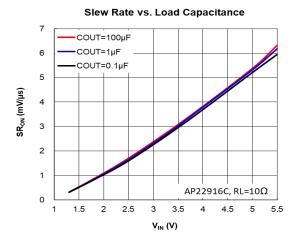
11000 RL=3Ω RL=10Ω 9000 RL=Open t_{oN} (µs) 7000 5000 3000 AP22916C, CL=100µF 1000 2.5 1.5 2 3 3.5 4 4.5 5 5.5 1 V_{IN} (V)

Output Turn-on Time vs. Load Resistance

Output Rise Time vs. Load Resistance

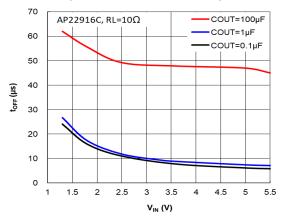




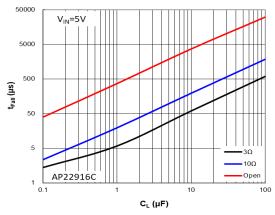


Slew Rate vs. Load Resistance 7 RL=3Ω RL=10Ω 6 RL=Open 5 SR_{ov} (mV/µs) 4 3 2 1 AP22916C, CL=100µF 0 1.5 2 2.5 3 3.5 4 4.5 5 5.5 1 V_{IN} (V)

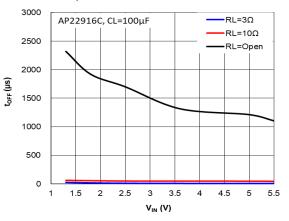
Output Turn-off Time vs. Load Capacitance



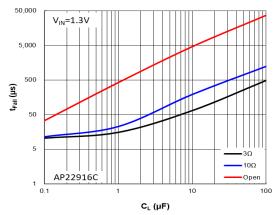




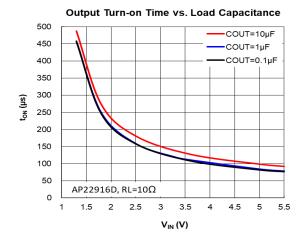
Output Turn-off Time vs. Load Resistance



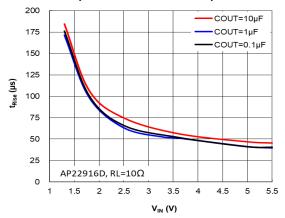
Output Fall Time vs. Input voltage





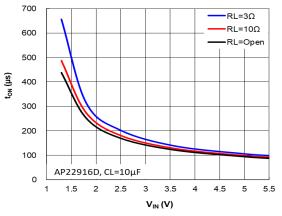


Output Rise Time vs. Load Capacitance

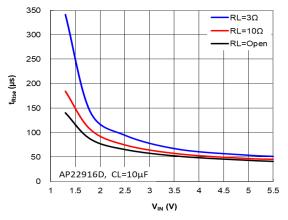


Slew Rate vs. Load Capacitance 125 COUT=10µF COUT=1µF 100 COUT=0.1µF SR_{ov} (mV/µs) 75 50 25 AP22916D. RL=10Ω 0 2.5 5 1.5 2 3 3.5 4 4.5 5.5 1 V_{IN} (V)

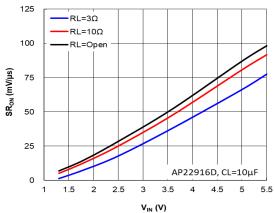
Output Turn-on Time vs. Load Resistance



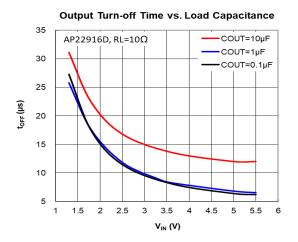
Output Rise Time vs. Load Resistance



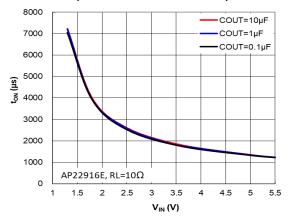
Slew Rate vs. Load Resistance



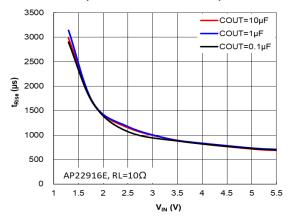




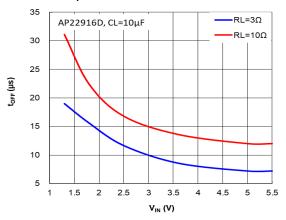
Output Turn-on Time vs. Load Capacitance



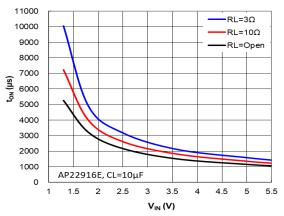
Output Rise Time vs. Load Capacitance



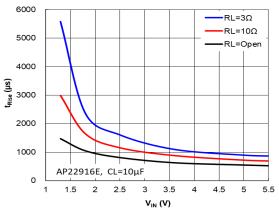
Output Turn-off Time vs. Load Resistance



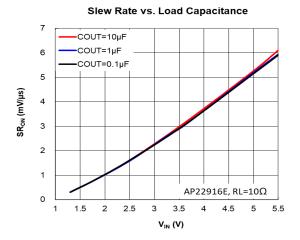
Output Turn-on Time vs. Load Resistance



Output Rise Time vs. Load Resistance

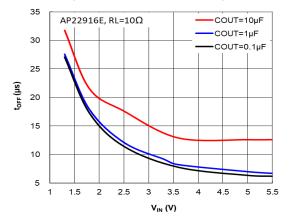




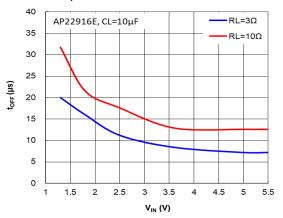


Slew Rate vs. Load Resistance 8 RL=3Ω 7 RL=10Ω RL=Open 6 5 SR_{ov} (mV/µs) 4 3 2 1 AP22916E, CL=10μF 0 1 1.5 2 2.5 3 3.5 4 4.5 5 5.5 V_{IN} (V)

Output Turn-off Time vs. Load Capacitance

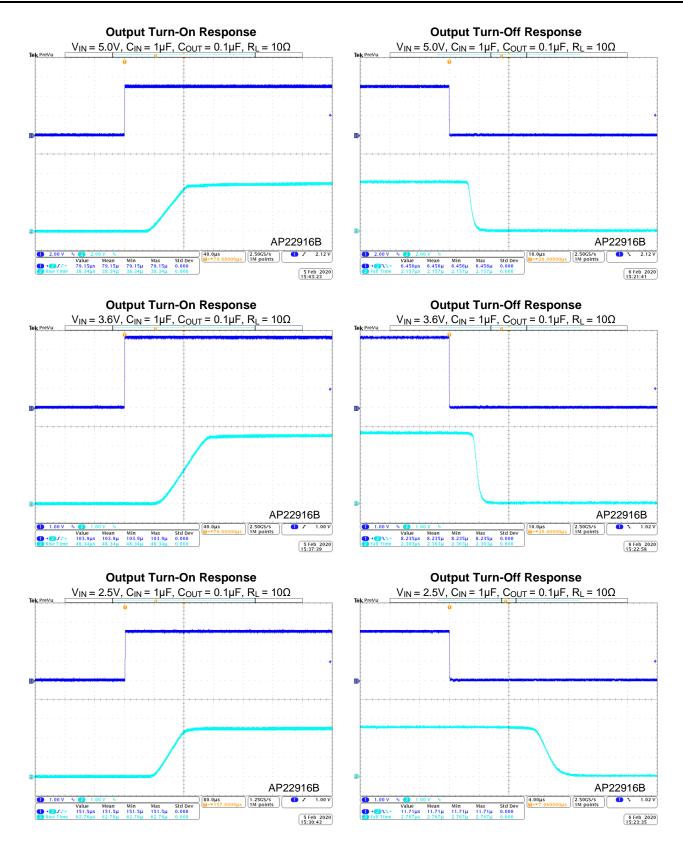


Output Turn-off Time vs. Load Resistance



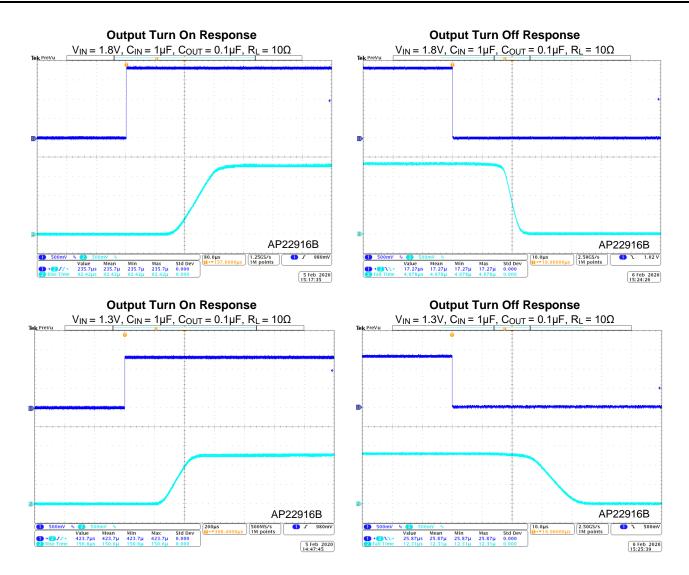


Typical Performance Characteristics (C_{IN} = 1µF, C_{OUT} = 0.1µF, unless otherwise specified.)



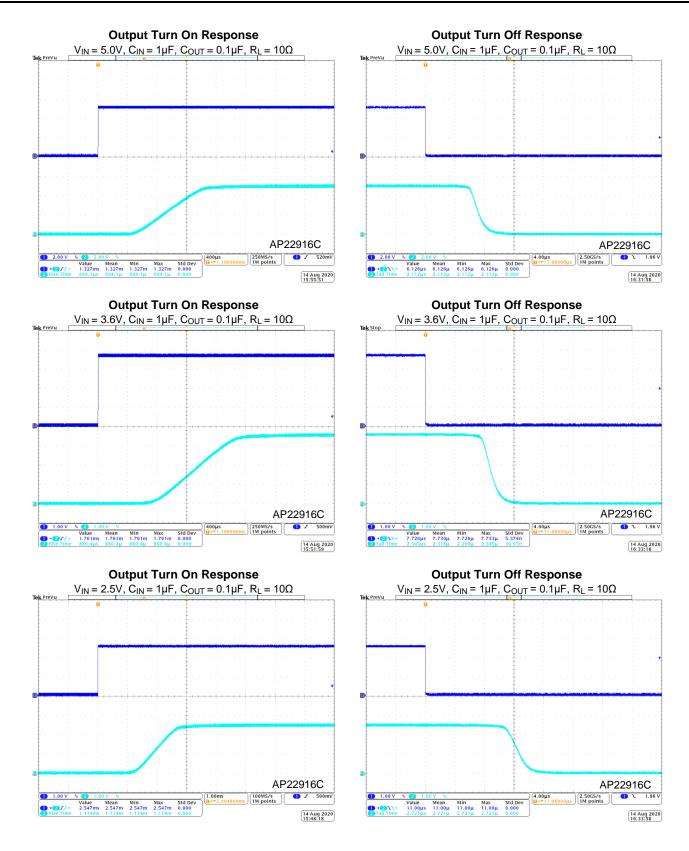


Typical Performance Characteristics ($C_{IN} = 1\mu F$, $C_{OUT} = 0.1\mu F$, unless otherwise specified.) (continued)



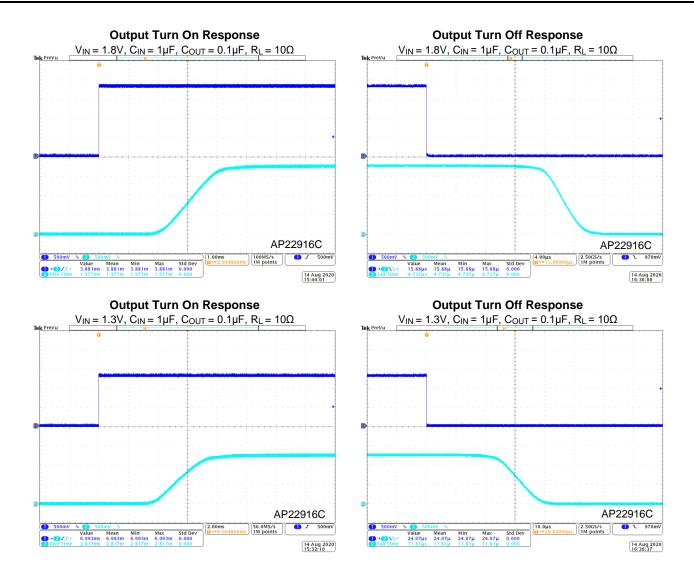


Typical Performance Characteristics (C_{IN} = 1µF, C_{OUT} = 0.1µF, unless otherwise specified.) (continued)



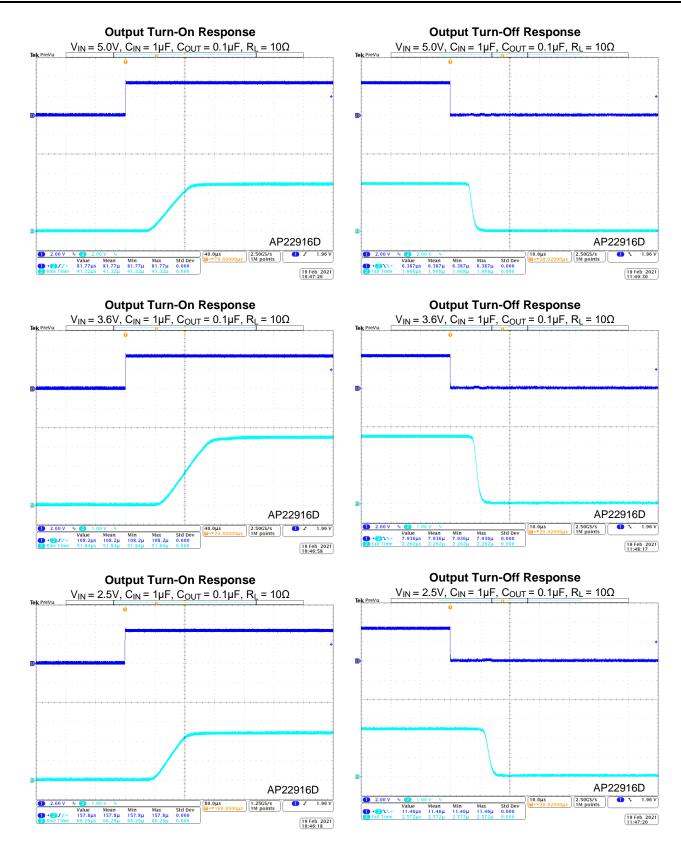


Typical Performance Characteristics (C_{IN} = 1µF, C_{OUT} = 0.1µF, unless otherwise specified.) (continued)



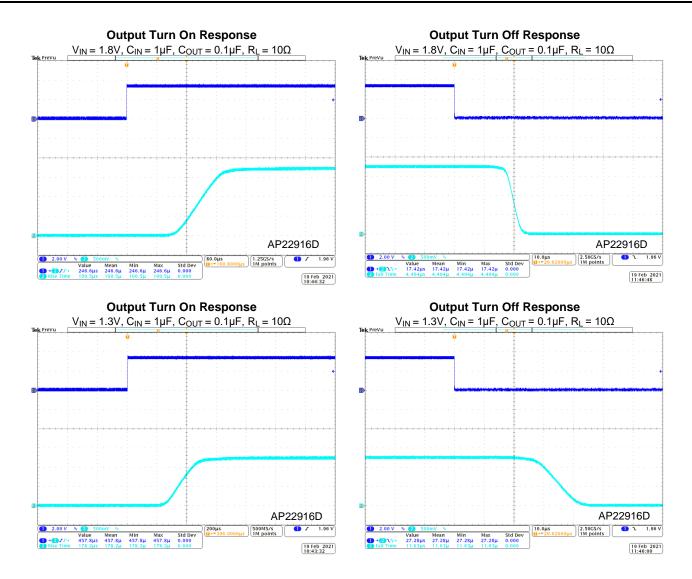


Typical Performance Characteristics ($C_{IN} = 1\mu F$, $C_{OUT} = 0.1\mu F$, unless otherwise specified.) (continued)



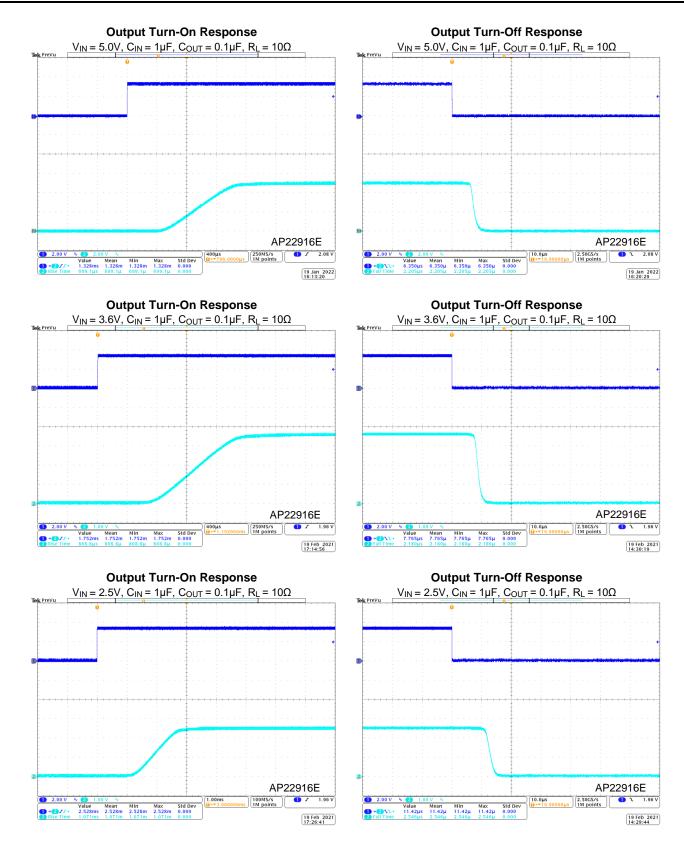


Typical Performance Characteristics (C_{IN} = 1µF, C_{OUT} = 0.1µF, unless otherwise specified.) (continued)



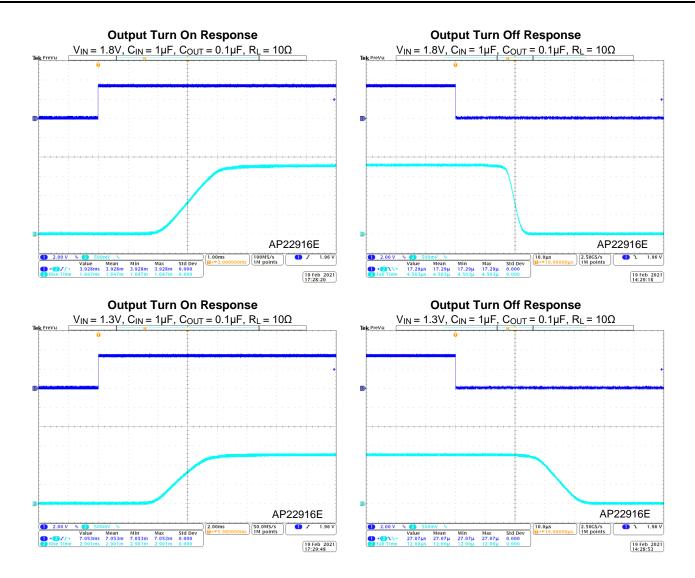


Typical Performance Characteristics ($C_{IN} = 1\mu F$, $C_{OUT} = 0.1\mu F$, unless otherwise specified.) (continued)





Typical Performance Characteristics (C_{IN} = 1µF, C_{OUT} = 0.1µF, unless otherwise specified.) (continued)





Input Capacitor

A 1 μ F capacitor is recommended to connect between the V_{IN} and GND pins to decouple input power supply glitch and noise. The input capacitor has no specific type or ESR (equivalent series resistance) requirement. However, for higher current applications, ceramic capacitors are recommended due to their capability to withstand input current surges from low impedance sources, such as batteries in portable applications. This input capacitor must be located as close as possible to the device to assure input stability and less noise. For PCB layout, a wide copper trace is required for both V_{IN} and GND.

Output Capacitor

A 0.1μ F to 1μ F capacitor is recommended to connect between the V_{OUT} and GND pins to stabilize and accommodate load transient condition. The output capacitor has no specific type or ESR requirement. The amount of capacitance may be increased without limit. For PCB layout, the output capacitor must be placed as close as possible to the V_{OUT} and GND pins, and the traces must be kept as short as possible.

Enable/Shutdown Operation

The AP22916B/C is turned on by setting the ON pin high, and is turned off by pulling it low. To ensure proper operation, the signal source used to drive the ON pin must be able to swing above and below the specified turn-on/off voltage thresholds listed in the *Electrical Characteristics* section under V_{IL} and V_{IH} .

True Reverse Current Blocking

An internal reverse voltage comparator disables the power-switch when the output voltage (V_{OUT}) is driven higher than the input voltage (V_{IN}), by V_{RCB} , to quickly (10µs typ.) stop the flow of current towards the input side of the switch.

Reverse current protection is always active, even when the power switch is disabled. Additionally, undervoltage lockout (UVLO) protection turns the switch off if the input voltage is too low.

Discharge Operation

The AP22916/C offers a discharge option that helps to discharge the output charge when disabled.

Power Dissipation

The maximum IC junction temperature should be restricted to +125°C under normal operating conditions. The device power dissipation and proper sizing of the thermal plane are critical to avoid thermal shutdown and ensure reliable operation. Power dissipation of the device depends on input voltage and load conditions, and can be calculated by:

$$P_{\rm D} = I_{\rm OUT}^2 x R_{\rm DSON}$$
 (1)

However, the maximum power dissipation that can be handled by the device depends on the maximum junction to ambient thermal resistance, maximum ambient temperature, and maximum device junction temperature, which can be approximated by the equation below:

$$\mathsf{P}_{\mathsf{D}(\mathsf{MAX})} = \frac{(125^{\circ}\mathsf{C} - \mathsf{T}_{\mathsf{A}})}{\theta_{\mathsf{J}\mathsf{A}}} \tag{2}$$

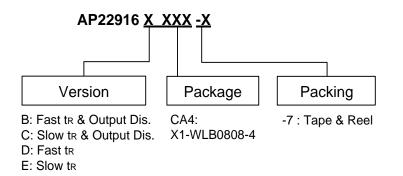
Layout Guideline

Good PCB layout is important for improving the thermal performance of the device. All trace lengths should be kept as short as possible. The input (V_{IN}) and output (V_{OUT}) PCB traces should be as wide as possible to reduce stray impedance.

Use a ground plane to enhance the power dissipation capability of the device if applicable. Place input and output capacitors close to the device to minimize the effects of parasitic inductance.



Ordering Information



Part Number	Baakaga Cada	Baakaga	7" Tape a	and Reel
Fait Number	Package Code	Package	Quantity	Part Number Suffix
AP22916BCA4-7	CA4	X1-WLB0808-4	3,000/Tape & Reel	-7
AP22916CCA4-7	CA4	X1-WLB0808-4	3,000/Tape & Reel	-7
AP22916DCA4-7	CA4	X1-WLB0808-4	3,000/Tape & Reel	-7
AP22916ECA4-7	CA4	X1-WLB0808-4	3,000/Tape & Reel	-7

Marking Information

(Top View)



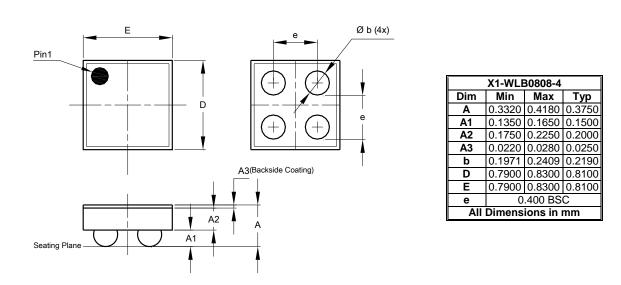
X : Identification Code Y : Year : 0~9 W : Week : A~Z : 1~26 week; a~z : 27~52 week; z represents 52 and 53 week

Part Number	Package	Identification Code
AP22916BCA4-7	X1-WLB0808-4	5
AP22916CCA4-7	X1-WLB0808-4	6
AP22916DCA4-7	X1-WLB0808-4	7
AP22916ECA4-7	X1-WLB0808-4	8



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

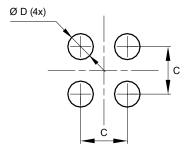


X1-WLB0808-4

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

X1-WLB0808-4



Dimensions	Value (in mm)
С	0.4000
D	0.2190



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