



2 CHANNEL LOW CAPACITANCE TVS DIODE ARRAY

Product Summary

V _{BR (min)}	I _{PP (max)}	C _{T (typ)}
6V	1.5A	0.5pF

Description

This new generation TVS is designed to protect sensitive electronics from the damage due to ESD. The combination of small size and high ESD surge capability makes it ideal for use in portable applications such as cellular phones, digital cameras, and MP3 players.

Applications

- Cellular Handsets
- Portable Electronics
- Computers and Peripheral

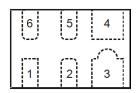
Features

- Low Profile Package (0.61mm max) and Ultra-Small PCB Footprint Area (1.68 * 1.08mm max) Suitable for Compact Portable Electronics
- Provides ESD Protection per IEC 61000-4-2 Standard:
 Air ±15kV, Contact ±15kV
- 2 Channels of ESD Protection
- Low Channel Input Capacitance
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

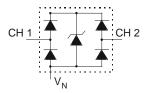
Mechanical Data

- Case: U-DFN1610-6
- Case Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: NiPdAu over Copper Leadframe. Solderable per MIL-STD-202, Method 208
- Weight: 0.003 grams (Approximate)

Pin#	Function
1, 2	Input
5, 6	No Connection
3, 4	Ground



Pin Description (Top View)



Device Schematic

Ordering Information (Note 4)

Product	Compliance	Marking	Reel Size (inches)	Tape Width (mm)	Quantity per Reel
D5V0F2U6LP-7	Standard	TG6	7	8	3,000/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information

TG6 YM TG6 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: B = 2014) M = Month (ex: 9 = September)

Date Code Key

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Year	201	4	2013		2014	20	15	2016		2017	2	2018
Code	В		С		D	E		F		G		Н
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D

D5V0F2U6LP Document number: DS36672 Rev. 3 - 2



Maximum Ratings (@ $T_A = +25^{\circ}C$, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	Conditions
Peak Pulse Current	I _{PP}	1.5	Α	8/20µs (Note 7)
ESD Protection – Contact Discharge	V _{ESD_Contact}	±15	kV	Standard IEC 61000-4-2
ESD Protection – Air Discharge	V _{ESD_Air}	±15	kV	Standard IEC 61000-4-2

Thermal Characteristics

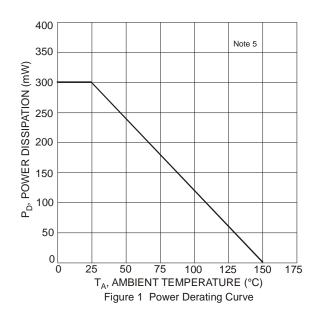
Characteristic	Symbol	Value	Unit
Power Dissipation (Note 5)	P_{D}	300	mW
Thermal Resistance, Junction to Ambient T _A = +25°C	$R_{ heta JA}$	417	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

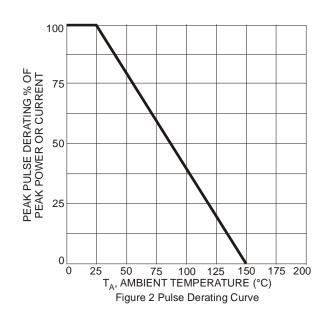
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Conditions
Reverse Standoff Voltage	V_{RWM}	_	_	5.5	V	_
Channel Leakage Current (Note 6)	I _R	_	_	100	nA	V _R = 5V, Any I/O to GND
Reverse breakdown voltage	V_{BR}	6.0	_	_	V	I _R = 1mA
Clamping Voltage, Positive Transients (Note 7)	Vc	_	10	12	V	$I_{PP} = 1A, t_p = 8/20 \mu s$
Channel Input Canacitanae (Note 9)		_	0.5	_	5E	$V_R = 0V$, $f = 1MHz$, Any I/O to GND
Channel Input Capacitance (Note 8)	C _T	_	0.4	0.65	pF	$V_R = 2.5V$, $f = 1MHz$, Any I/O to GND
Dynamic Resistance	R _{DYN}	_	0.9	_	Ω	$I_{PP} = 1A$, $t_p = 8/20 \mu s$

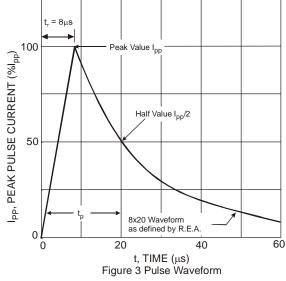
Notes:

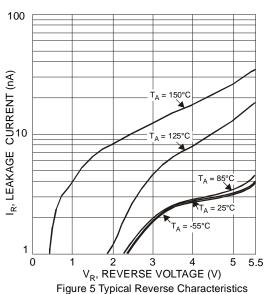
- 5. Device mounted on FR-4 PCB pad layout (2oz copper) as shown on Diodes, Inc. suggested pad layout AP02001, which can be found on our website at http://www.diodes.com.
- 6. Short duration pulse test used to minimize self-heating effect.
 - 7. Clamping voltage value is based on an 8x20µs peak pulse current (Ipp) waveform.
- 8. Measured from any I/O to GND.
- 9. For information on the impact of Diodes' USB 2.0 compatible ESD protectors on signal integrity including eye diagram plots, please refer to AN77 at the following URL: http://www.diodes.com/destools/appnote_dnote.html.

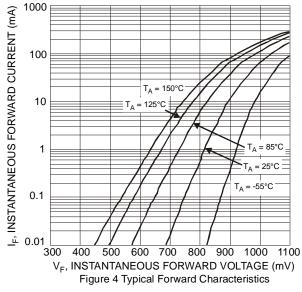












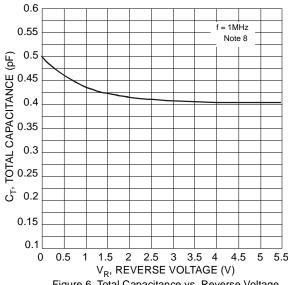
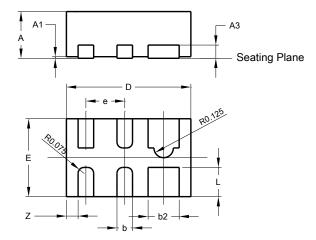


Figure 6 Total Capacitance vs. Reverse Voltage

Package Outline Dimensions

Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.

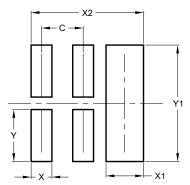


	U-DFN1610-6							
Dim	Min	Max	Тур					
Α	0.545	0.605	0.575					
A1	0.00	0.05	0.03					
A3	-	-	0.13					
b	0.15	0.25	0.20					
b2	0.35	0.45	0.40					
D	1.550	1.675	1.600					
Е	0.950	1.075	1.000					
е		0.50 B	SC					
L	0.325	0.425	0.375					
Z	-	-	0.150					
All	All Dimensions in mm							



Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
С	0.500
Х	0.250
X1	0.450
X2	1.350
Υ	0.625
Y1	1.400

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