





LOW CAPACITANCE UNIDIRECTIONAL TVS

Features

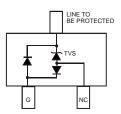
- 300 Watts Peak Pulse Power (tp = 8x20µs)
- Transient Protection for Data, Signal, and VCC Bus to IEC61000-4-2 Level 4 (ESD) and IEC 61000-4-4 (EFT)
- Low Capacitance, typ. 1.6pF
- Low Leakage Current
- Unidirectional Configuration
- Surface Mount Package Ideally Suited for Automated Insertion
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

Mechanical Data

- Case: SOT23
- Case Material: Molded Plastic. "Green" Molding Compound.
 UL Flammability Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish—Matte Tin Plated Leads, Solderable per MIL-STD-202, Method 208@3
- Weight: 0.008 grams (Approximate)



Top View



Device Schematic

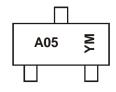
Ordering Information (Note 4)

Part Number	Case	Marking	Reel Size	Tape Width	Quantity per Reel
DLP05LC-7-F	SOT-23	A05	7"	8mm	3000

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information



A05 = Product Type Marking Code YM = Date Code Marking

Y = Year (ex: G = 2019)

M = Month (ex: 9 = September)

Date Code Key

Date Code Rey												
Year	2010	2011			2019	2020	2021	2022	2 20	023	2024	2025
Code	Х	Υ			G	Н	I	J		K	L	М
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	1	5	6	7	Ω	Q	0	N	D

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Maximum Ratings (@ $T_A = +25$ °C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Peak Pulse Power (tp = 8x20µs)	P_{pk}	300	W

Thermal Characteristics

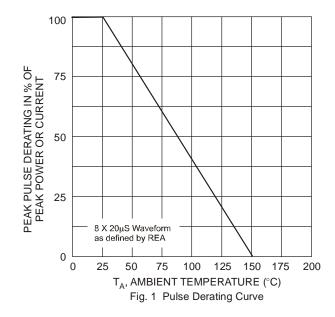
Characteristic	Symbol	Value	Unit
Typical Thermal Resistance, Junction to Ambient (Note 5)	$R_{\Theta JA}$	408	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

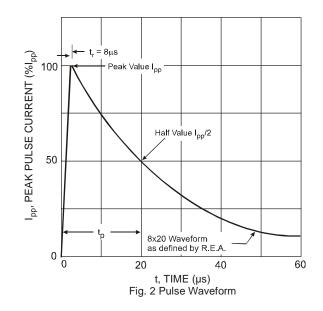
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Reverse Standoff Voltage	tandoff Vpp @ Iz		Test Current	Max. Reverse Leakage @ V _{RWM}	Max. Clamping Voltage @ I _{pp} = 1A (Note 8)	Typical Peak Pulse Current (Note 7)	Typical Total Capacitance (Note 6)
V _{RWM} (V)	Min (V)	Max (V)	I _T (mA)	I _R (μ A)	V _C (V)	(A)	(pF)
5	6.0	_	1.0	20	11.0	17	1.6

Notes:

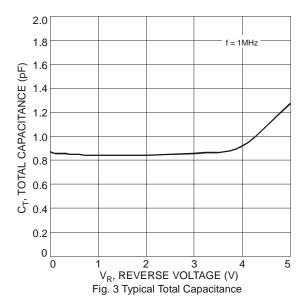
- 5. Device mounted on FR-4 PCB pad layout with 2oz Cu traces and with pad dimensions 1" x 1".
- 6. $V_R = 0V$, f = 1MHz.
- 7. $tp = 8x20\mu s$.
- 8. Clamping voltage value is based on an 8x20µs peak pulse current (Ipp) waveform.
 9. Product manufactured with Data Code V9 (week 33, 2008) and newer are built with Green Molding Compound. Product manufactured prior to Date Code V9 are built with Non-Green Molding Compound and may contain Halogens or Sb₂O₃ Fire Retardants.



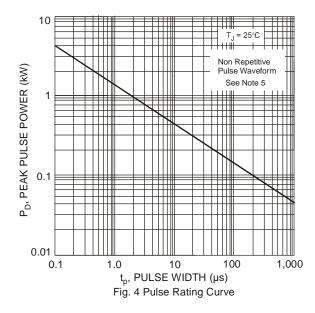


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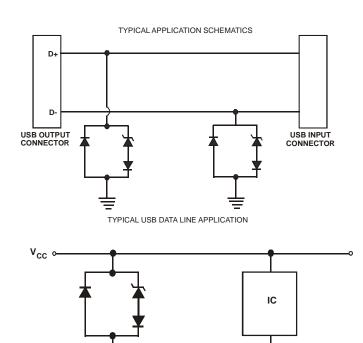




GROUND ↔



Typical Application Schemes

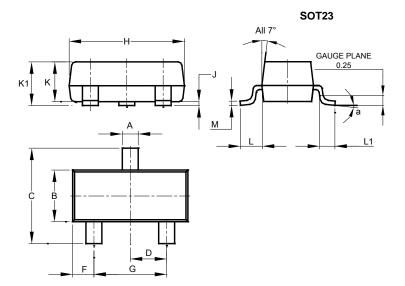


TYPICAL $V_{\rm CC}$ POWER LINE PROTECTION



Package Outline Dimensions

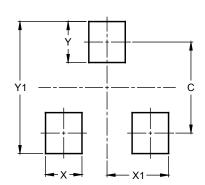
Please see http://www.diodes.com/package-outlines.html for the latest version.



SOT23						
Dim	Min	Max	Тур			
Α	0.37	0.51	0.40			
В	1.20	1.40	1.30			
С	2.30	2.50	2.40			
D	0.89	1.03	0.915			
F	0.45	0.60	0.535			
G	1.78	2.05	1.83			
Н	2.80	3.00	2.90			
J	0.013	0.10	0.05			
K	0.890	1.00	0.975			
K1	0.903	1.10	1.025			
L 0.45 0.61 0.55						
L1	0.25	0.55	0.40			
М	0.085	0.150	0.110			
а	0°	8°				
All Dimensions in mm						

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.



SOT23

Dimensions	Value (in mm)
C	2.0
Х	0.8
X1	1.35
Υ	0.9
Y1	2.9

May 2019



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