

## 1A SURFACE MOUNT SCHOTTKY BARRIER RECTIFIER

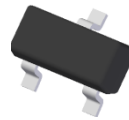
### Features

- Very Low Forward Voltage Drop
- High Conductance
- For Use in DC-DC Converter, PCMCIA, and Mobile Telecommunications Applications
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 and 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **The BAT1000Q is suitable for automotive applications requiring specific change control; this part is AEC-Q101 qualified, PPAP capable, and manufactured in IATF 16949 certified facilities.**

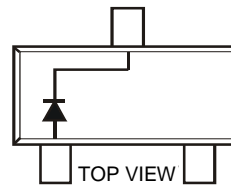
<https://www.diodes.com/quality/product-definitions/>

### Mechanical Data

- Case: SOT23
- Case Material: Molded Plastic. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Matte Tin Finish Annealed over Alloy 42 Leadframe (Lead Free Plating). Solderable per MIL-STD-202, Method 208 e3
- Polarity: See Diagram
- Weight: 0.008 grams (Approximate)



Top View



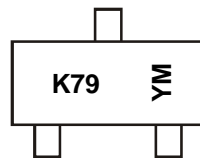
Schematic and Pin Configuration

### Ordering Information (Note 4)

Part Number	Compliance	Case	Packaging
BAT1000-7-F	Commercial	SOT23	3000/Tape & Reel
BAT1000Q-7-F	Automotive	SOT23	3000/Tape & Reel

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
  2. See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
  3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
  4. For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

### Marking Information



K79 = Product Type Marking Code  
 YM = Date Code Marking  
 Y = Year (ex: G = 2019)  
 M = Month (ex: 9 = September)

#### Date Code Key

Year	2002	2003	2004	....	2016	2017	2018	2019	2020	2021	2022
Code	N	P	R	....	D	E	F	G	H	I	J

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

**Maximum Ratings** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Single phase, half wave, 60Hz, resistive or inductive load.  
For capacitive load, derate current by 20%.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage	V <sub>RRM</sub>	40	V
Working Peak Reverse Voltage	V <sub>RWM</sub>		
DC Blocking Voltage	V <sub>R</sub>		
RMS Reverse Voltage	V <sub>R(RMS)</sub>	28	V
Average Rectified Current	I <sub>O</sub>	1.0	A
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load	I <sub>FSM</sub>	5.5	A

**Thermal Characteristics**

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 5)	P <sub>D</sub>	500	mW
Typical Thermal Resistance, Junction to Ambient Air (Note 5)	R <sub>θJA</sub>	200	°C/W
Operating Temperature Range	T <sub>J</sub>	-40 to +125	°C
Storage Temperature Range	T <sub>STG</sub>	-40 to +150	°C

**Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
Reverse Breakdown Voltage (Note 6)	V <sub>(BR)R</sub>	40	—	—	V	I <sub>R</sub> = 300μA
Forward Voltage	V <sub>F</sub>	—	225	270	mV	I <sub>F</sub> = 50mA
			235	290		I <sub>F</sub> = 100mA
			290	340		I <sub>F</sub> = 250mA
			340	400		I <sub>F</sub> = 500mA
			390	450		I <sub>F</sub> = 750mA
			420	500		I <sub>F</sub> = 1000mA
			475	600		I <sub>F</sub> = 1500mA
Reverse Current (Note 6)	I <sub>R</sub>	—	25	100	μA	V <sub>R</sub> = 30V
Total Capacitance	C <sub>T</sub>	—	175	—	pF	V <sub>R</sub> = 0V, f = 1.0MHz
		—	25	—	pF	V <sub>R</sub> = 25V, f = 1.0MHz
Reverse Recovery Time	t <sub>RR</sub>	—	12	—	ns	I <sub>F</sub> = 10mA, I <sub>RR</sub> = 0.1*I <sub>R</sub>

Notes: 5. Part mounted on FR-4 board with recommended pad layout, which can be found on our website at <http://www.diodes.com/package-outlines.html>.  
6. Short duration pulse test used to minimize self-heating effect.

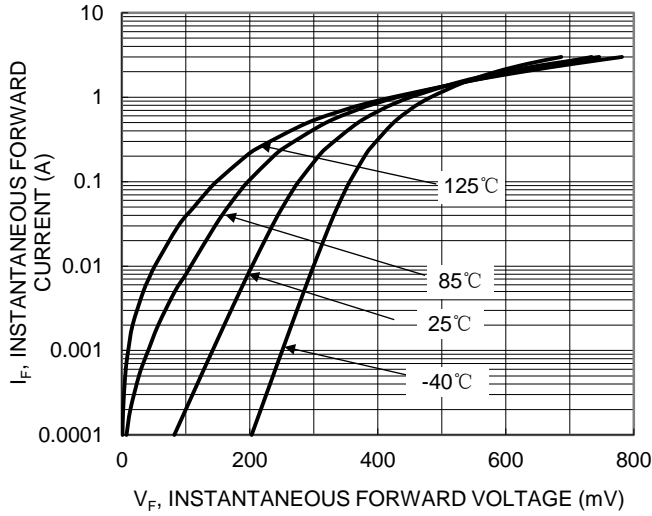


Fig.1 Typical Forward Characteristics

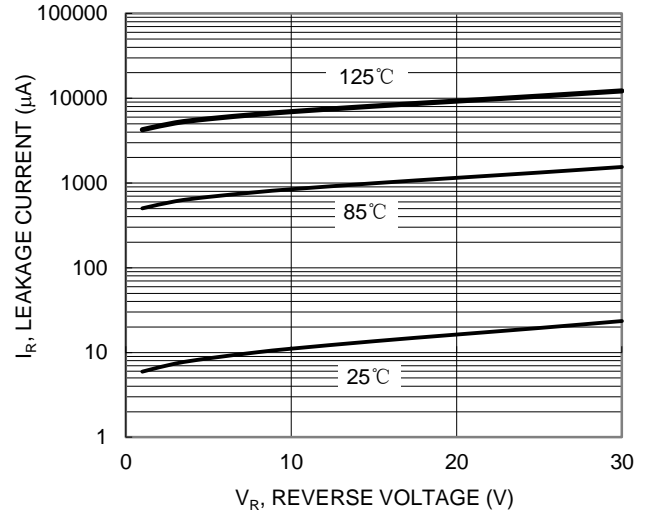


Fig.2 Typical Reverse Characteristics

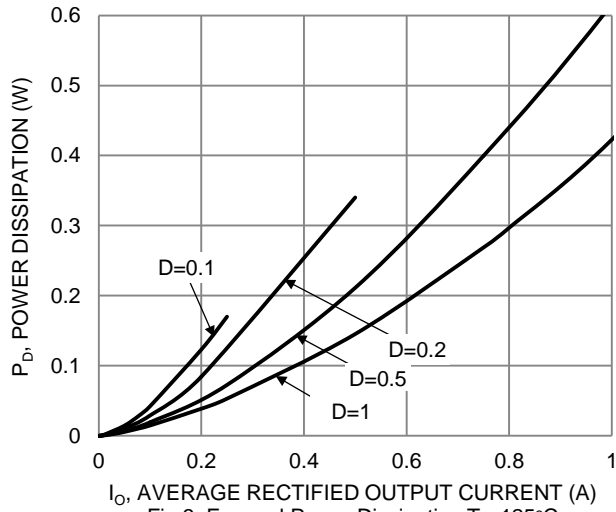


Fig.3. Forward Power Dissipation  $T_J=125^\circ\text{C}$

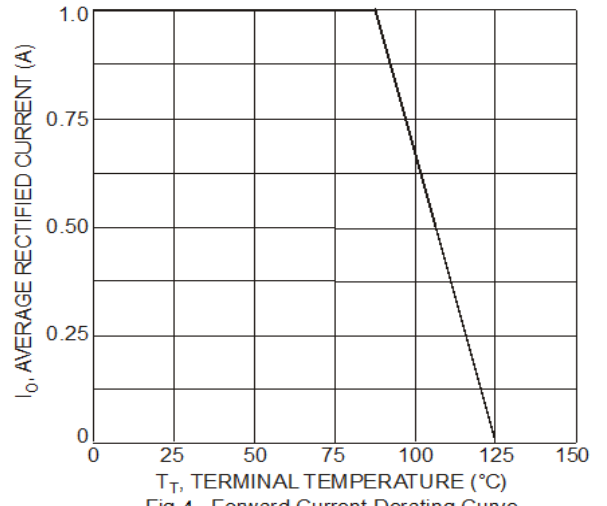
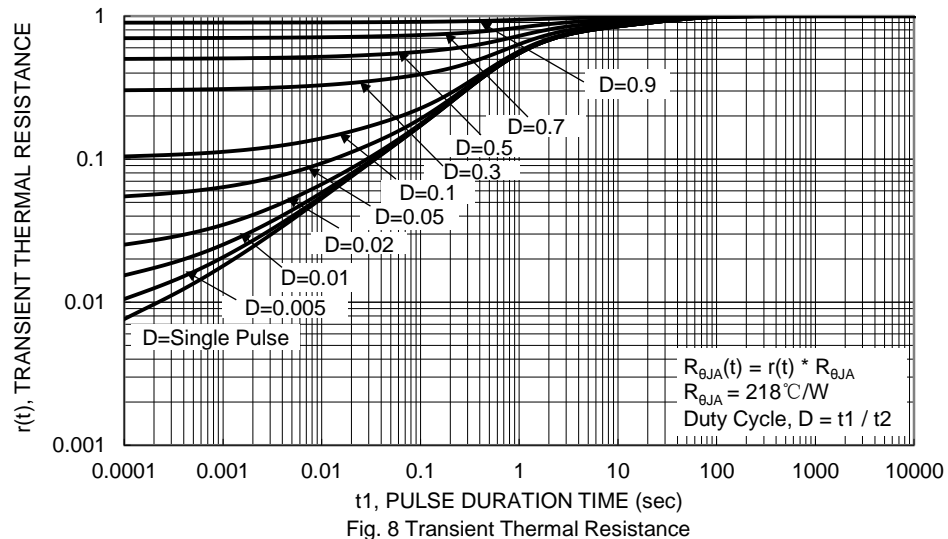
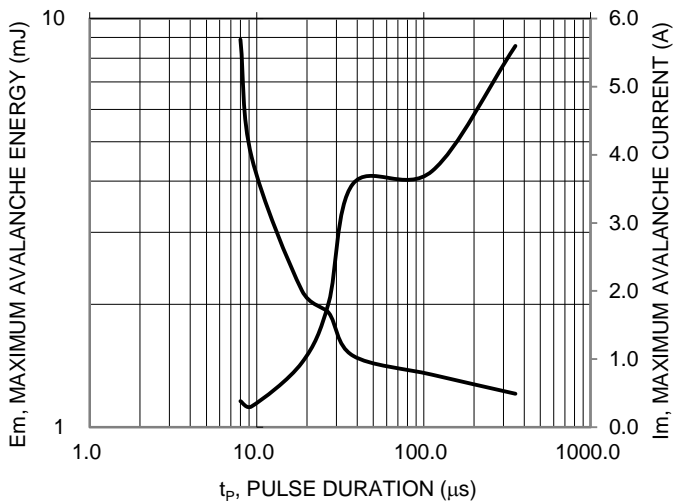
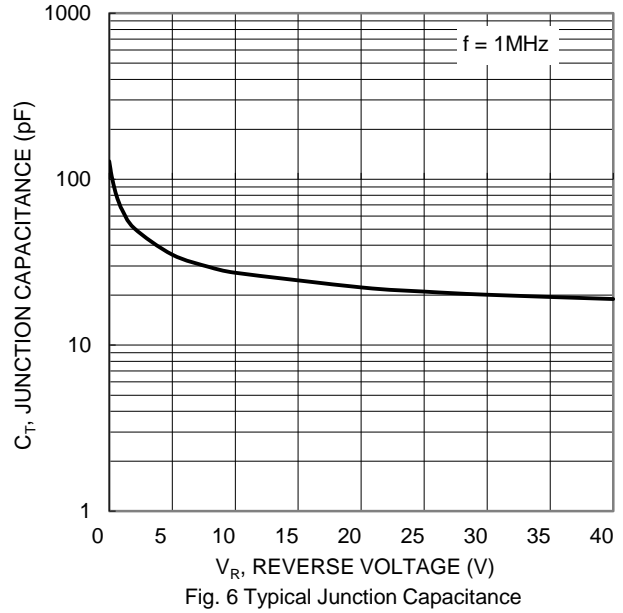
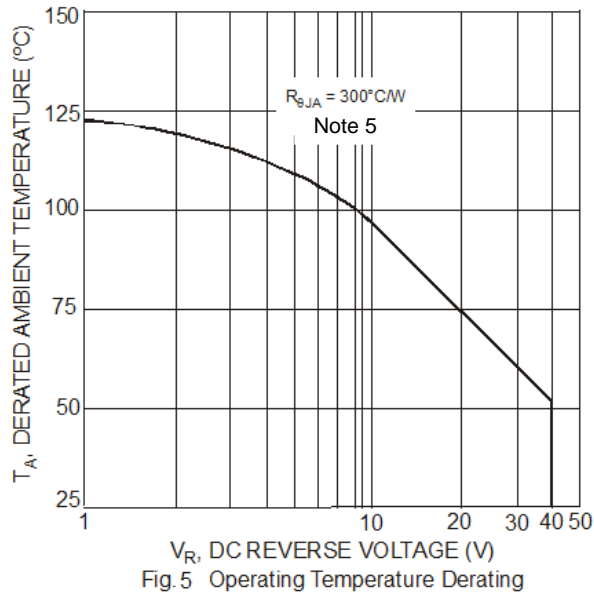


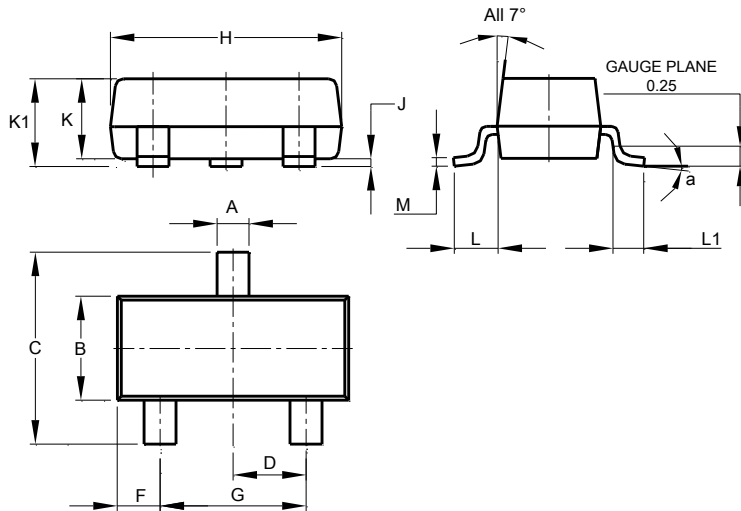
Fig.4 Forward Current Derating Curve



**Package Outline Dimensions**

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

**SOT23**

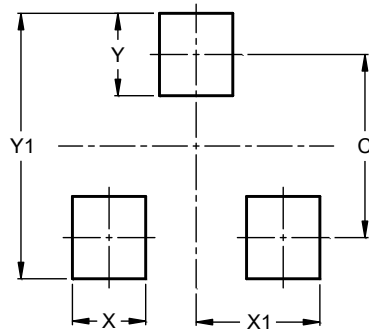


SOT23			
Dim	Min	Max	Typ
A	0.37	0.51	0.40
B	1.20	1.40	1.30
C	2.30	2.50	2.40
D	0.89	1.03	0.915
F	0.45	0.60	0.535
G	1.78	2.05	1.83
H	2.80	3.00	2.90
J	0.013	0.10	0.05
K	0.890	1.00	0.975
K1	0.903	1.10	1.025
L	0.45	0.61	0.55
L1	0.25	0.55	0.40
M	0.085	0.150	0.110
a	0°	8°	--
All Dimensions in mm			

**Suggested Pad Layout**

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

**SOT23**



Dimensions	Value (in mm)
C	2.0
X	0.8
X1	1.35
Y	0.9
Y1	2.9

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