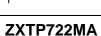




A Product Line of Diodes Incorporated



70V PNP LOW SATURATION TRANSISTOR

Features and Benefits

- BV_{CEO} > -70V
- I_C = -2.5A Continuous Collector Current
- Low Saturation Voltage (-220mV max @ -1A)
- $R_{SAT} = 117 \text{ m}\Omega$ for a low equivalent On-Resistance
- hFE specified up to -3A for high current gain hold up
- Low profile 0.6mm high package for thin applications
- R_{0JA} efficient, 60% lower than SOT23
- 4mm² footprint, 50% smaller than SOT23
- Lead-Free, RoHS Compliant (Note 1)
- Halogen and Antimony Free. "Green" Device (Note 2)
- Qualified to AEC-Q101 Standards for High Reliability

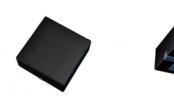
DFN2020B-3

Mechanical Data

- Case: DFN2020B-3
- Case material: Molded Plastic. "Green" Molding Compound.
- Terminals: Pre-Plated NiPdAu leadframe.
- Nominal package height: 0.6mm
- UL Flammability Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Weight: 0.01 grams (approximate)

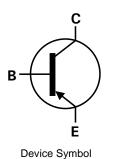
Applications

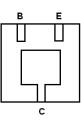
- MOSFET Gate Driving
- DC-DC Converters
- Charging Circuits
- Power switches
- Motor control



Top View

Bottom View





Bottom View Pin-Out

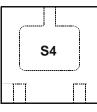
Ordering Information

Product	Marking	Reel size (inches)	Tape width (mm)	Quantity per reel
ZXTP722MATA	S4	7	8	3000

Notes: 1. No purposefully added lead.

2. Diodes Inc's "Green" policy can be found on our website at http://www.diodes.com

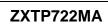
Marking Information



S4 = Product Type Marking code

Top View





Maximum Ratings @T_A = 25°C unless otherwise specified

Characteristic		Symbol	Value	Unit	
Collector-Base Voltage		V _{CBO}	-70		
Collector-Emitter Voltage		V _{CEO}	-70	V	
Emitter-Base Voltage		V _{EBO}	-7		
Peak Pulse Current		I _{CM}	-3		
Continuous Collector Current	(Note 3)		-2.5	٨	
	(Note 4)	IC	-2.7	~	
Base Current		IB	-1		

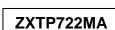
Thermal Characteristics @T_A = 25°C unless otherwise specified

Characteristic		Symbol	Value	Unit
Power Dissipation	(Note 3)		1.5 12	W
Linear Derating Factor	(Note 4)		2.45 19.6	mW/°C
Thermal Resistance, Junction to Ambient	(Note 3) (Note 4)	R _{θJA}	<u>83</u> 51	°C/W
Thermal Resistance, Junction to Lead	(Note 5)	R _{θJL}	16.8	
Operating and Storage Temperature Range		TJ, TSTG	-55 to +150	°C

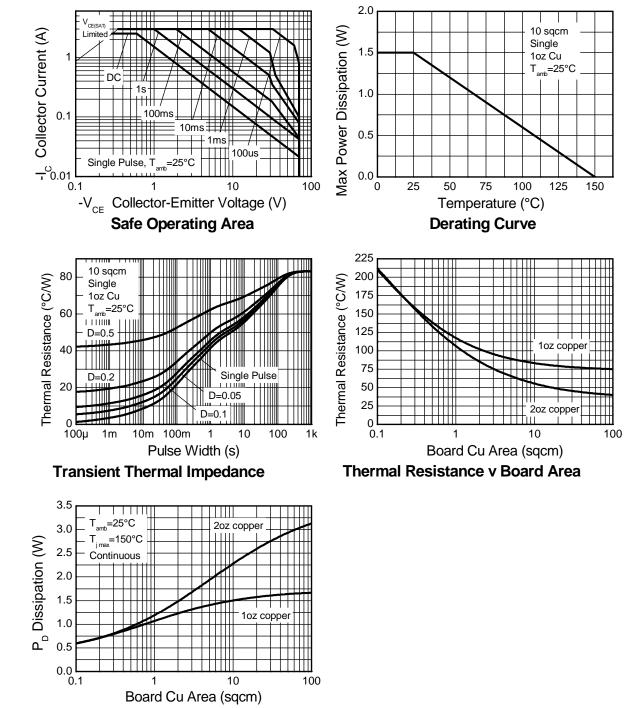
Notes: 3. For a device surface mounted on 31mm x 31mm (10cm²) FR4 PCB with high coverage of single sided 1oz copper, in still air conditions; the device is measured when operating in a steady-state condition. The entire exposed collector pad is attached to the heatsink.

Same as note (3), except the device is measured at t ≤ 5 sec.
For a single device, thermal resistance from junction to solder-point (at the end of the drain lead).



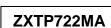


Thermal Characteristics



Power Dissipation v Board Area



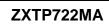


Electrical Characteristics @T_A = 25°C unless otherwise specified

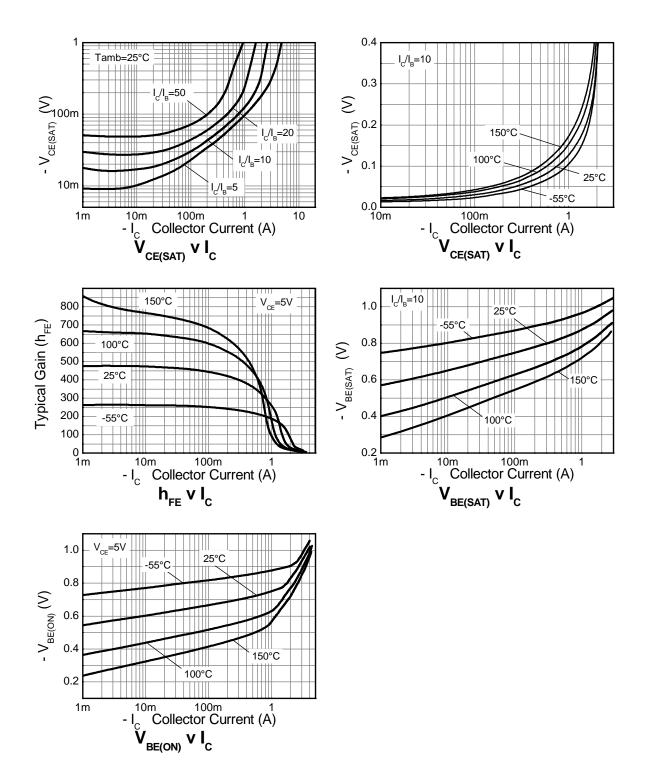
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Collector-Base Breakdown Voltage	BV _{CBO}	-70	-150	-	V	I _C = -100μA
Collector-Emitter Breakdown Voltage (Note 6)	BV _{CEO}	-70	-125	-	V	I _C = -10mA
Emitter-Base Breakdown Voltage	BV _{EBO}	-7	-8.5	-	V	I _E = -100μA
Collector Cutoff Current	I _{CBO}	-	-	-100	nA	V _{CB} = -55V
Emitter Cutoff Current	I _{EBO}	-	-	-100	. nA	V _{EB} = -6V
Collector Emitter Cutoff Current	I _{CES}	-	-	-100	nA	$V_{CES} = -55V$
Static Forward Current Transfer Ratio (Note 6)	h _{FE}	300 300 175 40	470 450 275 60 10		-	$\begin{split} I_{C} &= -10 \text{mA}, \ V_{CE} &= -5 \text{V} \\ I_{C} &= -100 \text{mA}, \ V_{CE} &= -5 \text{V} \\ I_{C} &= -1A, \ V_{CE} &= -5 \text{V} \\ I_{C} &= -1.5A, \ V_{CE} &= -5 \text{V} \\ I_{C} &= -3A, \ V_{CE} &= -5 \text{V} \end{split}$
Collector-Emitter Saturation Voltage (Note 6)	V _{CE(sat)}		-35 -135 -140 -175	-50 -200 -220 -270	mV	$\begin{split} I_{C} &= -0.1A, \ I_{B} &= -10 \text{mA} \\ I_{C} &= -0.5A, \ I_{B} &= -20 \text{mA} \\ I_{C} &= -1A, \ I_{B} &= -100 \text{mA} \\ I_{C} &= -1.5A, \ I_{B} &= -200 \text{mA} \end{split}$
Base-Emitter Turn-On Voltage (Note 6)	V _{BE(on)}	-	-0.78	-1.00	V	I _C = -1.5A, V _{CE} = -5V
Base-Emitter Saturation Voltage (Note 6)	V _{BE(sat)}	-	-0.94	-1.05	V	I _C = -1.5A, I _B = -200mA
Output Capacitance	C _{obo}	-	14	20	pF	$V_{CB} = -10V. f = 1MHz$
Transition Frequency	f _T	150	180	-	MHz	$V_{CE} = -10V, I_C = -50mA,$ f = 100MHz
Turn-On Time	t _{on}	-	40	-	ns	$V_{CC} = -50V, I_C = -1A$
Turn-Off Time	t _{off}	-	700	-	ns	$I_{B1} = I_{B2} = -50 \text{mA}$

Notes: 6. Measured under pulsed conditions. Pulse width \leq 300 µs. Duty cycle \leq 2%.





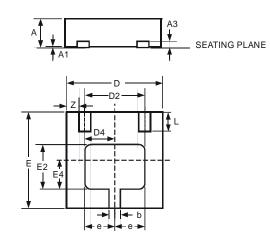
Typical Electrical Characteristics





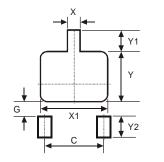


Package Outline Dimensions



DFN2020B-3					
Dim	Min	Max	Тур		
Α	0.57	0.63	0.60		
A1	0	0.05	0.02		
A3			0.152		
b	0.20	0.30	0.25		
D	1.95	2.075	2.00		
D2	1.22	1.42	1.32		
D4	0.56	0.76	0.66		
е			0.65		
Е	1.95	2.075	2.00		
E2	0.79	0.99	0.89		
E4	0.48	0.68	0.58		
L	0.25	0.35	0.30		
Ζ			0.225		
All Dimensions in mm					

Suggested Pad Layout



Dimensions	Value (in mm)		
С	1.30		
G	0.24		
Х	0.35		
X1	1.52		
Y	1.09		
Y1	0.47		
Y2	0.50		

ZXTP722MA Document Number DS31885 Rev. 5 - 2



ZXTP722MA

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