

N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV _{DSS}	R _{DS(ON)} max	I _D max T _A = +25°C	
201/	1.5Ω @ $V_{GS} = 4.0V$	0.54	
30V	2.0Ω @ V _{GS} = 2.5V	0.5A	

Features and Benefits

- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- **ESD Protected Gate**
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

Description and Applications

This MOSFET is designed to minimize the on-state resistance (RDS(ON)) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

- Motor Control
- **Power Management Functions**
- Backlighting

Mechanical Data

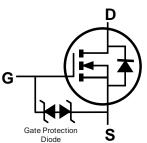
- Case: SOT23
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 @3
- Terminals Connections: See Diagram Below
- Weight: 0.009 grams (Approximate)



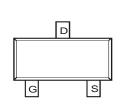


SOT23









Top View

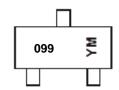
Ordering Information (Note 4)

Part Number	Case	Packaging
DMN31D5L-7	SOT23	3000/Tape & Reel
DMN31D5L-13	SOT23	10000/Tape & Reel

Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.

- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + CI) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/

Marking Information



099 = Product Type Marking Code YM = Date Code Marking Y or \overline{Y} = Year (ex: F = $2\overline{0}18$) M = Month (ex: 9 = September)

Date Code Key

Year	2018	20	19	2020	20)21	2022	2	2023	2024		2025
Code	F	(ì	Н		l	J		K	L		М
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D

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Maximum Ratings ($@T_A = +25^{\circ}C$, unless otherwise specified.)

Characteristic	Symbol	Value	Unit		
Drain-Source Voltage			V_{DSS}	30	V
Gate-Source Voltage			V_{GSS}	±20	V
Continuous Drain Current (Note 6) Vgs = 4V $\begin{array}{c c} Steady & T_A = +25^{\circ}C \\ State & T_A = +75^{\circ}C \end{array}$			I _D	0.5 0.4	А
Maximum Continuous Body Diode Forward Current	(Note 5)	I _S	0.3	Α	
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%	6)	I _{DM}	5	Α	

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)		P_{D}	350	mW
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	$R_{\theta JA}$	357	°C/W
Total Power Dissipation (Note 6)		P _D	520	mW
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	$R_{\theta JA}$	240	°C/W
Operating and Storage Temperature Range		T _{J,} T _{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

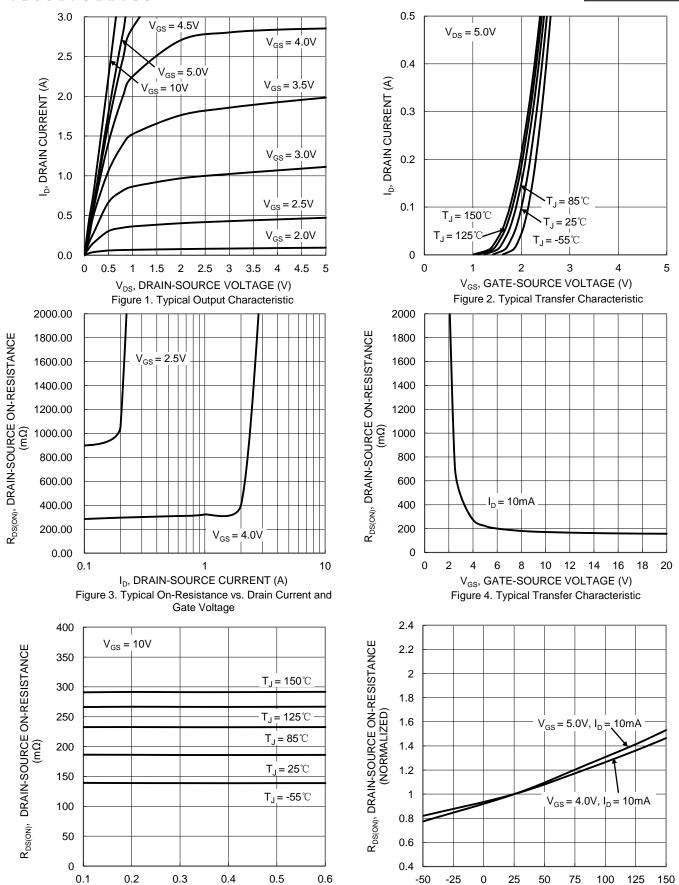
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BV _{DSS}	30	_	_	V	$V_{GS} = 0V, I_{D} = 100 \mu A$	
Zero Gate Voltage Drain Current @T _C = +25°C	I _{DSS}	_	_	1.0	μA	$V_{DS} = 30V, V_{GS} = 0V$	
Gate-Source Leakage	I _{GSS}	_	_	±10	μA	$V_{GS} = \pm 10V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)	•						
Gate Threshold Voltage	V _{GS(TH)}	0.8	_	1.6	V	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	
Static Drain-Source On-Resistance		_	_	1.5	Ω	$V_{GS} = 4.0V, I_D = 10mA$	
Static Drain-Source On-Resistance	R _{DS(ON)}	_	_	2.0	1 12	$V_{GS} = 2.5V, I_D = 10mA$	
Diode Forward Voltage		_	_	1.2	V	$V_{GS} = 0V, I_{S} = 10mA$	
DYNAMIC CHARACTERISTICS (Note 8)	•						
Input Capacitance		_	50	_	pF	1/ 45)/)/ 0)/	
Output Capacitance		_	12	_	pF	$V_{DS} = 15V, V_{GS} = 0V,$ - f = 1.0MHz	
Reverse Transfer Capacitance		_	10	_	pF	1 = 1.000112	
Total Gate Charge (V _{GS} = 4.5V)	Qg	_	0.5	_	nC		
Total Gate Charge (V _{GS} = 10V)	Qg	_	1.2	_	nC	$V_{GS} = 10V, V_{DS} = 10V,$	
Gate-Source Charge		_	0.2	_	nC	I _D = 250mA	
Gate-Drain Charge		_	0.1	_	nC	1	
Turn-On Delay Time		_	3.5	_	ns		
Turn-On Rise Time		_	3.3	_	ns	$V_{DD} = 30V, V_{GS} = 10V,$	
Turn-Off Delay Time		_	16.8	_	ns	$R_G = 25\Omega$, $I_D = 200 \text{mA}$	
Turn-Off Fall Time		_	13.8	_	ns		

Notes:

- 5. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
 - So Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
 Short duration pulse test used to minimize self-heating effect.

 - 8. Guaranteed by design. Not subject to product testing.





I_D, DRAIN CURRENT (A)
Figure 5. Typical On-Resistance vs .Drain Current and
Temperature

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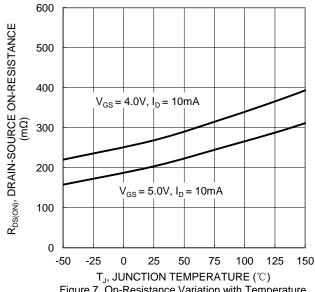
T_J, JUNCTION TEMPERATURE (°C)

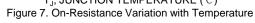
Figure 6. On-Resistance Variation with Temperature

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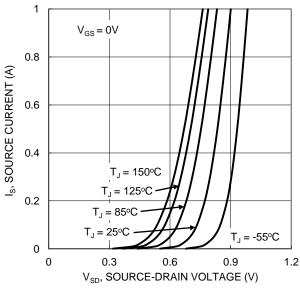


Figure 9. Diode Forward Voltage vs. Current

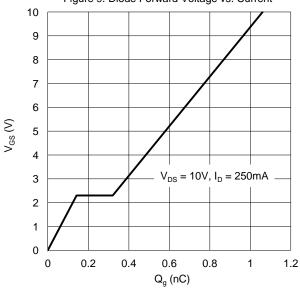


Figure 11. Gate Charge

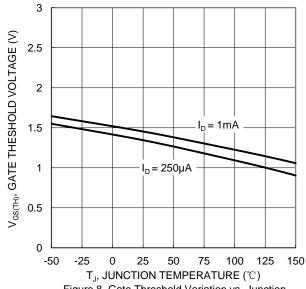


Figure 8. Gate Threshold Variation vs. Junction Temperature

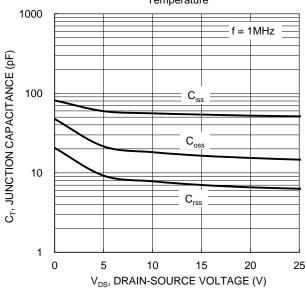


Figure 10. Typical Junction Capacitance

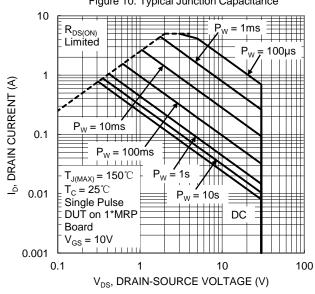


Figure 12. SOA, Safe Operation Area



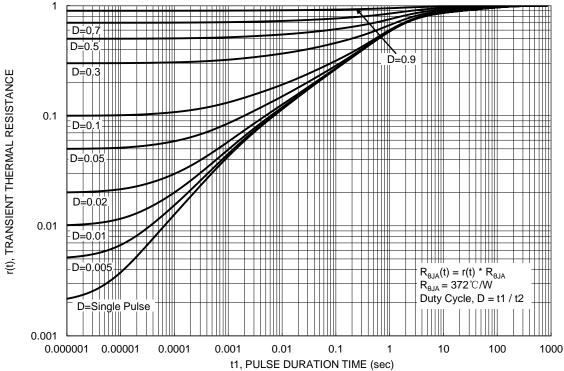


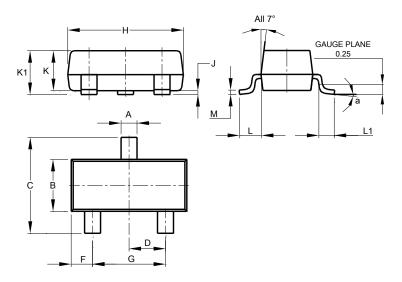
Figure 13. Transient Thermal Resistance



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT23

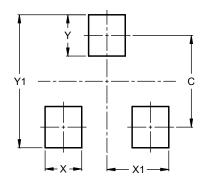


SOT23							
Dim	Min	Max	Тур				
Α	0.37	0.51	0.40				
В	1.20	1.40	1.30				
C	2.30	2.50	2.40				
D	0.89	1.03	0.915				
F	0.45	0.60	0.535				
G	1.78	2.05	1.83				
Η	2.80	3.00	2.90				
7	0.013	0.10	0.05				
K	0.890	1.00	0.975				
K1	0.903	1.10	1.025				
٦	0.45	0.61	0.55				
L1	0.25	0.55	0.40				
M	0.085	0.150	0.110				
а	0°	8°					
All Dimensions in mm							

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT23



Dimensions	Value (in mm)
С	2.0
Х	0.8
X1	1.35
Υ	0.9
Y1	2.9



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