





#### 30V N-CHANNEL ENHANCEMENT MODE MOSFET

#### **Product Summary**

V <sub>(BR)DSS</sub>	R <sub>DS(on)</sub>	I <sub>D</sub> T <sub>A</sub> = 25°C		
30V	24mΩ @ V <sub>GS</sub> = 10V	8.5A		
30 V	36mΩ @ V <sub>GS</sub> = 4.5V	6.9A		

### **Description and Applications**

This new generation MOSFET has been designed to minimize the onstate resistance (R<sub>DS(on)</sub>) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

- Motor control
- Backlighting
- DC-DC Converters
- Power management functions

#### **Features and Benefits**

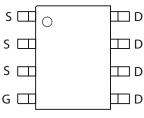
- Low on-resistance
- Fast switching speed
- "Green" component and RoHS compliant (Note 1)

#### **Mechanical Data**

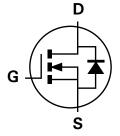
- Case: SO-8
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0 (Note 1)
- Moisture Sensitivity: Level 1 per J-STD-020D
- Terminals Connections: See Diagram
- Terminals: Finish Matte Tin annealed over Copper lead frame. Solderable per MIL-STD-202, Method 208
- Weight: 0.074 grams (approximate)











**Equivalent Circuit** 

### Ordering Information (Note 1)

Product	Marking	Reel size (inches)	Tape width (mm)	Quantity per reel	
DMN3024LSS-13	N3024LS	13	12	2,500	

Note:

1. Diodes, Inc. defines "Green" products as those which are Eu RoHS compliant and contain no halogens or antimony compounds; further information about Diodes Inc.'s "Green" Policy can be found on our website. For packaging details, go to our website.

## **Marking Information**



N3024LS = Product Type Marking Code ☐ = Manufacturer's Marking YY WW = Date Code Marking YY = Year (ex: 09 = 2009)WW = Week (01-52)





### **Maximum Ratings** @T<sub>A</sub> = 25°C unless otherwise specified

Charac	cteristic		Symbol	Value	Unit
Drain-Source voltage			$V_{DSS}$	30	V
Gate-Source voltage			V <sub>GS</sub>	±20	V
		(Note 3)	I <sub>D</sub>	8.5	
Continuous Drain current	V <sub>GS</sub> = 10V	$T_A = 70^{\circ}C \text{ (Note 3)}$		6.8	Α
		(Note 2)		6.4	
Pulsed Drain current	V <sub>GS</sub> = 10V	(Note 4)	I <sub>DM</sub>	36	Α
Continuous Source current (Body diode) (Note			Is	4.5	Α
Pulsed Source current (Body diode) (Note 4)			I <sub>SM</sub>	36	А

## Thermal Characteristics @T<sub>A</sub> = 25°C unless otherwise specified

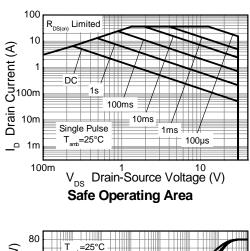
Characteristic		Symbol	Value	Unit	
Power dissipation	(Note 2)	D	1.6 12.5	W	
Linear derating factor	(Note 3)	P <sub>D</sub>	2.8 22.2	mW/°C	
Thermal Resistance, Junction to Ambient	(Note 2) (Note 3)	$R_{ hetaJA}$	80 45	°C/W	
Thermal Resistance, Junction to Lead	(Note 5)	$R_{ heta JL}$	35	°C/W	
Operating and storage temperature range		TJ, T <sub>STG</sub>	-55 to 150	°C	

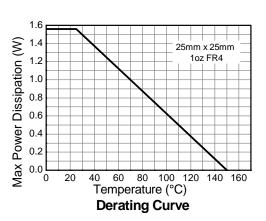
Notes:

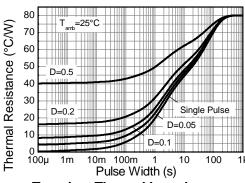
- 2. For a device surface mounted on 25mm x 25mm x 1.6mm FR4 PCB with high coverage of single sided 1oz copper, in still air conditions; the device is measured when operating in a steady-state condition.
- 3. Same as note (2), except the device is measured at  $t \leq 10 \mbox{ sec.}$
- 4. Same as note (2), except the device is pulsed with D= 0.02 and pulse width 300 µs. The pulse current is limited by the maximum junction temperature.
- 5. Thermal resistance from junction to solder-point (at the end of the drain lead): the device is operating in a steady-state condition.

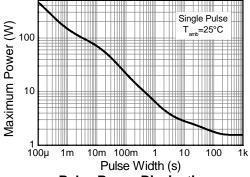


### **Thermal Characteristics**









**Transient Thermal Impedance** 

**Pulse Power Dissipation** 





## Electrical Characteristics @T<sub>A</sub> = 25°C unless otherwise specified

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition		
OFF CHARACTERISTICS								
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	30	_	_	V	$I_D = 250 \mu A, V_{GS} = 0 V$		
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	_	_	0.5	μΑ	V <sub>DS</sub> = 30V, V <sub>GS</sub> = 0V		
Gate-Source Leakage	I <sub>GSS</sub>	_	_	±100	nA	V <sub>GS</sub> = ±20V, V <sub>DS</sub> = 0V		
ON CHARACTERISTICS	ON CHARACTERISTICS							
Gate Threshold Voltage	V <sub>GS(th)</sub>	1.0	_	3.0	V	I <sub>D</sub> = 250μA, V <sub>DS</sub> = V <sub>GS</sub>		
Static Drain-Source On-Resistance (Note 6)	D			0.024	Ω	V <sub>GS</sub> = 10V, I <sub>D</sub> = 7.0A		
Static Dialii-Source Off-Resistance (Note 6)	R <sub>DS (ON)</sub>	_	_	0.036	12	V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 6.0A		
Forward Transconductance (Notes 6 & 7)	<b>g</b> fs	_	16.5	_	S	V <sub>DS</sub> = 15V, I <sub>D</sub> = 7.1A		
Diode Forward Voltage (Note 6)	$V_{SD}$	_	0.82	1.2	V	I <sub>S</sub> = 1.7A, V <sub>GS</sub> = 0V		
Reverse recovery time (Note 7)	t <sub>rr</sub>		12	_	ns	1 0 0 0 15/14 100 0 / -		
Reverse recovery charge (Note 7)	Q <sub>rr</sub>	_	4.8	_	nC	I <sub>S</sub> = 2.2A, di/dt= 100A/μs		
DYNAMIC CHARACTERISTICS (Note 7)								
Input Capacitance	C <sub>iss</sub>	_	608	_	pF			
Output Capacitance	Coss	_	132	_	pF	V <sub>DS</sub> = 15V, V <sub>GS</sub> = 0V -f= 1MHz		
Reverse Transfer Capacitance	C <sub>rss</sub>	_	71	_	pF	-1= 11VII 12		
Total Gate Charge	Qg	_	6.3	_	nC	V <sub>DS</sub> = 15V, V <sub>GS</sub> = 4.5V I <sub>D</sub> = 7A		
Total Gate Charge	Qg	_	12.9	_	nC			
Gate-Source Charge	Q <sub>gs</sub>	_	2.5		nC	V <sub>DS</sub> = 15V, V <sub>GS</sub> = 10V -I <sub>D</sub> = 7A		
Gate-Drain Charge	Q <sub>gd</sub>	_	2.5		nC	ID= /A		
Turn-On Delay Time (Note 8)	t <sub>D(on)</sub>	_	2.9	_	ns			
Turn-On Rise Time (Note 8)	t <sub>r</sub>	_	3.3		ns	V <sub>DD</sub> = 15V, V <sub>GS</sub> = 10V		
Turn-Off Delay Time (Note 8)	t <sub>D(off)</sub>	_	16		ns	$I_D=1A, R_G\cong 6.0\Omega$		
Turn-Off Fall Time (Note 8)	t <sub>f</sub>	_	8		ns	]		

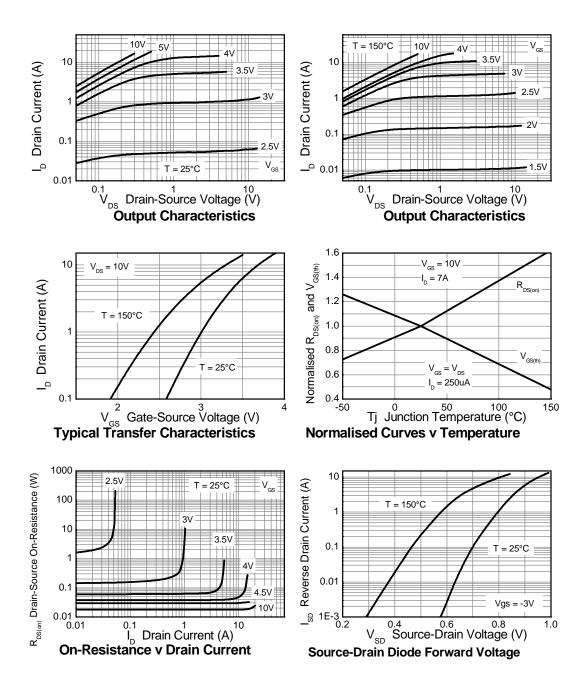
Notes:

- 6. Measured under pulsed conditions. Pulse width  $\leq 300 \mu s;$  duty cycle  $\leq 2\%$
- 7. For design aid only, not subject to production testing.
  8. Switching characteristics are independent of operating junction temperatures.



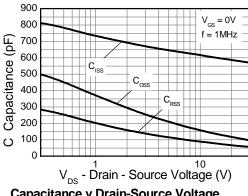


## **Typical Characteristics**





## **Typical Characteristics - continued**

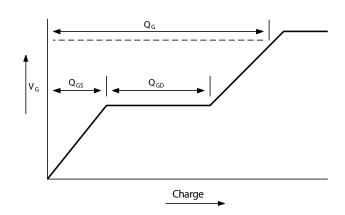


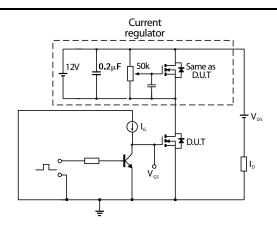
Gate-Source Voltage (V) 8 3 V SS V 4 5 6 7 8 9 Q - Charge (nC)

Capacitance v Drain-Source Voltage

Gate-Source Voltage v Gate Charge

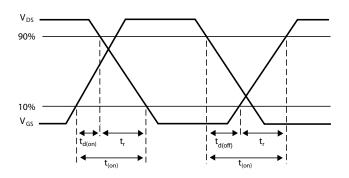
### **Test Circuits**

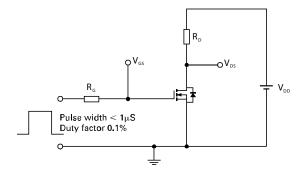




Basic gate charge waveform

Gate charge test circuit



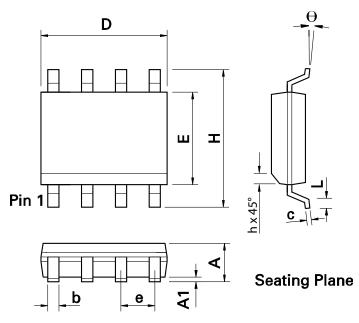


Switching time waveforms

Switching time test circuit

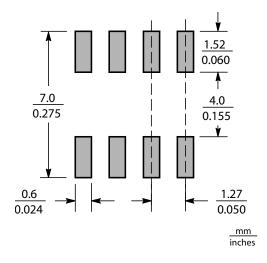


## **Package Outline Dimensions**



DIM	Inc	Inches		Millimeters		Inches		Millimeters	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
Α	0.053	0.069	1.35	1.75	е	0.050 BSC		1.27 BSC	
A1	0.004	0.010	0.10	0.25	b	0.013	0.020	0.33	0.51
D	0.189	0.197	4.80	5.00	С	0.008	0.010	0.19	0.25
Н	0.228	0.244	5.80	6.20	θ	0°	8°	0°	8°
Е	0.150	0.157	3.80	4.00	h	0.010	0.020	0.25	0.50
L	0.016	0.050	0.40	1.27	-	-	-	-	-

# Suggested Pad Layout







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