



N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV _{DSS}	R _{DS(ON)} Max	I _D Max T _A = +25°C
	0.45Ω @ V _{GS} = 4.5V	0.95A
20V	0.6Ω @ $V_{GS} = 2.5V$	0.82A
	0.75Ω @ V _{GS} = 1.8V	0.73A

Description and Applications

This MOSFET has been designed to minimize the on-state resistance (R_{DS(ON)}) yet maintain superior switching performance, making it ideal for high efficiency power management applications.

- General Purpose Interfacing Switch
- Power Management Functions
- DC-DC Converters
- Analog Switch

Features and Benefits

- Low On-Resistance
- Low Gate Threshold Voltage
- Low Input Capacitance
- · Fast Switching Speed
- Low Input/Output Leakage
- ESD Protected
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- The DMG1012UWQ is suitable for automotive applications requiring specific change control; this part is AEC-Q101 qualified, PPAP capable, and manufactured in IATF 16949 certified facilities.

https://www.diodes.com/quality/product-definitions/

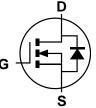
Mechanical Data

- Case: SOT323
- Case Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections Indicator: See Diagram
- Terminals: Finish Matte Tin Annealed over Copper Leadframe.
 Solderable per MIL-STD-202, Method 208 (e3)
- Weight: 0.027 grams (Approximate)

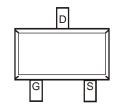




Top View







Top View

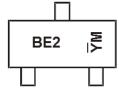
Ordering Information (Note 4)

Part Number	Case	Packaging
DMG1012UWQ-7	SOT323	3000 / Tape & Reel
DMG1012UWQ-13	SOT323	10000 / Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/

Marking Information



BE2 = Product Type Marking Code $\overline{Y}M$ = Date Code Marking \overline{Y} = Year (ex: H = 2020) M = Month (ex: 9 = September)

Date Code Kev

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Year	2020	2021	2022	2023	2024	2025	2026	2027	2028	2029	2030	2031
Code	Н		٦	K	L	М	N	0	Р	R	S	Т
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec



Maximum Ratings (@ $T_A = +25^{\circ}C$, unless otherwise specified.)

Characterist	Symbol	Value	Unit	
Drain-Source Voltage	VDSS	20	V	
Gate-Source Voltage		V _{GSS}	±6	V
Continuous Drain Current (Note 6) V _{GS} = 4.5V	ID	0.95 0.75	Α	
Pulsed Drain Current (10µs Pulse, Duty Cycle=1%)	IDM	6	Α	
Maximum Body Diode Forward Current (Note 5)		Is	0.58	Α

Thermal Characteristics

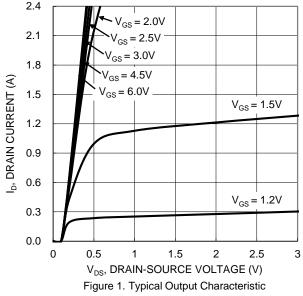
Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)		PD	0.46	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	$R_{\theta JA}$	269	°C/W
Total Power Dissipation (Note 6)		PD	0.61	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	RθJA	205	°C/W
Operating and Storage Temperature Range		TJ, TSTG	-55 to +150	°C

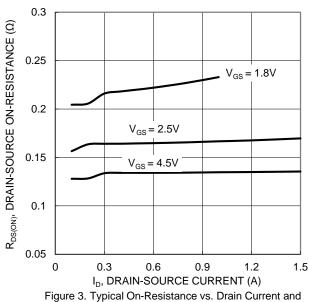
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition		
OFF CHARACTERISTICS (Note 7)								
Drain-Source Breakdown Voltage	BVDSS	20	_	_	V	$V_{GS} = 0V, I_{D} = 250\mu A$		
Zero Gate Voltage Drain Current T _J = +25°C	I _{DSS}	_	_	100	nA	$V_{DS} = 20V, V_{GS} = 0V$		
Gate-Source Leakage	Igss	_	_	±1.0	μΑ	$V_{GS} = \pm 4.5V, V_{DS} = 0V$		
ON CHARACTERISTICS (Note 7)								
Gate Threshold Voltage	Vgs(TH)	0.5	_	1.0	V	$V_{DS} = V_{GS}$, $I_D = 250\mu A$		
			0.13	0.45		$V_{GS} = 4.5V, I_{D} = 600mA$		
Static Drain-Source On-Resistance	R _{DS(ON)}	_	0.16	0.6	Ω	$V_{GS} = 2.5V, I_D = 500mA$		
			0.21	0.75		$V_{GS} = 1.8V, I_{D} = 350mA$		
Diode Forward Voltage	VsD	_	0.7	1.2	V	V _G S = 0V, I _S = 150mA		
DYNAMIC CHARACTERISTICS (Note 8)	-							
Input Capacitance	Ciss	_	43	_	pF			
Output Capacitance	Coss	_	12	_	pF	V _{DS} = 16V, V _{GS} = 0V, f = 1.0MHz		
Reverse Transfer Capacitance	Crss	_	7	_	pF	T = 1.0IVINZ		
Total Gate Charge	Qg	_	1.0	_	nC	45)/)/ 40)/		
Gate-Source Charge	Qgs	_	0.2	_	nC	$V_{GS} = 4.5V, V_{DS} = 10V,$		
Gate-Drain Charge	Qgd	_	0.1	_	nC	I _D = 250mA		
Turn-On Delay Time	t _{D(ON)}	_	5.2	_	ns			
Turn-On Rise Time	t _R	_	2.1	_	ns	V _{DD} = 10V, V _{GS} = 4.5V,		
Turn-Off Delay Time	t _{D(OFF)}	_	452	_	ns	$R_L = 47\Omega$, $R_G = 10\Omega$, $I_D = 200 \text{mA}$		
Turn-Off Fall Time	tF	_	239	_	ns	1D = 20011A		

- 5. Device mounted on FR-4 substrate PC board, with minimum recommended pad layout.6. Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1inch square copper plate.7. Short duration pulse test used to minimize self-heating effect.
- 8. Guaranteed by design. Not subject to production testing.







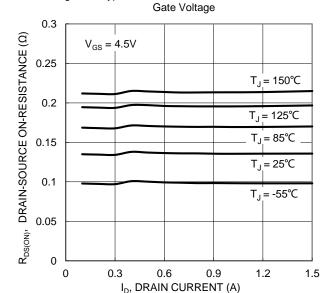


Figure 5. Typical On-Resistance vs. Drain Current and Temperature

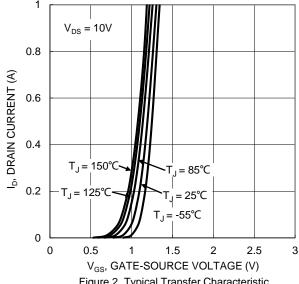


Figure 2. Typical Transfer Characteristic

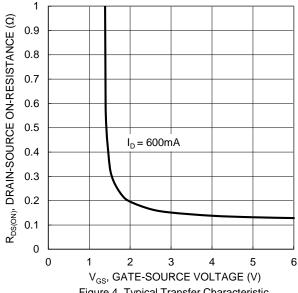


Figure 4. Typical Transfer Characteristic

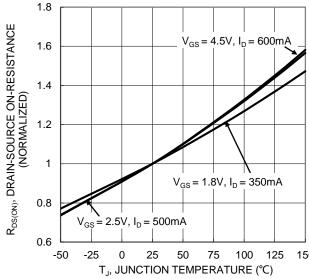


Figure 6. On-Resistance Variation with Temperature





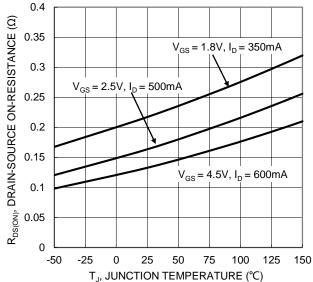
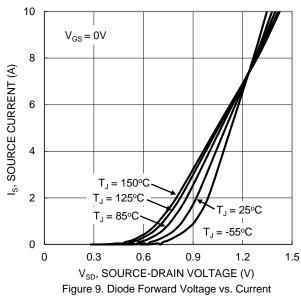


Figure 7. On-Resistance Variation with Temperature



Prigule 9. Diode Forward Vollage vs. Current

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VDS = 10V, ID = 250mA

Qn (nC)

Figure 11. Gate Charge

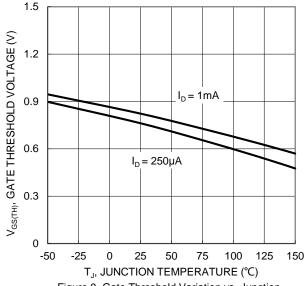
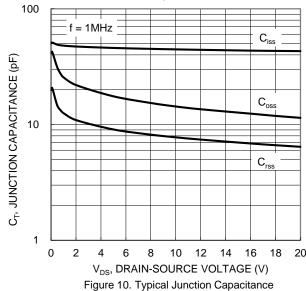


Figure 8. Gate Threshold Variation vs. Junction Temperature



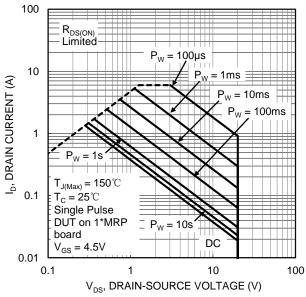


Figure 12. SOA, Safe Operation Area



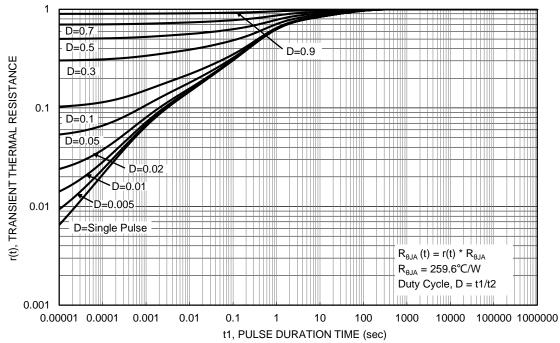


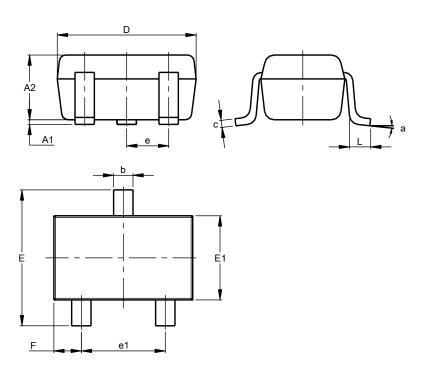
Figure 13. Transient Thermal Resistance



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT323

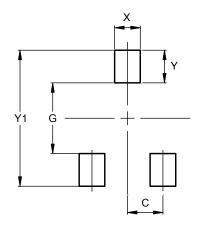


SOT323						
Dim	Min	Max	Тур			
A1	0.00	0.10	0.05			
A2	0.90	1.00	0.95			
b	0.25	0.40	0.30			
С	0.10	0.18	0.11			
D	1.80	2.20	2.15			
Е	2.00	2.20	2.10			
E1	1.15	1.35	1.30			
е	0).650 B	SC			
e1	1.20	1.40	1.30			
F	0.375	0.475	0.425			
١	0.25	0.40	0.30			
а	0°	8°				
All Dimensions in mm						

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT323



Dimensions	Value (in mm)
С	0.650
G	1.300
X	0.470
Y	0.600
Y1	2.500



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