



# 60V 175°C DUAL N-CHANNEL ENHANCEMENT MODE MOSFET PowerDI

### **Product Summary**

V <sub>(BR)DSS</sub>	R <sub>DS(ON)</sub> Max	I <sub>D</sub> Max T <sub>C</sub> = +25°C
60V	$25m\Omega$ @ $V_{GS} = 10V$	32A
	40m0 @ VGs = 4.5V	25A

### **Features and Benefits**

- Rated to +175°C Ideal for High Ambient Temperature Environments
- 100% Unclamped Inductive Switching Ensures More Reliable and Robust End Application
- High Conversion Efficiency
- Low R<sub>DS(ON)</sub> Minimizes On-State Losses
- Low Input Capacitance
- Fast Switching Speed
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- PPAP Capable (Note 4)

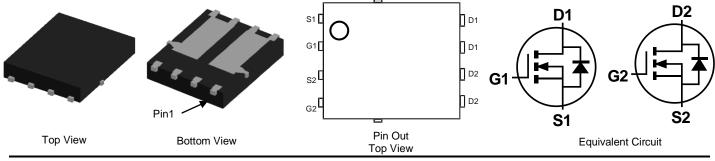
### **Description and Applications**

This MOSFET is designed to meet the stringent requirements of Automotive applications. It is qualified to AEC-Q101, supported by a PPAP and is ideal for use in:

- Backlighting
- Power Management Functions
- DC-DC Converters

### **Mechanical Data**

- Case: PowerDI<sup>®</sup>5060-8 (Type C)
- Case Material: Molded Plastic, "Green" Molding Compound.
   UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections Indicator: See diagram
- Terminals: Finish Matte Tin Annealed over Copper Leadframe.
   Solderable per MIL-STD-202, Method 208 (§3)
- Weight: 0.097 grams (Approximate)



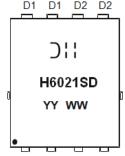
### Ordering Information (Note 5)

Part Number	Case	Packaging
DMNH6021SPDQ-13	PowerDI5060-8 (Type C)	2,500/Tape & Reel

Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

- 2. See http://www.diodes.com/quality/lead\_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. Automotive products are AEC-Q101 qualified and are PPAP capable. Please refer to http://www.diodes.com/quality/product\_compliance\_definitions/.
- 5. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

### **Marking Information**



⊃¦¦ = Manufacturer's Marking H6021SD = Product Type Marking Code YYWW = Date Code Marking YY = Year (ex: 16 = 2016) WW = Week (01 - 53)



## **Maximum Ratings** (@ $T_A = +25$ °C, unless otherwise specified.)

Characteristic	Symbol	Value	Units	
Drain-Source Voltage	$V_{DSS}$	60	V	
Gate-Source Voltage	V <sub>GSS</sub>	±20	V	
Continuous Drain Current (Note 7) V <sub>GS</sub> = 10V	$T_A = +25$ °C $T_A = +70$ °C	I <sub>D</sub>	8.2 6.5	А
Continuous Drain Current (Note 8) $V_{GS} = 10V$ $T_C = +25^{\circ}C$ $T_C = +100^{\circ}C$		I <sub>D</sub>	32 22	А
Pulsed Drain Current (380µs Pulse, Duty Cycle = 1%)	I <sub>DM</sub>	80	Α	
Maximum Continuous Body Diode Forward Current (Note 8)	Is	32	Α	
Avalanche Current, L = 0.1mH (Note 9)	I <sub>AS</sub>	35	Α	
Avalanche Energy, L = 0.1mH (Note 9)	Eas	64	mJ	

### **Thermal Characteristics**

Characteristic	Symbol	Value	Units	
Total Power Dissipation (Note 6)		P <sub>D</sub>	1.5	W
Thermal Desistance, Junction to Ambient (Note 6)		_	99	°C/W
Thermal Resistance, Junction to Ambient (Note 6)	t<10s	$R_{\theta JA}$	53	C/VV
Total Power Dissipation (Note 7)		P <sub>D</sub>	2.8	W
Thermal Resistance, Junction to Ambient (Note 7)	Steady State	D	54	°C/W
Thermal Resistance, Junction to Ambient (Note 7)	t<10s	$R_{\theta JA}$	27	
Thermal Resistance, Junction to Case (Note 8)		$R_{ heta JC}$	2.2	°C/W
Operating and Storage Temperature Range		T <sub>J</sub> , T <sub>STG</sub>	-55 to +175	°C

# **Electrical Characteristics** (@ $T_A = \pm 25$ °C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 10)							
Drain-Source Breakdown Voltage		60	_	_	V	$V_{GS} = 0V, I_D = 250\mu A$	
Zero Gate Voltage Drain Current T <sub>J</sub> = +25°C	I <sub>DSS</sub>	_	_	1	μΑ	$V_{DS} = 60V, V_{GS} = 0V$	
Gate-Source Leakage		_	_	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 10)		•	•	•	•	•	
Gate Threshold Voltage	V <sub>GS(TH)</sub>	1	_	3	V	$V_{DS} = V_{GS}$ , $I_D = 250\mu A$	
Static Drain-Source On-Resistance	В	_	15	25	mΩ	V <sub>GS</sub> = 10V, I <sub>D</sub> = 15A	
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>	_	21	40	11112	$V_{GS} = 4.5V, I_D = 12A$	
Diode Forward Voltage	V <sub>SD</sub>	_	0.75	1.2	V	V <sub>GS</sub> = 0V, I <sub>S</sub> = 2.6A	
DYNAMIC CHARACTERISTICS (Note 11)		•	•	•	•	•	
Input Capacitance	C <sub>ISS</sub>	_	1,143	_	pF	V 05V V 0V	
Output Capacitance	Coss	_	168	_	pF	$V_{DS} = 25V, V_{GS} = 0V,$ - f = 1MHz	
Reverse Transfer Capacitance	C <sub>RSS</sub>	_	69	_	pF T = TWH2		
Gate Resistance	R <sub>G</sub>	_	2.1	_	Ω	$V_{DS} = 0V$ , $V_{GS} = 0V$ , $f = 1MHz$	
Total Gate Charge (V <sub>GS</sub> = 10V)	$Q_{G}$	_	20.1	_	nC		
Total Gate Charge (V <sub>GS</sub> = 6V)	Q <sub>G</sub>	_	12	_	nC	), 20V I 20A	
Gate-Source Charge	Q <sub>GS</sub>	_	4.3	_	nC	$V_{DS} = 30V, I_{D} = 20A,$	
Gate-Drain Charge	$Q_{GD}$	_	5.5	_	nC	1	
Turn-On Delay Time	t <sub>D(ON)</sub>	_	4.4	_	ns		
Turn-On Rise Time	t <sub>R</sub>	_	6.0	_	ns	V <sub>DD</sub> = 30V, V <sub>GS</sub> = 10V,	
Turn-Off Delay Time	t <sub>D(OFF)</sub>	_	14.2	_	ns	$R_G = 4.7\Omega$ , $I_D = 20A$	
Turn-Off Fall Time	t <sub>F</sub>	_	5.4	_	ns	1	
Body Diode Reverse Recovery Time	t <sub>RR</sub>	_	21.2	_	ns	IE 004 divis 4004/s	
Body Diode Reverse Recovery Charge	Q <sub>RR</sub>	_	15.2	_	nC		

Notes: 6. Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.

7. Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1-inch square copper plate.

DMNH6021SPDQ Document number: DS38111 Rev. 2 - 2

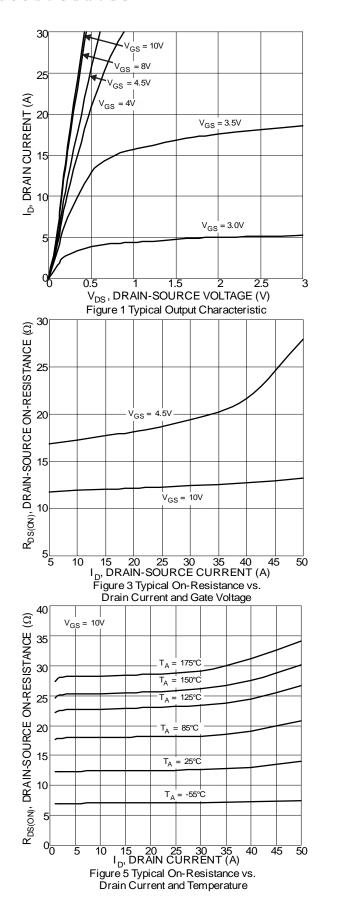
<sup>8.</sup> Thermal resistance from junction to soldering point (on the exposed drain pad).

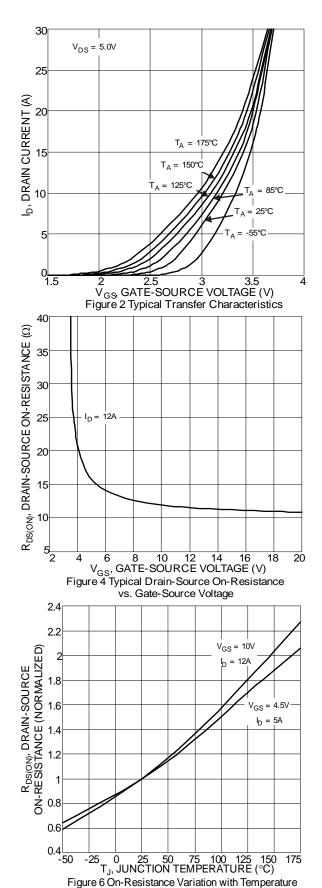
<sup>9.</sup> IAS and EAS rating are based on low frequency and duty cycles to keep  $T_J = 25^{\circ}C$ 

<sup>10.</sup> Short duration pulse test used to minimize self-heating effect.

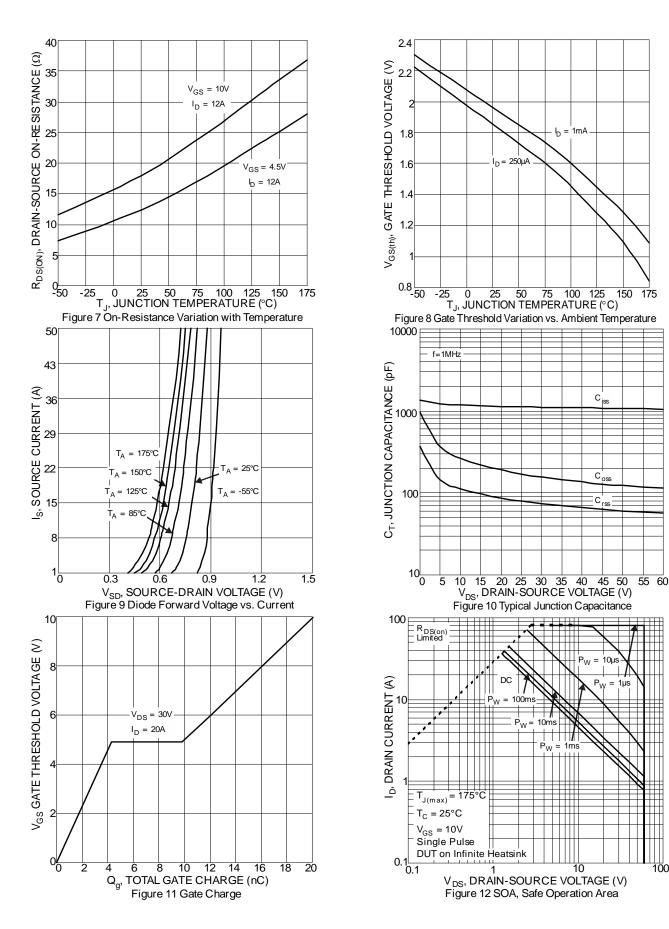
<sup>11.</sup> Guaranteed by design. Not subject to product testing.



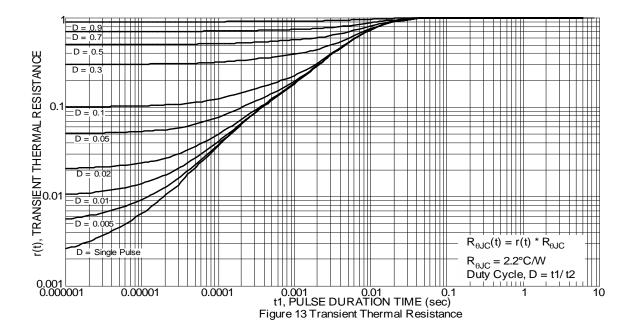










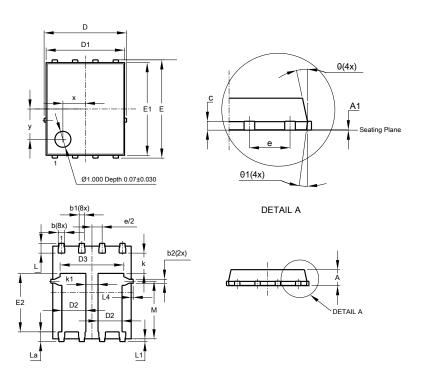




## **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.

#### PowerDI5060-8 (Type C)

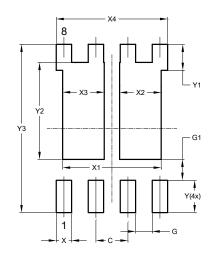


PowerDI5060-8 (Type C)					
Dim	Min Max		Тур		
Α	0.90	1.10	1.00		
A1	0	0.05	0.02		
b	0.33	0.51	0.41		
b1	0.300	0.366	0.333		
b2	0.20	0.35	0.25		
С	0.23	0.33	0.277		
D	5	.15 BS0	)		
D1	4.85	4.95	4.90		
D2	1.40	1.60	1.50		
D3	_	_	3.98		
Е	6	.15 BS0	2		
E1	5.75	5.85	5.80		
E2	3.56	3.76	3.66		
е	1.27BSC				
k	-   -		1.27		
k1	0.56	ı	_		
L	0.51	0.71	0.61		
La	0.51	0.71	0.61		
L1	0.05	0.20	0.175		
L4	ı	ı	0.125		
М	3.50	3.71	3.605		
х	_	_	1.400		
у	-	-	1.900		
θ	10°	12°	11°		
θ1	6°	8°	7°		
All Dimensions in mm					

## **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.

### PowerDI5060-8 (Type C)



Dimensions	Value		
Dillicitations	(in mm)		
C	1.270		
G	0.660		
G1	0.820		
X	0.610		
X1	3.910		
X2	1.650		
Х3	1.650		
X4	4.420		
Y	1.270		
Y1	1.020		
Y2	3.810		
Y3	6.610		



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