



A Product Line of **Diodes Incorporated**



ZXT953K

100V PNP LOW SATURATION MEDIUM POWER TRANSISTOR

Features

- $BV_{CEO} > -100V$
- I_C = -5A Continuous Collector Current
- I_{CM} = -10A Peak Collector Current
- $R_{SAT} = 67m\Omega$ Typical for Low Equivalent On Resistance
- Low Saturation Voltage
- High Gain Hold-Up (100 min @ 1A)
- Lead-Free Finish; RoHS Compliant (Note 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

Mechanical Data

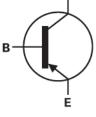
- Case: TO252 (DPAK)
- Case Material: Molded Plastic, "Green" Molding Compound; UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin; Solderable per MIL-STD-202, Method 208 (e3)
- Weight: 0.34 grams (Approximate)

Application

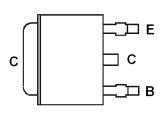
- **DC-DC Converters**
- Power Switches
- Motor Control
- Automotive Circuits
- **Inverter Circuits**



Top View



Device Schematic



Pin Out Configuration Top view

Ordering Information (Note 4)

Product	Compliance	Marking	Reel size (inches)	Tape width (mm)	Quantity per reel
ZXT953KTC	AEC-Q101	ZXT953	13	16	2,500
Notes: 1. EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. All applicable RoHS exemptions applied.					

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2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at http:// /www.diodes.com/products/packages.html.

Marking Information



ZXT953 = Product Type Marking Code YYWW = Date Code Marking YY = Last Digit of Year (ex: 15 = 2015) WW = Week Code (01 - 53)





Absolute Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	BV _{CBO}	-140	V
Collector-Base Voltage	BV _{CER}	-140	V
Collector-Emitter Voltage	V _{CEO}	-100	V
Emitter-Base Voltage	V _{EBO}	-7	V
Continuous Collector Current	I _C	-5	A
Base Current	Ι _Β	-0.5	A
Peak Pulse Collector Current	Ісм	-10	A

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit		
	(Note 5)		2.1		
Power Dissipation	(Note 6)	PD	3.2	W	
	(Note 7)		4.2	1	
	(Note 5)		59		
Thermal Resistance, Junction to Ambient Air	(Note 6)	R _{0JA}	39	°C/W	
	(Note 7)		30	1	
Thermal Resistance, Junction to Leads (Note 8)		R _{θJL}	1.8	°C/W	
Operating and Storage Temperature Range	T _J ,T _{STG}	-55 to +150	°C		

ESD Ratings (Note 9)

Characteristic	Symbol	Value	Unit	JEDEC Class
Electrostatic Discharge - Human Body Model	ESD HBM	4,000	V	ЗA
Electrostatic Discharge - Machine Model	ESD MM	400	V	С

Notes: 5. For a device mounted with the exposed collector pad on 25mm x 25mm 1oz copper that is on a single-sided 1.6mm FR4 PCB; device is measured under still air conditions whilst operating in a steady-state.

6. Same as Note 5, except the device is mounted on 50mm x 50mm with 1oz copper.

7. Same as Note 5, except the device is mounted on 50mm x 50mm with 2oz copper.

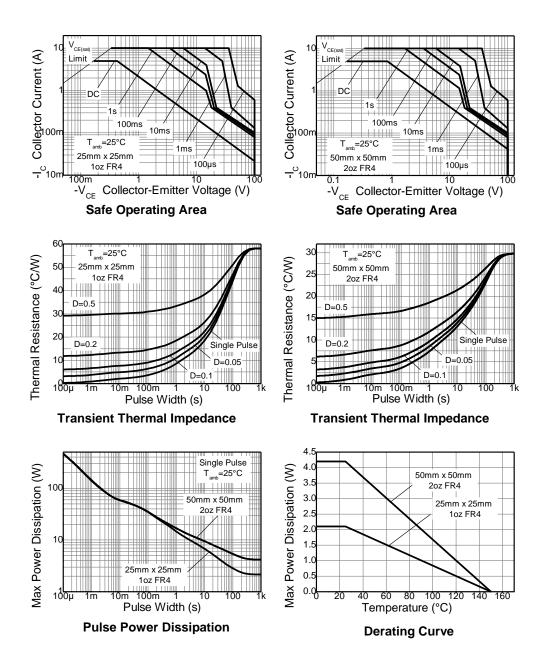
8. Thermal resistance from junction to solder-point (at the end of the collector lead).

9. Refer to JEDEC specification JESD22-A114 and JESD22-A115.





Thermal Characteristics and Derating Information







Electrical Characteristics ($@T_A = +25^{\circ}C$, unless otherwise specified.)

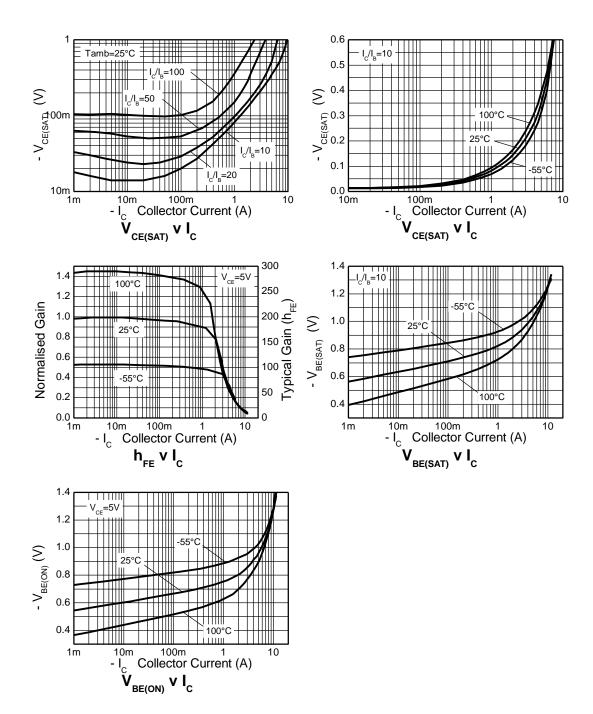
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Characteristic	Symbol	Min	Тур.	Max	Unit	Test Condition
Collector-Base Breakdown Voltage	BV _{CBO}	-140	-170	-	V	I _C = -100μA
Collector-Base Breakdown Voltage	BV _{CER}	-140	-170	-	V	I _C = -1μA, R _{BE} ≤1kΩ
Collector-Emitter Breakdown Voltage (Note 10)	BV _{CEO}	-100	-125	-	V	I _C = -10mA
Emitter-Base Breakdown Voltage	BV _{EBO}	-7	-8.1	-	V	I _E = -100μA
Collector Cut-Off Current	I _{СВО}	-	<1	-20	nA	V _{CB} = -100V
Emitter Cut-Off Current	I _{EBO}	-	<1	-10	nA	$V_{EB} = -6V$
Emitter Cut-Off Current	ICER	-	<1	-20	nA	V _{CE} = -100V, R _{BE} ≤1kΩ
DC Current Transfer Static Ratio (Note 10)	h _{FE}	100 100 50 15	220 200 85 30	- 300 - -	-	$\label{eq:lc} \begin{array}{l} I_{C} = -10 \text{mA}, \ V_{CE} = -1 \text{V} \\ I_{C} = -1 \text{A}, \ V_{CE} = -1 \text{V} \\ I_{C} = -3 \text{A}, \ V_{CE} = -1 \text{V} \\ I_{C} = -5 \text{A}, \ V_{CE} = -1 \text{V} \end{array}$
Collector-Emitter Saturation Voltage (Note 10)	V _{CE(sat)}		-20 -80 -140 -335	-30 -100 -175 -390	mV	$I_{C} = -0.1A, I_{B} = -10mA$ $I_{C} = -1A, I_{B} = -100mA$ $I_{C} = -2A, I_{B} = -200mA$ $I_{C} = -5A, I_{B} = -500mA$
Base-Emitter Saturation Voltage (Note 10)	V _{BE(sat)}	-	-1.01	-1.1	V	$I_{\rm C} = -5A, I_{\rm B} = -500 {\rm mA}$
Base-Emitter Turn-On Voltage (Note 10)	V _{BE(on)}	-	-0.94	-1.05	V	I _C = -5A, V _{CE} = -1V
Transitional Frequency	f _T	-	125	-	MHz	I _C = -100mA, V _{CE} = -10V f = 50MHz
Output Capacitance	Сово	-	65	-	pF	$V_{CB} = -10V, f = 1MHz,$
Switching Times	t _{ON} toff	-	110 460	-	nS	$I_{C} = -2A, V_{CC} = -10V,$ $I_{B1} = I_{B2} = -200mA$

Note: 10. Measured under pulsed conditions. Pulse width \leq 300µs. Duty cycle \leq 2%.





Typical Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)





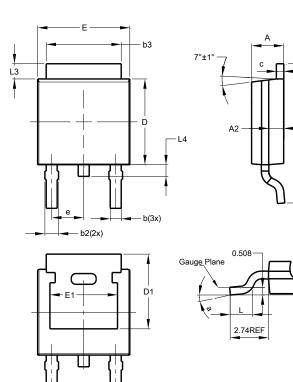


Package Outline Dimensions

Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.

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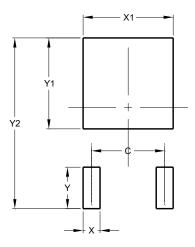
Seating Plane



TO252 (DPAK)					
Dim	Min	Max	Тур		
Α	2.19	2.39	2.29		
A1	0.00	0.13	0.08		
A2	0.97	1.17	1.07		
b	0.64	0.88	0.783		
b2	0.76	1.14	0.95		
b3	5.21	5.46	5.33		
С	0.45	0.58	0.531		
D	6.00	6.20	6.10		
D1	5.21	-	-		
е	-	-	2.286		
Ε	6.45	6.70	6.58		
E1	4.32	-	-		
Н	9.40	10.41	9.91		
L	1.40	1.78	1.59		
L3	0.88	1.27	1.08		
L4	0.64	1.02	0.83		
а	0°	10°	-		
All	All Dimensions in mm				

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)		
С	4.572		
Х	1.060		
X1	5.632		
Y	2.600		
Y1	5.700		
Y2	10.700		

For high voltage applications, the appropriate industry sector guidelines should be considered with regards to creepage and clearance distances between device terminals and PCB tracking.





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