



DMTH4005SPSQ

40V +175°C N-CHANNEL ENHANCEMENT MODE MOSFET POWERDI®

Product Summary

BV _{DSS}	R _{DS(ON)} Max	I _D T _C = +25°C (Note 10)
40V	$3.7\text{m}\Omega$ @ $V_{GS} = 10V$	100A

Features

- Rated to +175°C Ideal For High Ambient Temperature Environments
- 100% Unclamped Inductive Switching Ensures More Reliable And Robust End Application
- Low R_{DS(ON)} Minimizes Power Losses
- Low Q_a Minimizes Switching Losses
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- PPAP Capable (Note 4)

Description and Applications

This MOSFET is designed to meet the stringent requirements of Automotive applications. It is qualified to AEC-Q101, supported by a PPAP and is ideal for use in:

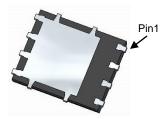
- · Engine Management Systems
- Body Control Electronics
- DC-DC Converters

Mechanical Data

- Case: POWERDI[®]5060-8
- Case Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Finish Matte Tin Annealed over Copper Leadframe.
 Solderable per MIL-STD-202, Method 208 (23)
- Weight: 0.097 grams (Approximate)

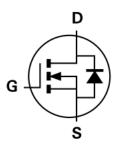


Top View

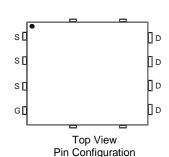


POWERDI®5060-8

Bottom View



Internal Schematic



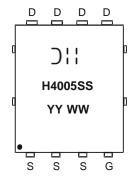
Ordering Information (Note 5)

Part Number	Case	Packaging
DMTH4005SPSQ-13	POWERDI [®] 5060-8	2,500 /Tape & Reel

Notes:

- 1. EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. All applicable RoHS exemptions applied.
- 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- Automotive products are AEC-Q101 qualified and are PPAP capable. For more information, please refer to http://www.diodes.com/product_compliance_definitions.html.
- 5. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information



⊃¦¦ = Manufacturer's Marking
 H4005SS = Product Type Marking Code
 YYWW = Date Code Marking
 YY = Year (ex: 14 = 2014)
 WW = Week (01 to 53)

POWERDI is a registered trademark of Diodes Incorporated.



Maximum Ratings ($@T_A = +25^{\circ}C$, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Drain-Source Voltage		V_{DSS}	40	V
Gate-Source Voltage		V_{GSS}	±20	V
Continuous Drain Current (Note 6)	$T_A = +25$ °C $T_A = +70$ °C	ΙD	20.9 17.5	А
Continuous Drain Current (Notes 7 & 10)	$T_{C} = +25^{\circ}C$ $T_{C} = +100^{\circ}C$	I _D	100 100	А
Maximum Continuous Body Diode Forward Current (Note 7)		Is	100	Α
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)		I _{DM}	150	Α
Avalanche Current, L=0.6mH		I _{AS}	21	Α
Avalanche Energy, L=0.6mH		E _{AS}	132.3	mJ

Thermal Characteristics

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 6)	T _A = +25°C	P_{D}	2.6	W
Thermal Resistance, Junction to Ambient (Note 6)		$R_{\theta JA}$	57	°C/W
Total Power Dissipation (Note 7)	$T_C = +25$ °C	P_{D}	150	W
Thermal Resistance, Junction to Case (Note 7)		R ₀ JC	1	°C/W
Operating and Storage Temperature Range		T _{J,} T _{STG}	-55 to +175	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

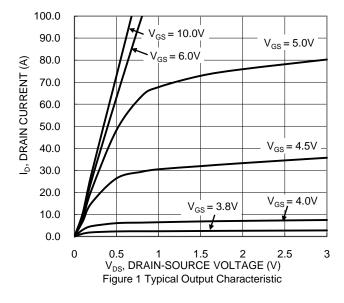
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 8)			1	1			
Drain-Source Breakdown Voltage	BV _{DSS}	40			V	$V_{GS} = 0V$, $I_D = 1mA$	
Zero Gate Voltage Drain Current	I _{DSS}			1	μΑ	$V_{DS} = 32V, V_{GS} = 0V$	
Gate-Source Leakage	I _{GSS}			±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 8)							
Gate Threshold Voltage	V _{GS(TH)}	2		4	V	$V_{DS} = V_{GS}$, $I_D = 250\mu A$	
Static Drain-Source On-Resistance	R _{DS(ON)}		2.9	3.7	mΩ	$V_{GS} = 10V, I_D = 50A$	
Diode Forward Voltage	V _{SD}		0.88		V	$V_{GS} = 0V, I_S = 50A$	
DYNAMIC CHARACTERISTICS (Note 9)							
Input Capacitance	C _{iss}		3,062				
Output Capacitance	Coss	_	902.2	_	pF	$V_{DS} = 20V$, $V_{GS} = 0V$, $f = 1MHz$	
Reverse Transfer Capacitance	C _{rss}		179.2				
Gate Resistance	Rg		0.67		Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$	
Total Gate Charge	Qg		49.1			V _{DD} = 20V, I _D = 50A, V _{GS} = 10V	
Gate-Source Charge	Q _{gs}		10.3		nC		
Gate-Drain Charge	Q _{gd}		13				
Turn-On Delay Time	t _{D(ON)}		8.7			$V_{DD} = 20V, V_{GS} = 10V,$ $I_{D} = 50A, R_{G} = 3\Omega$	
Turn-On Rise Time	t _R		6.8		20		
Turn-Off Delay Time	t _{D(OFF)}		18.6		ns		
Turn-Off Fall Time	t _F		7.3				
Body Diode Reverse Recovery Time	t _{RR}		31.8	_	ns	I 504 IV I 4004/	
Body Diode Reverse Recovery Charge	Q _{RR}	_	26.5	_	nC	$I_F = 50A$, di/dt = 100A/ μ s	

Notes:

- 6. Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1-inch square copper plate.
 7. Thermal resistance from junction to soldering point (on the exposed drain pad).
 8. Short duration pulse test used to minimize self-heating effect.
 9. Guaranteed by design. Not subject to product testing.
 10. Package limited.







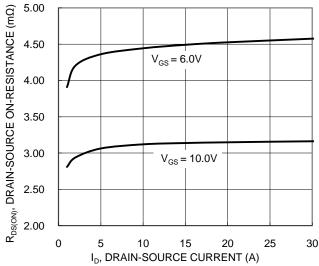


Figure 3 Typical On-Resistance vs. Drain Current and Gate Voltage

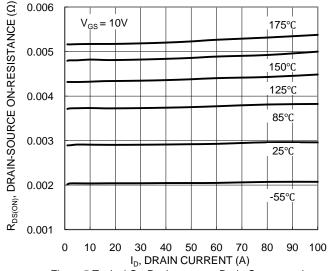
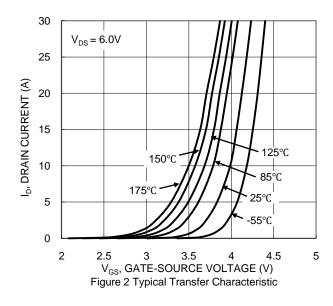
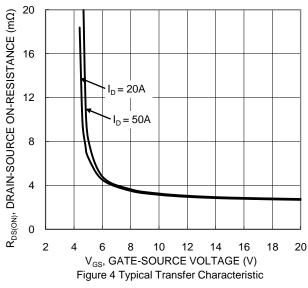


Figure 5 Typical On-Resistance vs. Drain Current and Temperature





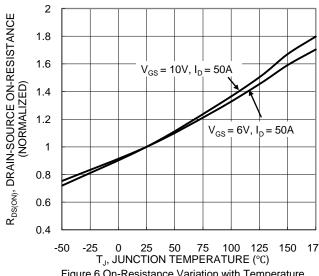


Figure 6 On-Resistance Variation with Temperature



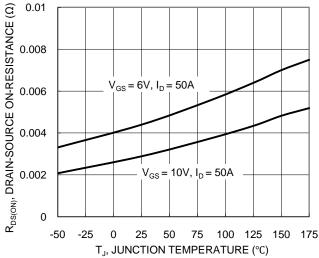
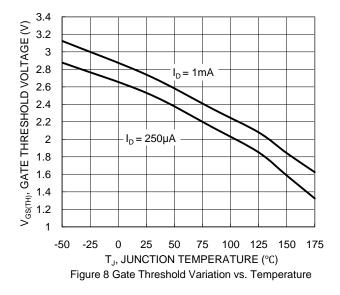
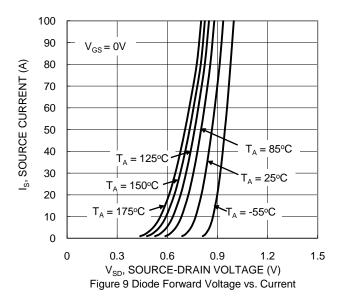
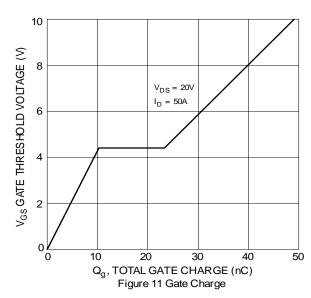
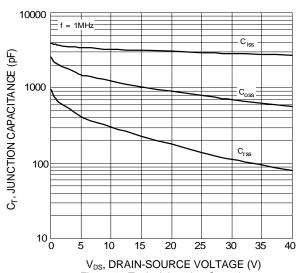


Figure 7 On-Resistance Variation with Temperature

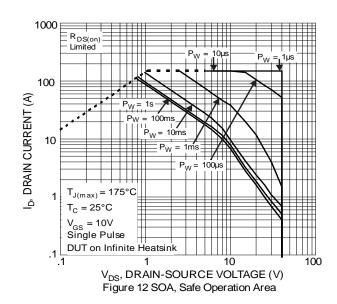




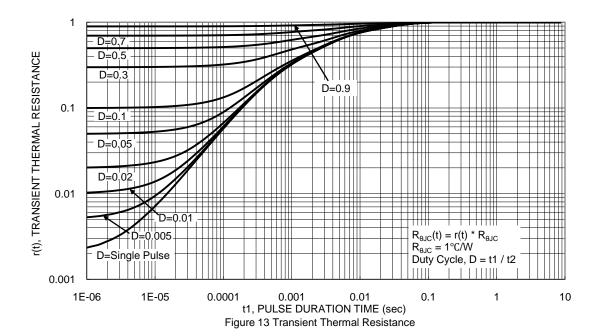




V_{DS}, DRAIN-SOURCE VOLTAGE (V) Figure 10 Typical Junction Capacitance





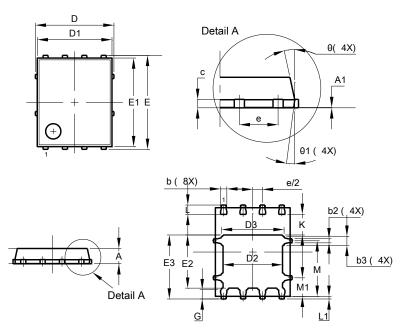




Package Outline Dimensions

Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.

POWERDI®5060-8

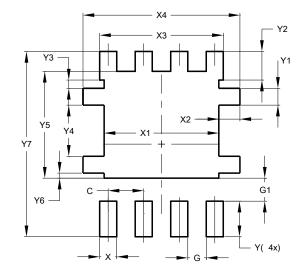


POWERDI®5060-8					
Dim	Min	Max	Тур		
Α	0.90	1.10	1.00		
A1	0.00	0.05	-		
b	0.33	0.51	0.41		
b2	0.200	0.350	0.273		
b3	0.40	0.80	0.60		
С	0.230	0.330	0.277		
D	;	5.15 BSC	;		
D1	4.70	5.10	4.90		
D2	3.70	4.10	3.90		
D3	3.90	4.30	4.10		
Е	(6.15 BSC	;		
E1	5.60	6.00	5.80		
E2	3.28	3.68	3.48		
E3	3.99	4.39	4.19		
е	1.27 BSC				
G	0.51	0.71	0.61		
K	0.51	_	-		
L	0.51	0.71	0.61		
L1	0.100	0.200	0.175		
M	3.235	4.035	3.635		
M1	1.00	1.40	1.21		
Θ	10°	12º	11º		
Θ1	6º	80	7º		
All Dimensions in mm					

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.

POWERDI®5060-8



Dimensions	Value (in mm)		
С	1.270		
G	0.660		
G1	0.820		
Х	0.610		
X1	4.100		
X2	0.755		
Х3	4.420		
X4	5.610		
Y	1.270		
Y1	0.600		
Y2 1.020			
Y3	0.295		
Y4	1.825		
Y5	3.810		
Y6	0.180		
Y7 6.610			



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