



SMALL SIGNAL COMPLEMENTARY PRE-BIASED DUAL TRANSISTOR

#### **Features**

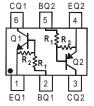
- Epitaxial Planar Die Construction
- Built-In Biasing Resistors
- Surface Mount Package Suited for Automated Assembly
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- PPAP Capable (Note 4)

R1(NOM)	R2(NOM)
10kΩ	10kΩ



### **Mechanical Data**

- Case: SOT363
- Case Material: Molded Plastic, "Green" Molding Compound; UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Plated Leads, Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.006 grams (Approximate)



**Device Schematic** 

#### Ordering Information (Notes 4 & 5)

Product	Compliance	Marking	Reel Size (inches)	Tape Width (mm)	Quantity per Reel
ACX114EUQ-7R	Automotive	2A3	7	8	3,000
ACX114EUQ-13R	Automotive	2A3	13	8	10,000

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

2. See http://www.diodes.com/quality/lead\_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

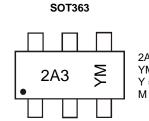
4. Automotive products are AEC-Q101 qualified and are PPAP capable. Refer to http://www.diodes.com/quality/product\_compliance\_definitions/.

5. -7R/-13R are parts rotated in the pocket tape by +180°. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

#### **Marking Information**

Data Cada Kay

Notes:



2A3 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: E = 2017) M = Month (ex: 9 = September)

Date Code Key												
Year	2017	2018	2019	2020	) 202	21 20	)22	2023	2024	2025	2026	2027
Code	E	F	G	Н			J	К	L	М	Ν	0
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



## Absolute Maximum Ratings NPN Section (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Supply Voltage <pin: (1)="" (6)="" to=""></pin:>	V <sub>CC</sub>	50	V
Input Voltage <pin: (1)="" (2)="" to=""></pin:>	V <sub>IN</sub>	-10 to +40	V
Output Current	lo	50	mA
Output Current	I <sub>C</sub> (Max)	100	mA

## Absolute Maximum Ratings PNP Section (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Supply Voltage <pin: (3)="" (4)="" to=""></pin:>	Vcc	-50	V
Input Voltage <pin: (4)="" (5)="" to=""></pin:>	V <sub>IN</sub>	+10 to -40	V
Output Current	lo	-50	mA
Output Current	I <sub>C</sub> (Max)	-100	mA

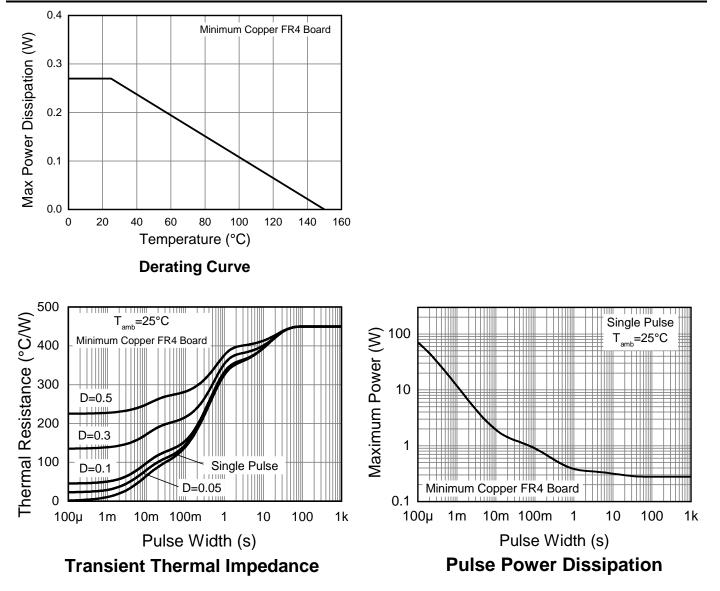
# Thermal Characteristics ( $@T_A = +25^{\circ}C$ , unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Power Dissipation (Notes 6 & 7)	PD	270	mW
Thermal Resistance, Junction to Ambient Air (Note 6)	R <sub>0</sub> JA	450	°C/W
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C

Notes: 6. Mounted on FR4 PC Board with minimum recommended pad layout 7. 150mW per element must not be exceeded.



## Thermal Characteristics and Derating Information





## Electrical Characteristics NPN Section (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
	VI(OFF) (Note 8)	0.5	1.1	—	V	$V_{CC} = 5V, I_{O} = 100\mu A$
Input Voltage	VI(ON) (Note 9)	_	1.9	3.0	v	V <sub>O</sub> = 0.3V, I <sub>O</sub> = 10mA
Output Voltage	V <sub>O(ON)</sub>	_	0.1	0.3	V	$I_0/I_1 = 10mA / 0.5mA$
Input Current	lı	_	_	0.88	mA	$V_1 = 5V$
Output Current	IO(OFF)	_	_	0.5	μA	$V_{CC} = 50V, V_I = 0V$
DC Current Gain	GI	30			_	$V_0 = 5V, I_0 = 5mA$
Input Resistor (R1) Tolerance	$\Delta R_1$	-30	_	+30	%	—
Resistance Ratio Tolerance	$\Delta R_2/R_1$	-20		+20	%	—
Gain-Bandwidth Product (Note 10)	f⊤		250		MHz	V <sub>CE</sub> = 10V, I <sub>E</sub> = 5mA, f = 100MHz
Notes: 8. Guarantees that the device will be switche	d OFF if the Input Volt	age is less	s than 0.5\	/.		

B. Guarantees that the device will be switched OFF if the Input Voltage is less than 0.5V.
Guarantees that the device will be switched ON if the Input Voltage is more than 3V.

10. Transistor - For Reference Only.

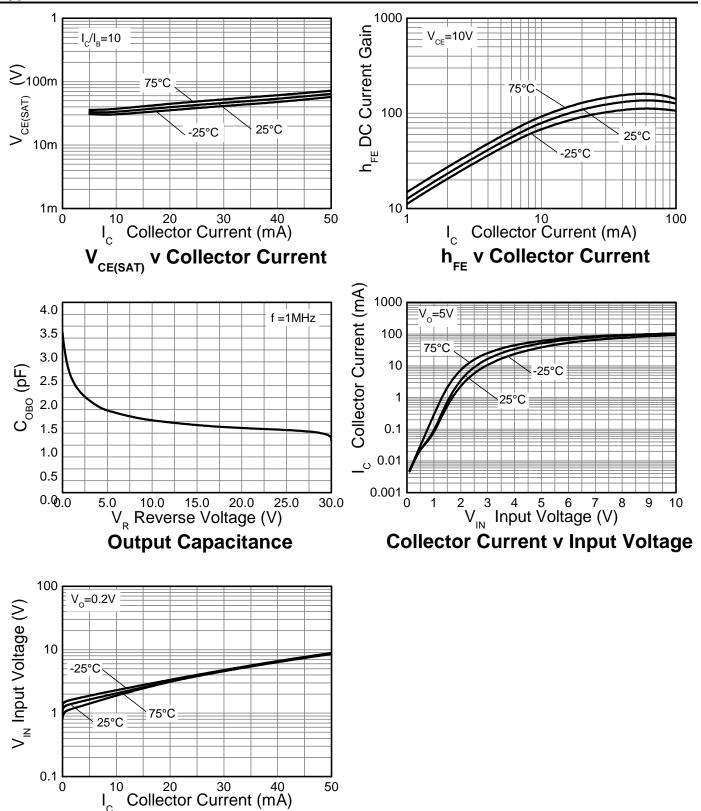
# Electrical Characteristics PNP Section (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Input Voltage	V <sub>I(OFF)</sub> (Note 11)	-0.5	-1.1		V	$V_{CC} = -5V, I_{O} = -100\mu A$
input voltage	VI(ON) (Note 12)	_	-1.9	-3.0	v	V <sub>O</sub> = -0.3V, I <sub>O</sub> = -10mA
Output Voltage	V <sub>O(ON)</sub>	_	-0.1	-0.3	V	$I_0/I_1 = -10mA / -0.5mA$
Input Current	li li	_	_	-0.88	mA	V <sub>I</sub> = -5V
Output Current	I <sub>O(OFF)</sub>	_	—	-0.5	μA	$V_{CC} = 50V, V_I = 0V$
DC Current Gain	GI	30	_	_	_	$V_0 = -5V, I_0 = -5mA$
Input Resistor (R1) Tolerance	$\Delta R_1$	-30	—	+30	%	—
Resistance Ratio Tolerance	$\Delta R_2/R_1$	-20	_	+20	%	—
Gain-Bandwidth Product (Note 10)	f⊤		250		MHz	$V_{CE} = -10V, I_E = -5mA, f = 100MHz$
Notes: 11. Guarantees that the device will be sy	witched OFF if the Input Volt	age is les	ss than -0.	5V.		

Guarantees that the device will be switched OFF if the Input Voltage is less than -0.5V.
Guarantees that the device will be switched ON if the Input Voltage is more than -3V.



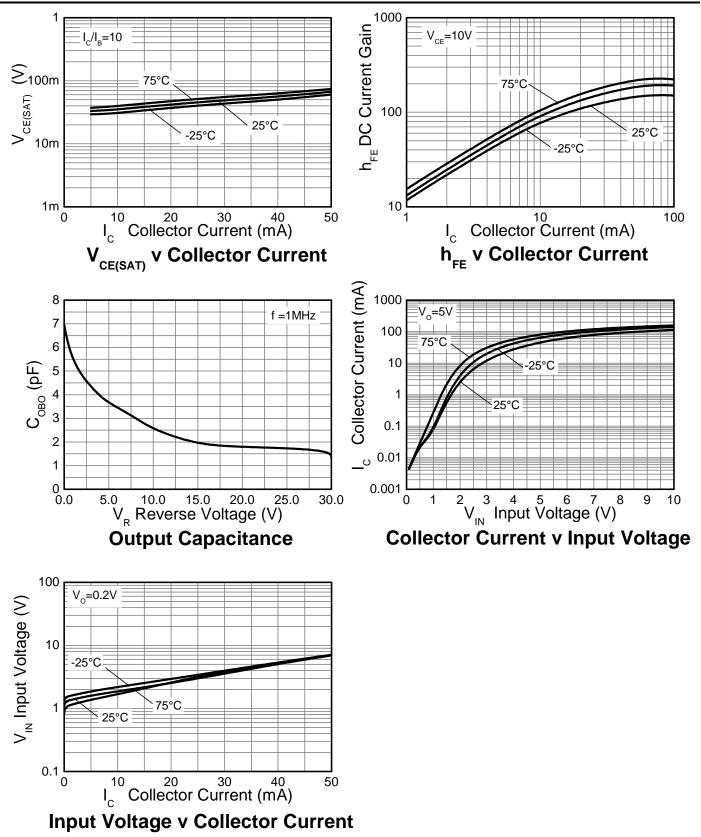
### Typical Curves – NPN Section (@T<sub>A</sub> = +25°C, unless otherwise specified.)



Input Voltage v Collector Current



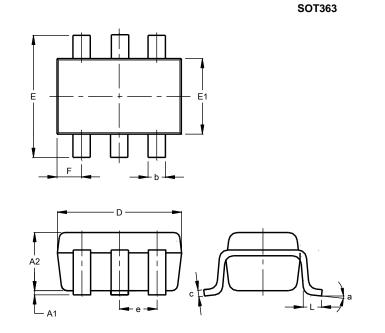
## Typical Curves – PNP Section (@T<sub>A</sub> = +25°C, unless otherwise specified.)





## **Package Outline Dimensions**

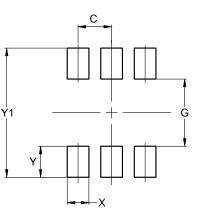
Please see http://www.diodes.com/package-outlines.html for the latest version.



	SOT363								
Dim	Min Max Typ								
A1	0.00	0.10	0.05						
A2	0.90	1.00	1.00						
b	0.10	0.30	0.25						
С	0.10	0.22	0.11						
D	1.80	2.20	2.15						
E	2.00	2.20	2.10						
E1	1.15	1.35	1.30						
е	C	).650 B	SC						
F	0.40	0.45	0.425						
L	0.25	0.40	0.30						
а	0°	8°							
All	Dimen	sions	in mm						

## **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.



Dimensions	Value (in mm)
С	0.650
G	1.300
Х	0.420
Y	0.600
Y1	2.500



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