



Product Summary

V _(BR) dss	R _{DS(ON)}	I _D T _A = 25°C		
	$30m\Omega @ V_{GS} = 10V$	13.8A		
40V	54mΩ @ V _{GS} = 4.5V	10.3A		

Description and Applications

This new generation MOSFET has been designed to minimize the onstate resistance ($R_{DS(on)}$) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

- Backlighting
- DC-DC Converters
- Power management functions

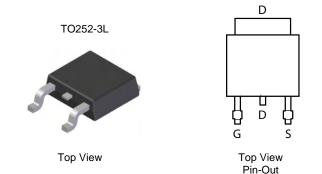
N-CHANNEL ENHANCEMENT MODE MOSFET

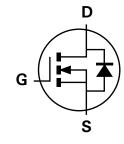
Features and Benefits

- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Lead Free By Design/RoHS Compliant (Note 1)
- "Green" Device (Note 2)
- Qualified to AEC-Q101 Standards for High Reliability

Mechanical Data

- Case: TO252-3L
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram Below
- Weight: 0.33 grams (approximate)





Equivalent Circuit

Ordering Information (Note 3)

Part Number	Case	Packaging
DMN4040SK3-13	TO252-3L	2500 / Tape & Reel

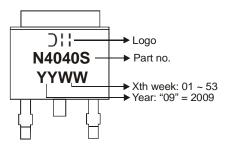
1. No purposefully added lead.

2. Diodes Inc.'s "Green" policy can be found on our website at http://www.diodes.com.

3. For packaging details, go to our website at http://www.diodes.com.

Marking Information

Notes:





Maximum Ratings @T_A = 25°C unless otherwise specified

Characteri	Symbol	Value 40	Unit V		
Drain-Source Voltage				V _{DSS}	
Gate-Source Voltage			V _{GSS}	±20	V
Continuous Drain Current (Note 4) V _{GS} = 10V	Steady State	T _A = 25°C T _A = 70°C	ID	6.0 4.8	А
Continuous Drain Current (Note 5) $V_{GS} = 10V$	Steady State	T _A = 25°C T _A = 70°C	I _D	9.3 7.4	А
Continuous Drain Current (Note 5) V _{GS} = 10V	t ≤ 10s	T _A = 25°C T _A = 70°C	Ι _D	13.8 11.0	А
Continuous Drain Current (Note 5) $V_{GS} = 4.5V$	Steady State	T _A = 25°C T _A = 70°C	ID	6.9 5.5	А
Continuous Drain Current (Note 5) $V_{GS} = 4.5V$	t ≤ 10s	T _A = 25°C T _A = 70°C	ID	10.3 8.2	А
Pulsed Drain Current (Note 6)			I _{DM}	50	А

Thermal Characteristics

Characteristic	Symbol	Max	Unit
Power Dissipation (Note 4)	PD	1.71	W
Thermal Resistance, Junction to Ambient $@T_A = 25^{\circ}C$ (Note 4)	R _{θJA}	72.9	°C/W
Power Dissipation (Note 5)	PD	4.1	W
Thermal Resistance, Junction to Ambient $@T_A = 25^{\circ}C$ (Note 5)	R _{0JA}	30.8	°C/W
Power Dissipation (Note 5) t \leq 10s	PD	8.9	W
Thermal Resistance, Junction to Ambient @T _A = 25°C (Note 5) t \leq 10s	R _{0JA}	14	°C/W
Operating and Storage Temperature Range	TJ, TSTG	-55 to +150	°C

Electrical Characteristics @ T_A = 25°C unless otherwise stated

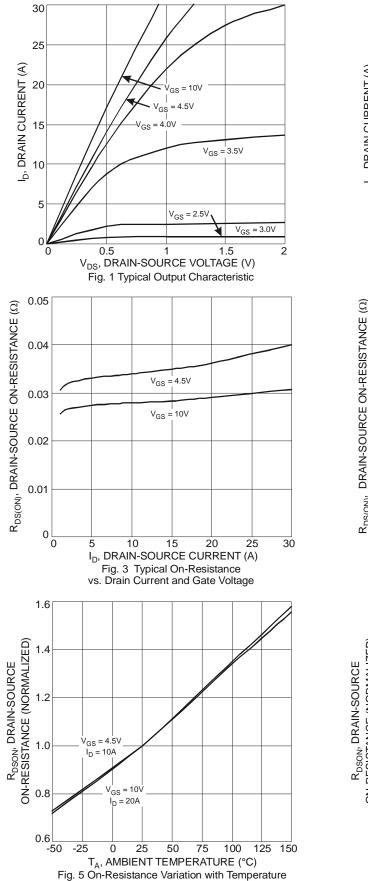
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)	Oymbol		Тур	Max	Onit		
Drain-Source Breakdown Voltage	BV _{DSS}	40	-	-	V	$V_{GS} = 0V, I_{D} = 250\mu A$	
Zero Gate Voltage Drain Current T _J = 25°C	I _{DSS}	-	-	1.0	μA	$V_{DS} = 40V, V_{GS} = 0V$	
Gate-Source Leakage	I _{GSS}	-	-	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)						· · · ·	
Gate Threshold Voltage	V _{GS(th)}	1.8	2.3	3.0	V	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	
Statia Drain Source On Desistance		-	20	30	m ()	V _{GS} = 10V, I _D = 12A	
Static Drain-Source On-Resistance	R _{DS (ON)}	-	43	54	mΩ	$V_{GS} = 4.5V, I_D = 6A$	
Forward Transfer Admittance	Y _{fs}	-	11	-	S	V _{DS} = 5V, I _D = 12A	
Diode Forward Voltage	V _{SD}	-	0.76	1.0	V	$V_{GS} = 0V, I_{S} = 1A$	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	Ciss	-	945	-			
Output Capacitance	Coss	-	69	-	pF	$V_{DS} = 20V$, $V_{GS} = 0V$, f = 1.0MHz	
Reverse Transfer Capacitance	C _{rss}	-	58	-			
Gate Resistance	Rg	-	1.45	-	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1MHz$	
Total Gate Charge V _{GS} = 4.5V	Qq	-	8.4	-		V _{GS} = 4.5V, V _{DS} = 20V, I _D = 12A	
Total Gate Charge V _{GS} = 10V	Qq	-	18.6	-			
Gate-Source Charge	Q _{gs}	-	3.3	-	nC	$V_{GS} = 10V, V_{DS} = 20V,$ $I_{D} = 12A$	
Gate-Drain Charge	Q _{gd}	-	2.2	-			
Turn-On Delay Time	t _{D(on)}	-	6.4	-	ns		
Turn-On Rise Time	tr	-	9.7	-	ns	V _{GS} = 10V, V _{DS} = 20V,	
Turn-Off Delay Time	t _{D(off)}	-	19.8	-	ns	$R_L = 1.6\Omega, R_G = 3\Omega$	
Turn-Off Fall Time	t _f	-	3.1	-	ns		

Notes: 4. Device mounted on FR-4 PCB with minimum recommended pad layout, single sided.

Device mounted of 1 "X 2" FR-4 PCB with high coverage 2 oz. Copper, single sided.
Repetitive rating, pulse width limited by junction temperature.
Short duration pulse test used to minimize self-heating effect.

8. Guaranteed by design. Not subject to production testing.





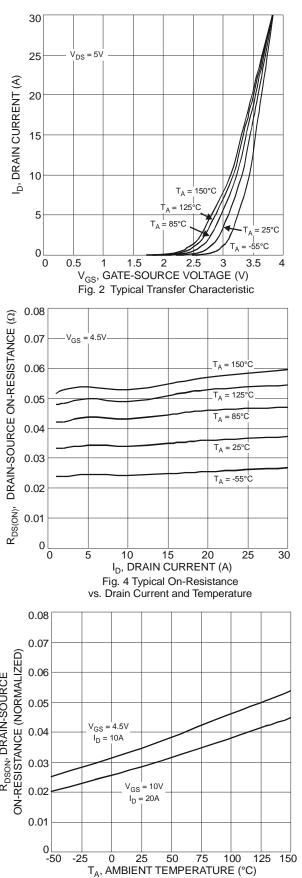
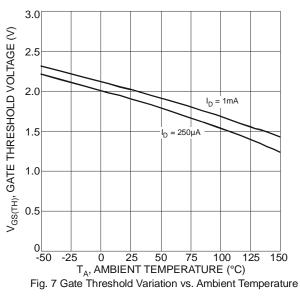
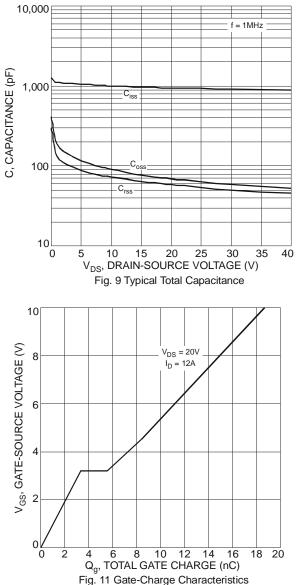
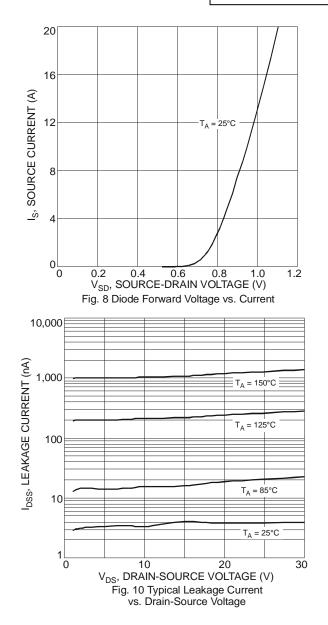


Fig. 6 On-Resistance Variation with Temperature

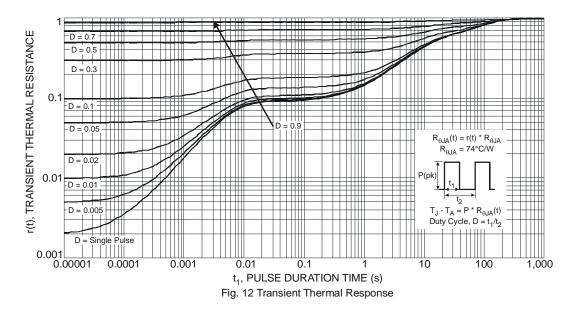




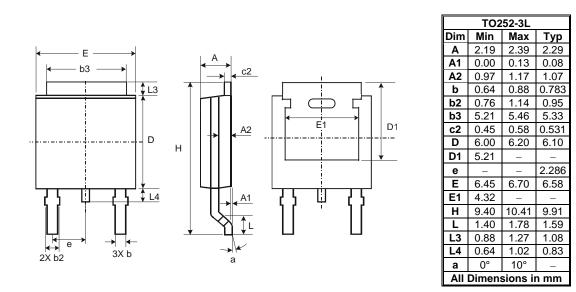




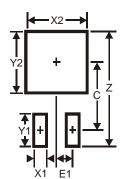




Package Outline Dimensions



Suggested Pad Layout



Dimensions	Value (in mm)
Z	11.6
X1	1.5
X2	7.0
Y1	2.5
Y2	7.0
C	6.9
E1	2.3



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