



Product Summary (Typ. @ V_{GS} = 4.5V, T_A = +25°C)

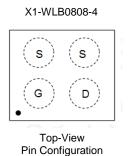
I	V _{DSS}	R _{DS(ON)}	Qg	Q_{gd}	ID
ſ	8V	35mΩ	9.6nC	0.9nC	4.0A

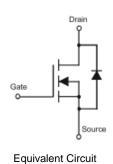
Description

The DMN1054UCB4 is a Trench MOSFET, engineered to minimize on-state losses and switch ultra-fast, making it ideal for high-efficiency power transfer. Using Chip-Scale Package (CSP) to increase power density by combining low thermal impedance with minimal R_{DS(ON)} per footprint area.

Applications

- **DC-DC** Converters
- **Battery Management**
- Load Switch





Ordering Information (Note 4)

Part Number	Case	Packaging
DMN1054UCB4-7	X1-WLB0808-4	3000/Tape & Reel

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. Notes:

2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information

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YW		
YM		

YW = Product Type Marking Code YM = Date Code Marking Y or \underline{Y} = Year (ex: D = 2016)

M or \overline{M} = Month (ex: 9 = September)

Date	Code	Key

Duie Coue hey												
Year	201	2	2013		2014	20	15	2016		2017	2	2018
Code	Z		А		В	(C	D		Е		F
Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D

N-CHANNEL ENHANCEMENT MODE MOSFET

Features

- Trench-CSP Technology with the Lowest on Resistance:
 - $R_{DS(ON)} = 35m\Omega$ to Minimize On-State Losses Q_g = 9.6nC for Ultra-Fast Switching
- V_{GS(TH)} = 0.6V Typ. for a Low Turn-On Potential
- CSP with Footprint 0.8mm × 0.8mm
- Height = 0.375mm for Low Profile
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

Mechanical Data

- Case: X1-WLB0808-4
- Terminal Connections: See Diagram Below



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Drain-Source Voltage		V _{DSS}	8	V
Gate-Source Voltage	V _{GSS}	±5	V	
Continuous Source Current @ V_{GS} = 4.5V (Note 5)	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	ID	2.7 2.2	A
Continuous Source Current @ V_{GS} = 4.5V (Note 6)	I _D	4.0 3.2	А	
Pulsed Drain Current (Pulse duration 10µs, duty cycle ≤1%	6)	I _{DM}	8	A
Continuous Source-Drain Diode Current	I _S	0.74	A	
Pulse Diode Forward Current	I _{SM}	15	A	

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 5)	PD	0.74	W
Thermal Resistance, Junction to Ambient (Note 5)	R _{0JA}	169	°C/W
Total Power Dissipation (Note 6)	PD	1.34	W
Thermal Resistance, Junction to Ambient (Note 6)	R _{0JA}	93	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

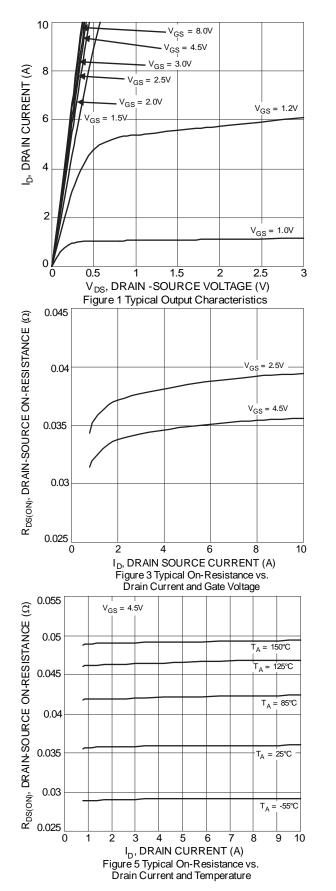
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV _{DSS}	8		—	V	$V_{GS} = 0V, I_D = 250 \mu A$
Zero Gate Voltage Drain Current	I _{DSS}	—		1.0	μA	$V_{DS} = 8V, V_{GS} = 0V$
Gate-Body Leakage	I _{GSS}	—		±100	nA	$V_{GS} = \pm 5V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V _{GS(TH)}	0.35	—	0.7	V	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$
Static Drain-Source On-Resistance	Rds(on)	_	35 38.5 46.4 53.3 64.7	42 50 65 80 110	mΩ	$ \begin{array}{l} V_{GS} = 4.5V, \ I_D = 1.0A \\ V_{GS} = 2.5V, \ I_D = 1.0A \\ V_{GS} = 1.8V, \ I_D = 0.5A \\ V_{GS} = 1.5V, \ I_D = 0.2A \\ V_{GS} = 1.2V, \ I_D = 0.1A \end{array} $
Forward Transfer Admittance	Y _{fs}	—	6.0	—	S	$V_{DS} = 6V, I_{S} = 1.0A$
Body Diode Forward Voltage	V _{SD}	—	0.7	1	V	$V_{GS} = 0V, I_{S} = 1.0A$
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	Ciss	—	698	908	pF	
Output Capacitance	Coss	_	97	127	pF	$V_{DS} = 6V, V_{GS} = 0V,$ f = 1.0MHz
Reverse Transfer Capacitance	C _{rss}	—	90	126	pF	1 = 1.00012
Gate Resistance	Rg	_	1.3	2.6	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1MHz$
Total Gate Charge	Qg	—	9.6	15	nC	
Gate-Source Charge	Q _{gs}	_	0.9	—	nC	$V_{GS} = 4.5V, V_{DS} = 6V,$
Gate-Drain Charge	Q _{qd}	_	0.9	_	nC	$I_D = 1.0A$
Turn-On Delay Time	t _{D(ON)}	_	5.2	10	ns	
Turn-On Rise Time	t _R	—	6.7	14	ns	$V_{DD} = 6V, I_D = 1.0A$
Turn-Off Delay Time	t _{D(OFF)}	—	16.6	32	ns	$V_{\text{GEN}} = 4.5 \text{V}, \text{ R}_{\text{G}} = 1\Omega, \text{ R}_{\text{L}} = 6\Omega$
Turn-Off Fall Time	tF	—	2	4	ns	
Reverse Recovery Charge	Q _{RR}	—	0.7	1.5	nC	
Body Diode Reverse Recovery Time	t _{RR}	—	6.9	14	ns	I _F = 1A, di/dt = 100A/μs

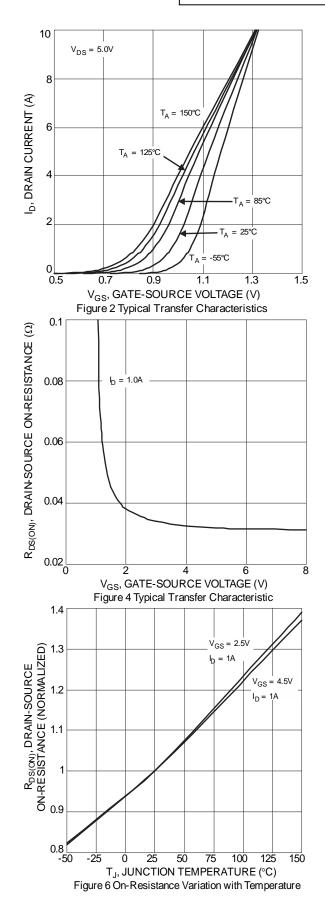
Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
 Device mounted on FR-4 substrate PC board, 2oz copper, with 1-inch square copper plate.
 Short duration pulse test used to minimize self-heating effect.

8. Guaranteed by design. Not subject to production testing.

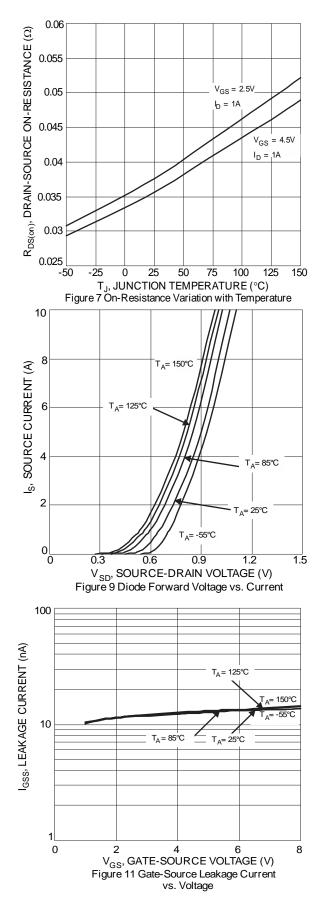


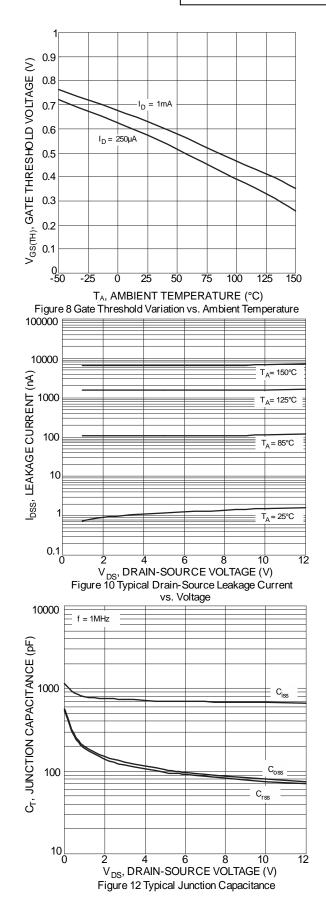
DMN1054UCB4





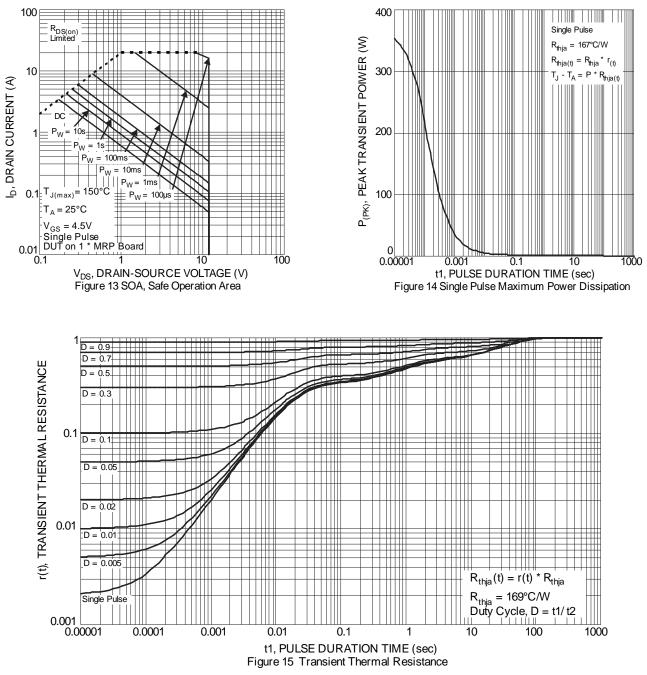








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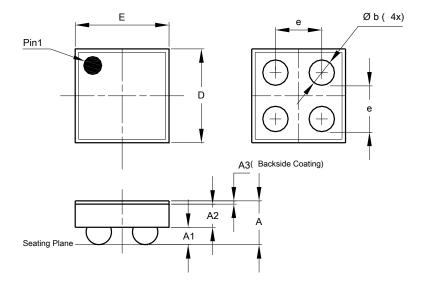




Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

X1-WLB0808-4

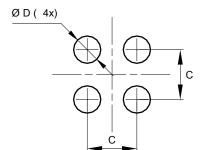


X1-WLB0808-4								
Dim Min Max Ty								
Α	0.3320	0.4180	0.3750					
A1	0.1350	0.1650	0.1500					
A2	0.1750	0.2250	0.2000					
A3	0.0220	0.0280	0.0250					
b	0.1971	0.2409	0.2190					
D	0.7900	0.8300	0.8100					
Е	0.7900	0.8300	0.8100					
е	e 0.400 BSC							
All	Dimens	ions in I	mm					

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

X1-WLB0808-4



Dimensions	Value (in mm)		
С	0.4000		
D	0.2190		



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