D2





20V DUAL N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

V _{(BR)DSS}	R _{DS(on) max}	Package	I _D T _A = +25°C
20V	$25mΩ$ @ V_{GS} = 4.5 V	SO-8	5.8A
200	35mΩ @ V _{GS} = 2.5V	5U-0	4.8A

Description

This new generation MOSFET has been designed to minimize the onstate resistance ($R_{DS(ON)}$) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

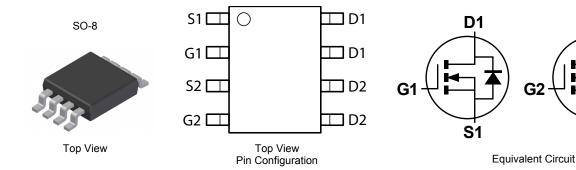
- DC-DC Converters
- Power Management Functions
- Backlighting

Features

- Low Input Capacitance
- Low On-Resistance
- Fast Switching Speed
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

Mechanical Data

- Case: SO-8
- Case Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram
- Terminals: Finish Tin Finish annealed over Copper leadframe.
 Solderable per MIL-STD-202, Method 208 ³
- Weight: 0.074 grams (approximate)



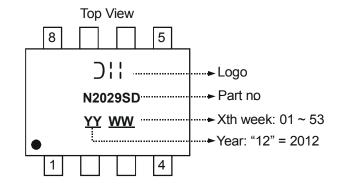
Ordering Information (Note 4)

Part Number	Case	Packaging
DMN2029USD-13	SO-8	2,500/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http"//www.diodes.com/products/packages.html.

Marking Information





Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Units
Drain-Source Voltage	V_{DSS}	20	V		
Gate-Source Voltage			V _{GSS}	±8	V
Continuous Dusin Courset (Nata C) V - 4 FV	Steady State	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	I _D	5.8 4.7	А
Continuous Drain Current (Note 6) V _{GS} = 4.5V	t < 10s	T _A = +25°C T _A = +70°C	I _D	6.9 5.7	А
Maximum Body Diode Forward Current (Note 6)			Is	2.1	Α
Pulsed Drain Current (10μs pulse, duty cycle = 1%)			I _{DM}	30	Α
Avalanche Current (Note 7) L = 0.1mH			I _{AS}	15	Α
Avalanche Energy (Note 7) L = 0.1mH			E _{AS}	11.2	mJ

Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Units
Total Power Dissipation (Note 5)	T _A = +25°C	Б	1.2	w
Total Power Dissipation (Note 5)	T _A = +70°C	P_{D}	0.7	
Thormal Booistanes, Junction to Ambient (Note 5)	Steady state	RθJA	115	°C/W
Thermal Resistance, Junction to Ambient (Note 5)	t < 10s	ROJA	70	
Total Dower Dissination (Note 6)	T _A = +25°C	0	1.4	W
Total Power Dissipation (Note 6)	T _A = +70°C	P _D	0.9	
Thermal Resistance, Junction to Ambient (Note 6)	Steady state	В	95	°C/W
Themal Resistance, Junction to Ambient (Note 6)	t < 10s	$R_{\theta JA}$	60	
Thermal Resistance, Junction to Case (Note 6)	$R_{ heta JC}$	14.5		
Operating and Storage Temperature Range		T _{J,} T _{STG}	-55 to +150	°C

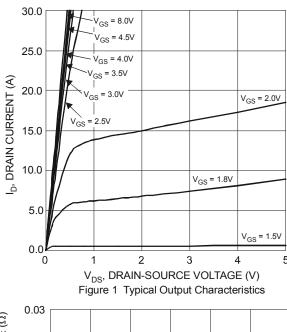
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

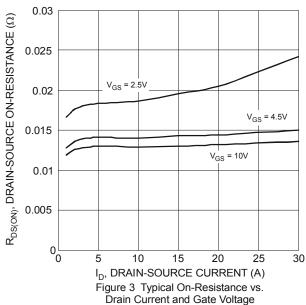
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition		
OFF CHARACTERISTICS (Note 8)								
Drain-Source Breakdown Voltage	BV _{DSS}	20		_	V	$V_{GS} = 0V, I_D = 250\mu A$		
Zero Gate Voltage Drain Current	I _{DSS}	-	_	1	μΑ	V _{DS} = 16V, V _{GS} = 0V		
Gate-Source Leakage	I _{GSS}	_	_	±100	nA	$V_{GS} = \pm 8V, V_{DS} = 0V$		
ON CHARACTERISTICS (Note 8)								
Gate Threshold Voltage	V _{GS(th)}	0.6		1.5	V	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$		
Static Drain-Source On-Resistance			14	25	mΩ	$V_{GS} = 4.5V, I_D = 6.5A$		
Static Drain-Source On-Nesistance	R _{DS(ON)}	1	19	35	11152	$V_{GS} = 2.5V, I_D = 5.4A$		
Forward Transfer Admittance	Y _{fs}	l	10	_	S	$V_{DS} = 5V$, $I_{D} = 6.5A$		
Diode Forward Voltage	V_{SD}	-	0.7	1.2	V	$V_{GS} = 0V, I_{S} = 1.3A$		
DYNAMIC CHARACTERISTICS (Note 9)								
Input Capacitance	Ciss	1	1171	_	pF	V _{DS} = 10V, V _{GS} = 0V f = 1.0MHz		
Output Capacitance	Coss	-	133	_				
Reverse Transfer Capacitance	C _{rss}	I	110	_		I = 1.0IVIHZ		
Gate Resistance	R_G	-	1.2	_	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1.0MHz$		
Total Gate Charge (V _{GS} = 4.5V)	Q_g	_	10.4	_		V _{DS} = 10V, I _D = 3A		
Total Gate Charge (V _{GS} = 8V)	Qg	-	18.6	_	nC			
Gate-Source Charge	Q_{gs}	_	1.9	_	IIC			
Gate-Drain Charge	Q_{gd}	_	2.3	_				
Turn-On Delay Time	t _{D(on)}	_	16.5	_	nS			
Turn-On Rise Time	t _r	_	33.3	_		$V_{GS} = 4.5V$, $V_{DD} = 10V$, $R_{GEN} = 6\Omega$, $I_{D} = 1A$		
Turn-Off Delay Time	$t_{D(off)}$	_	119.3	_	113			
Turn-Off Fall Time	t _f	_	53.5	_				
Body Diode Reverse Recovery Time	t _{rr}	_	7.5	_	nS	I _S = 6.5A, dl/dt = 100A/µs		
Body Diode Reverse Recovery Charge	Q _{rr}	-	2.0	_	nC	I _S = 6.5A, dl/dt = 100A/μs		

Notes:

- 5. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
- 6. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
- 7. I_{AR} and E_{AR} rating are based on low frequency and duty cycles to keep T_J = +25°C 8. Short duration pulse test used to minimize self-heating effect.
- 9. Guaranteed by design. Not subject to product testing.







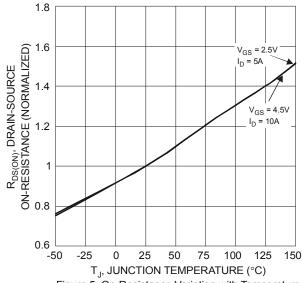
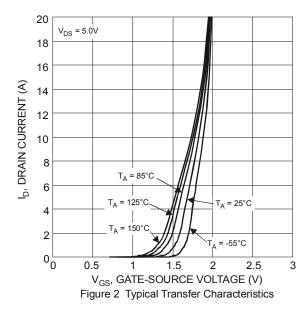
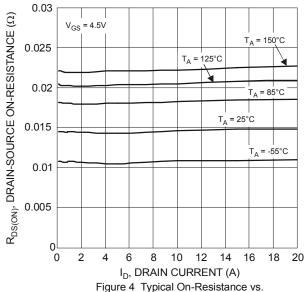


Figure 5 On-Resistance Variation with Temperature





Drain Current and Temperature 0.04 $R_{DS(ON)}$, DRAIN-SOURCE ON-RESISTANCE (Ω) 0.03 $V_{GS} = 2.5V$ $I_D = 5A$ I_D 0.02 $V_{GS} = 4.5V$ $I_D = 10A$ 0.01 25 -50 -25 0 50 75 100 125 T_J, JUNCTION TEMPERATURE (°C)



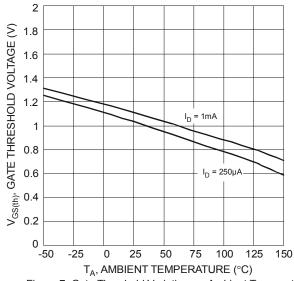
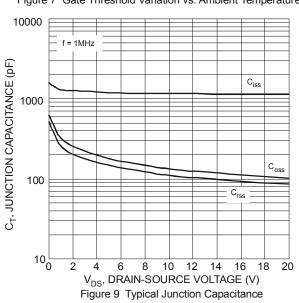
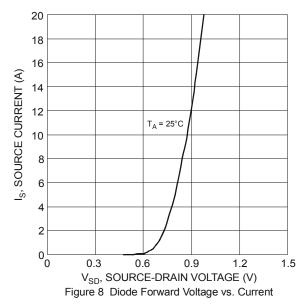
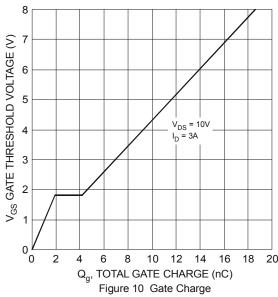


Figure 7 Gate Threshold Variation vs. Ambient Temperature







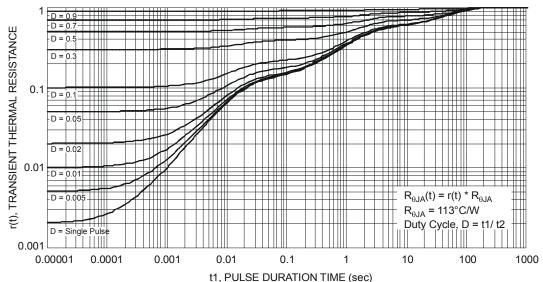
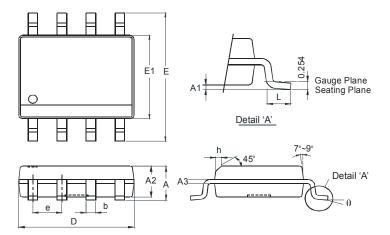


Figure 11 Transient Thermal Resistance



Package Outline Dimensions

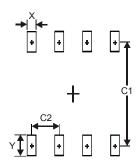
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.



SO-8				
Dim	Min	Max		
Α	ı	1.75		
A1	0.10	0.20		
A2	1.30	1.50		
A3	0.15	0.25		
b	0.3	0.5		
D	4.85	4.95		
Е	5.90	6.10		
E1	3.85	3.95		
е	1.27 Typ			
h	-	0.35		
٦	0.62	0.82		
θ	0°	8°		
All Dimensions in mm				

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)		
X	0.60		
Υ	1.55		
C1	5.4		
C2	1.27		



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