

PSE Technology Corporation

SPECIFICATION FOR APPROVAL

CUSTOMER	
NOMINAL FREQUENCY	50.000000 MHz
PRODUCT TYPE	TYPE FN 7.0x5.0 SEAM SEALED CRYSTAL CLOCK OSCILLATOR
SPEC. NO. (P/N)	FN5000109
CUSTOMER P/N	
ISSUE DATE	November 11, 2011
VERSION	E

APPROVED	PREPARED	QA	
Brenda	Vikloi Lu	Canthur	
APPROVED BY	APPROVED BY CUSTOMER:		
Please return one copy v			

PSE Technology Corporation

No.2, Tzu-Chiang 5th Rd, Chung Li Industrial Park, Chung Li City, Taoyuan County, Taiwan (R.O.C.)

TEL: 886-3-451-8888 FAX: 886-3-461-3865

http://www.saronix-ecera.com.tw

*Pb-free

*RoHS Compliant

*HF-Halogen Free

*REACH Compliant



*** A company of PERICOM Semiconductor Corporation ***

E0-R-4-014 Rev. E Page i

FN5000109

VER. E 11-Nov-11

VERSION HISTORY

Version No.	Version Date	Customer Receipt Date	Supplier Receipt Date	Description	Notes
А	Apr.27,2009			Initial Release	
В	Apr.29,2010			Change Output Wave form TR/TF to TF/TR Position	
С	Aug.10,2010			Changed Logo	
D	Feb.11,2011			Revised format	
E	Nov.11,2011			Added Start Up Time: 10ms max & Changed Mechanical Drawings	



TYPE FN 7.0x5.0 SEAM SEALED CRYSTAL CLOCK OSCILLATOR FN5000109 VER. E 11-Nov-11

ELECTRICAL SPECIFICATIONS

SRe Part Number: FN5000109

Item	Symbol	Specifications	Units	Notes
Nominal Frequency	Fo	50.000000	MHz	
Frequency Stability	FT	± 25	ppm	**See note
Operating Temperature Range	TR	-20 to +70	°C	
Supply Voltage	V_{DD}	+3.3 ± 10.0%	V	
Logic Type	LT	LVCMOS		
Supply Current, Output Enabled	I _{DD} /OE	25	mA	Max.
Supply Current, Output Disabled	I _{DD} /OD	10	μΑ	Max.
Duty Cycle (Symmetry)	DC/SY	45 / 55	%	Measured 50% of Waveform
Rise / Fall Time	T_R/T_F	7	ns	Max. measured 20/80% of Waveform
Output Voltage "0" Level	V _{OL}	10% V _{DD}	V	Max.
Output Voltage "1" Level	V _{OH}	90% V _{DD}	V	Min.
Output Load	CL	15	pF	Max
Jitter, Phase	RMS(1-σ)	1.5	ps	Max, 12KHz ~ 20MHz Frequency Band
Jitter, Accumulated	RMS(1-σ)	5	ps	Max, 20,000 Consecutive Periods
Jitter, Peak to Peak	Pk-Pk	50	ps	Max, 100,000 Random Periods
Start Up Time		10	ms	Max.
Storage Temperature Range		-55°C to +125°C	°C	

* This product doesn't include harmful substance that stipulated by SONY SS-00259 Level 1 and S-AT2-001 Level 1 standard. RoHS Compliant (Pb - Free).

Output Enable / Disable Function

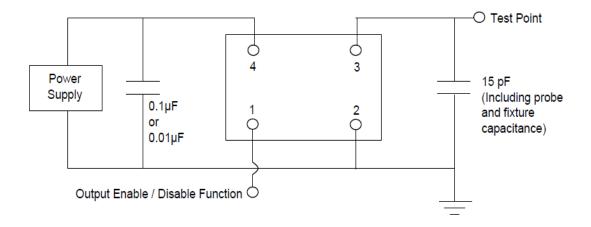
Parameter	Min.	Тур.	Max.	Units	Notes
Input Voltage (Pin1), Output Enable	0.7V _{DD}			V	Or Open
Input Voltage (Pin1), Output Disable (low power standby)			$0.3V_{DD}$	V	Output is Hi-Z
Internal Pullup Resistance	30			ΚΩ	
Output Disable Delay			200	ns	
Output Enable Delay			2	ms	

^{**}Stability includes all combinations of Operating Temperature, Load changes, rated Input (Supply) Voltage changes, Initial Calibration Tolerance (25°C), Aging (1 year at 25°C Average Effective Ambient Temperature), Shock and Vibration.

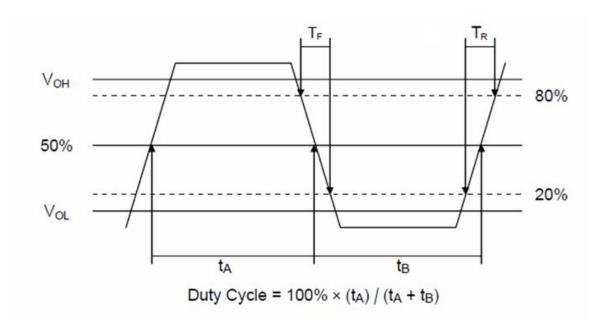
FN5000109

VER. E 11-Nov-11

TEST CIRCUIT



OUTPUT WAVEFORM





FN5000109

VER. E 11-Nov-11

RELIABILITY SPECIFICATIONS

ENVIRONMENTAL:

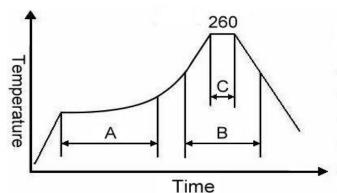
- a) THERMAL SHOCK: MIL-STD-883, Method 1011, Condition A
- b) MOISTURE RESISTANCE: MIL-STD-883, Method 1004
- c) VIBRATION: MIL-STD-883, Method 2007, Condition A
- d) RESISTANCE TO SOLDERING HEAT: J-STD-020D Table 5-2 Pb-free devices (except 2 cycles max)
- e) HAZARDOUS SUBSTANCE: Pb free and RoHS Compliant.

MECHANICAL:

- a) SHOCK: MIL-STD-883, Method 2002, Condition B
- b) SOLDERABILITY: JESD22-B102-D Method 2 (Preconditioning E)
- c) TERMINAL STRENGTH: MIL-STD-883, Method 2004, Test Condition D
- d) GROSS LEAK: MIL-STD-883, Method 1014, Condition C
- e) FINE LEAK: MIL-STD-883, Method 1014, Condition A2, R1=2x10⁻⁸ atm cc/s
- f) SOLVENT RESISTANCE: MIL-STD-202, Method 215

SUGGESTED IR REFLOW PROFILE

*As per IPC-JEDEC J-STD-020D

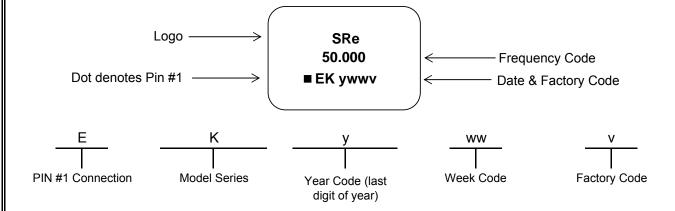


Note.						
	Stage	Temperature	Time			
Α	Preheat	150~200°C	60~120 Sec			
В	Primary Heat	217°C	60~150 Sec			
С	Peak	260°C	10 Sec			

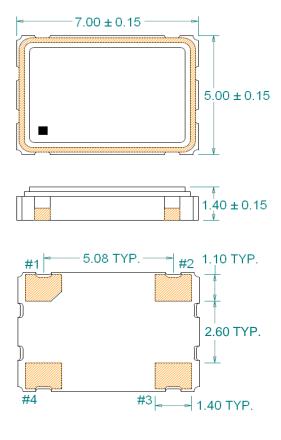
For soldering reflow profile and reliability test ratings go to: http://www.pericom.com/pdf/sre/reflow.pdf

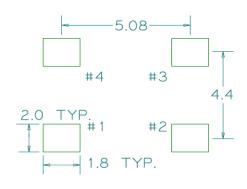
VER. E 11-Nov-11

MARKING



MECHANICAL DRAWINGS (Scale: None. Dimensions are in mm.)





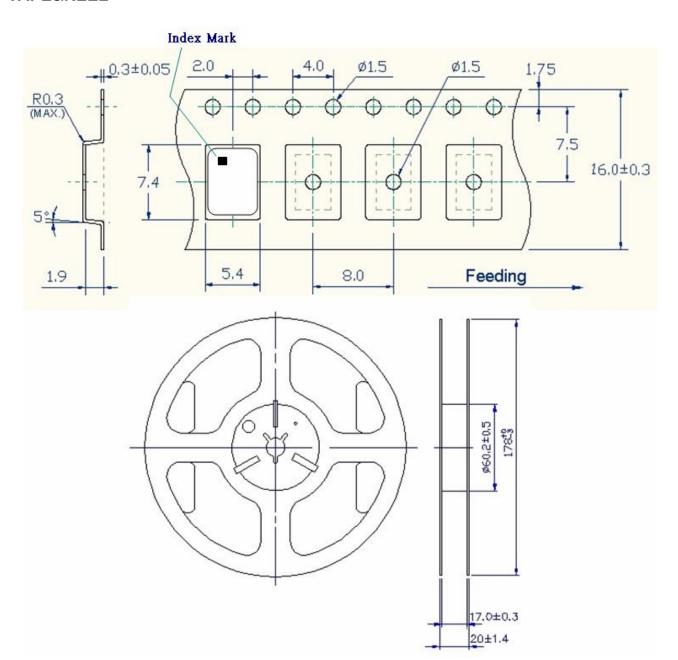
*External high-frequency power decoupling is recommended.(see test circuit for minimum recommendation). To ensure optimal performance, do not route traces beneath the package.

Pin	Function
1	OE
2	Ground
3	Clock Output
4	V_{DD}

FN5000109

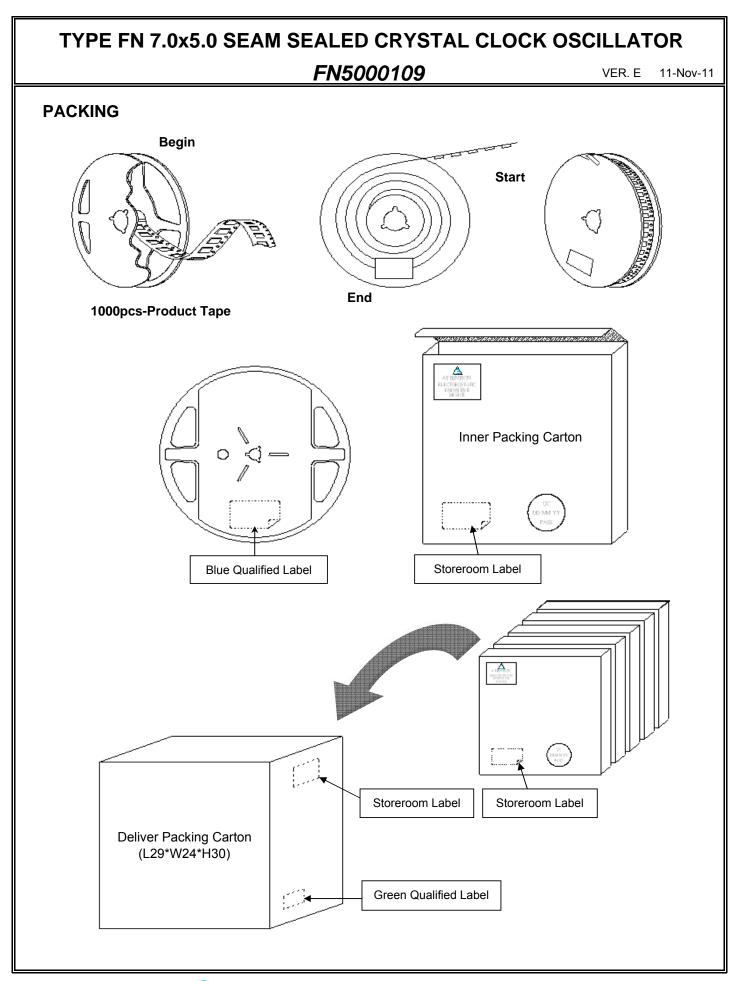
VER. E 11-Nov-11

TAPE&REEL



- 1. 230mm minimum leafer which consist of carrier and/or tape followed by a minimum of 160mm of empty carrier tape sealed with cover tape.
- 2. 160mm minimum trailer of empty carrier tape sealed with cover tape.







E0-R-4-014 Rev. E Page 6

单击下面可查看定价,库存,交付和生命周期等信息

>>Diodes Incorporated(达迩科技(美台))