



30V N-CHANNEL ENHANCEMENT MODE MOSFET H-BRIDGE

Product Summary

BV _{DSS}	R _{DS(ON)} Max	I _D Max T _A = +25°C	
30V	$10m\Omega @ V_{GS} = 10V$	13A	
307	$15m\Omega @ V_{GS} = 4.5V$	11A	

Description

This MOSFET is designed to minimize the on-state resistance $(R_{DS(ON)})$, yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

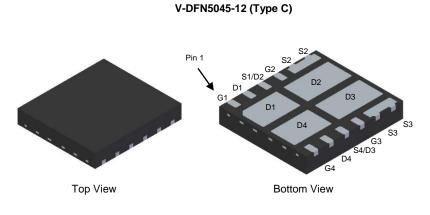
• High-Efficiency Bridge Rectifiers

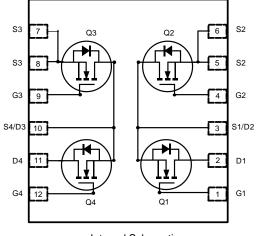
Features

- Low On-Resistance
- Low Input Capacitance
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

Mechanical Data

- Case: V-DFN5045-12
- Case Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish NiPdAu over Copper Leadframe. Solderable per MIL-STD-202, Method 208 @
- Weight: 0.056grams (Approximate)





Internal Schematic Top View

Ordering Information (Note 4)

	Part Number	Case	Quantity per Reel		
DMHT3006LFJ-13 V-E		V-DFN5045-12 (Type C)	3000		
Notos:	atos: 1 No purposoly added load Fully FLI Directive 2002/05/FC (PoHS) 2011/65/FLI (PoHS 2) & 2015/862/FLI (PoHS 2) compliant				

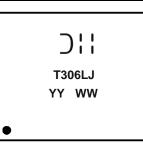
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.

 See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.</p>

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



);; = Manufacturer's Marking T306LJ = Product Type Marking Code YYWW = Date Code Marking YY = Year (ex: 18 = 2018) WW = Week (01 to 53)



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit		
Drain-Source Voltage		V _{DSS}	30	V	
Gate-Source Voltage			V _{GSS}	±20	V
Continuous Drain Current (Note 6) $V_{GS} = 10V$ State State T _A = +25°C T _A = +70°C			I _D	13 10	A
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)			I _{DM}	80	A
Continuous Source-Drain Diode Current (Note 6) T _A = +25°C			I _S	2	A
Avalanche Current (Note 7) L = 0.1mH			I _{AS}	23	A
Avalanche Energy (Note 7) L = 0.1mH			E _{AS}	28	mJ

Thermal Characteristics

Characteristic	Symbol	Value	Unit	
Total Power Dissipation (Note 5)	T _A = +25°C	PD	1.3	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	R _{0JA}	95	°C/W
Total Power Dissipation (Note 6)	T _A = +25°C	PD	2.1	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	R _{0JA}	60	°C/W
Thermal Resistance, Junction to Case (Note 6)		R _{0JC}	22	°C/W
Operating and Storage Temperature Range		T _{J,} T _{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

	_						
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 8)							
Drain-Source Breakdown Voltage	BV _{DSS}	30	_	—	V	$V_{GS} = 0V, I_D = 250 \mu A$	
Zero Gate Voltage Drain Current ($T_J = +25^{\circ}C$)	I _{DSS}	—	_	1	μA	$V_{DS} = 24V, V_{GS} = 0V$	
Gate-Source Leakage	I _{GSS}	—	_	±100	nA	$V_{GS} = +20V, V_{DS} = 0V$ $V_{GS} = -16V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 8)							
Gate Threshold Voltage	V _{GS(TH)}	1.0	_	3.0	V	$V_{DS} = V_{GS}$, $I_D = 250 \mu A$	
Static Drain-Source On-Resistance			5.8	10	mΩ	$V_{GS} = 10V, I_D = 10A$	
Static Diani-Source Off-Resistance	R _{DS(ON)}	_	7.8	15	11122	$V_{GS} = 4.5V, I_D = 8A$	
Diode Forward Voltage	V _{SD}	—	0.7	1.0	V	$V_{GS} = 0V, I_S = 2A$	
DYNAMIC CHARACTERISTICS (Note 9)							
Input Capacitance	C _{iss}	—	1171	—		$V_{DS} = 15V, V_{GS} = 0V,$ f = 1.0MHz	
Output Capacitance	C _{oss}	_	421	—	pF		
Reverse Transfer Capacitance	Crss	—	63	—		1 = 1.000HZ	
Gate Resistance	R _g	—	1.9	—	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1.0MHz$	
Total Gate Charge (V _{GS} = 4.5V)	Qg	_	9.0	—			
Total Gate Charge (V _{GS} = 10V)	Qg	_	17	—	nC	Vסע = 15V. א = 9A	
Gate-Source Charge	Q _{gs}	—	2.7	—	ne	$v_{DD} = 15v, I_D = 9A$	
Gate-Drain Charge	Q_{gd}	_	4.7	—			
Turn-On Delay Time	t _{D(ON)}	—	7.4	—			
Turn-On Rise Time	t _R	_	54	—	ns	$V_{DD} = 15V, V_{GS} = 10V,$	
Turn-Off Delay Time	t _{D(OFF)}	—	16	—	115	$R_g = 3\Omega, I_D = 9A$	
Turn-Off Fall Time	t _F	_	4.3	_			
Reverse Recovery Time	t _{RR}	_	18	—	ns		
Reverse Recovery Charge	Q _{RR}	—	8.5	—	nC	I _F = 1.5A, di/dt = 100A/μs	

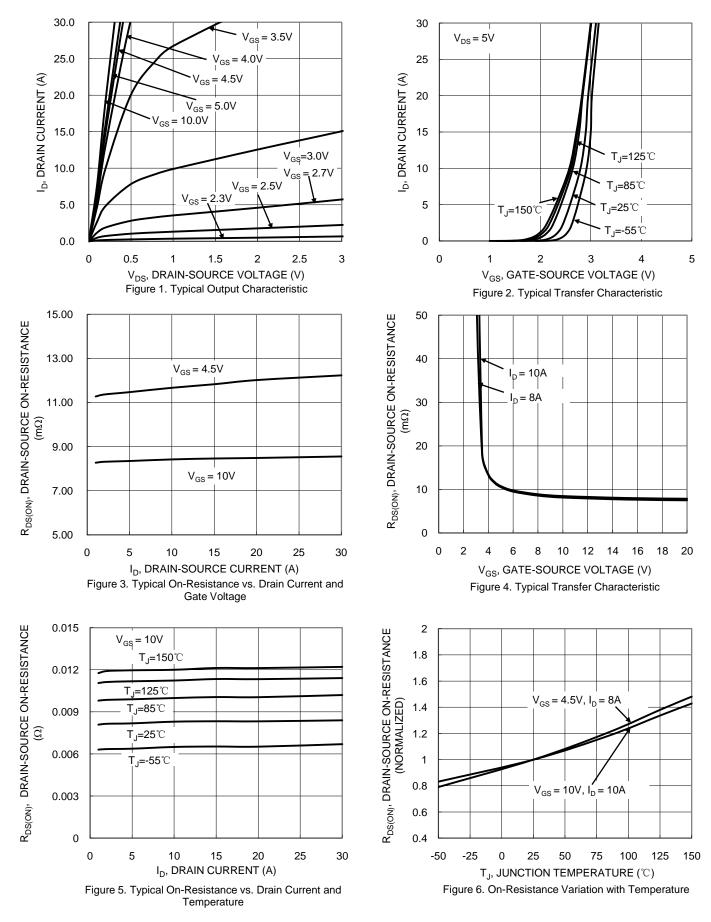
 Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
 Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate. Notes:

7. I_{AS} and E_{AS} ratings are based on low frequency and duty cycles to keep T_J = +25°C.

8. Short duration pulse test used to minimize self-heating effect.
 9. Guaranteed by design. Not subject to product testing.

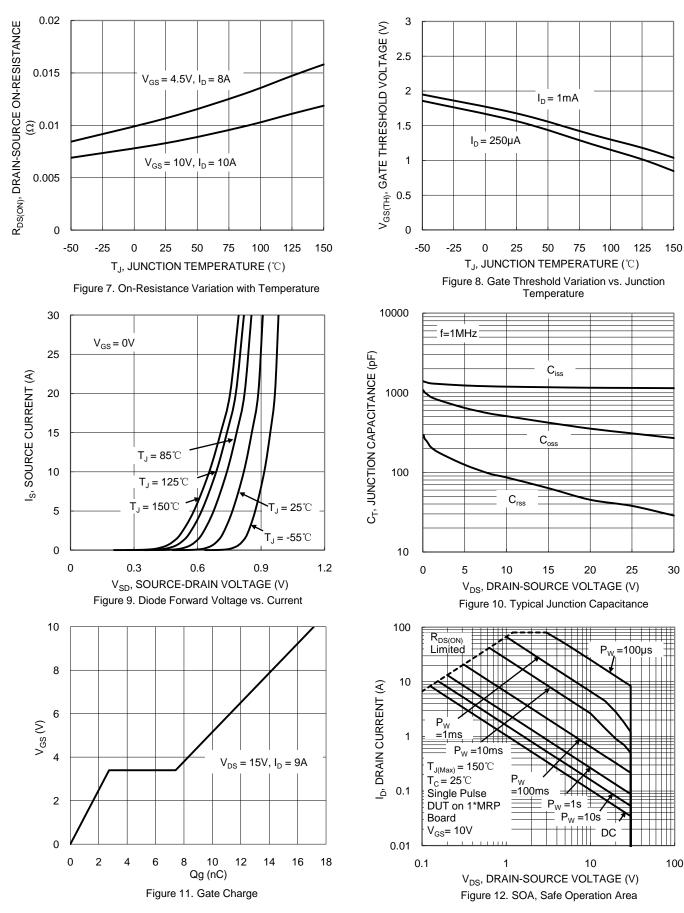


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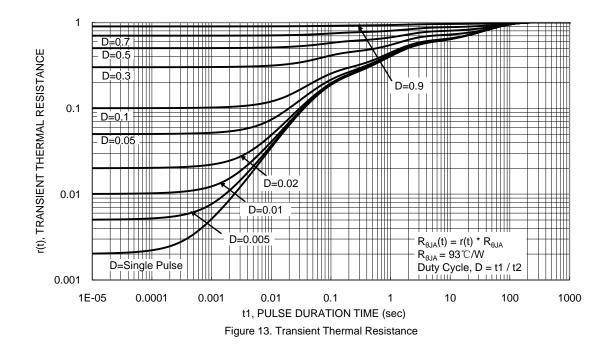


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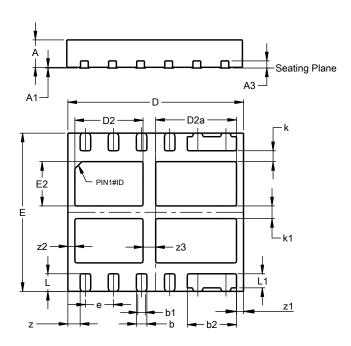






Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.



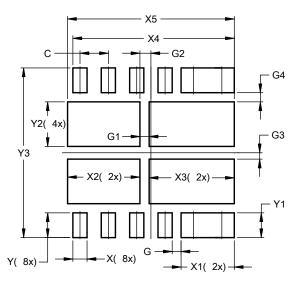
V-DFN5045-12	(Type	C)
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	V-DFN5045-12 (Type C)				
Dim	Min	Max	Тур		
Α	0.75	0.85	0.80		
A1	0.00	0.05	0.02		
A3		-	0.203		
b	0.25	0.35	0.30		
b1	0.17	0.27	0.22		
b2	1.35	1.45	1.40		
D	4.95	5.05	5.00		
D2	1.84	2.04	1.94		
D2a	2.20	2.40	2.30		
е	-	-	0.80		
ш	4.45	4.55	4.50		
E2	1.16	1.36	1.26		
k			0.31		
k1	-	-	0.36		
L	0.45	0.55	0.50		
L1	0.35	0.45	0.40		
Z			0.35		
z1			0.20		
z2			0.20		
z3			0.36		
All Dimensions in mm					

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

V-DFN5045-12 (Type C)



Dimensions	Value
Dimensions	(in mm)
С	0.800
G	0.250
G1	0.260
G2	0.310
G3	0.180
G4	0.260
Х	0.400
X1	1.500
X2	2.040
X3	2.400
X4	4.550
X5	4.700
Y	0.700
Y1	0.700
Y2	1.260
Y3	4.800



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