

NOT RECOMMENDED FOR NEW DESIGN **USE DMP2120U**



DMP2225L

P-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

| BV _{DSS} | R _{DS(ON)} | Package | I _D T _A = +25°C |
|-------------------|---------------------------------|---------|--|
| -20V | 110mΩ @ V _{GS} = -4.5V | SOT23 | -2.6A |
| -20V | 225mΩ @ $V_{GS} = -2.5V$ | 30123 | -2.0A |

Description

This new generation MOSFET is designed to minimize the on-state resistance (R_{DS(ON)}) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

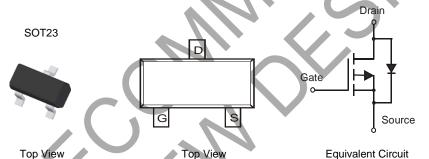
- General Purpose Interfacing Switch
- Power Management Functions

Features

- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- PPAP Capable (Note 4)

Mechanical Data

- Case: SOT23
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 @3
- Terminal Connections: See Diagram
- Weight: 0.008 grams (Approximate)



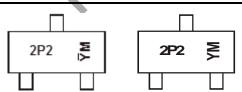
Ordering Information (Note 5)

| Part Number | Qualification | Case | Packaging |
|-------------|---------------|--------|------------------|
| DMP2225L-7 | Standard | SOT-23 | 3000/Tape & Reel |
| DMP2225LQ-7 | Automotive | SOT-23 | 3000/Tape & Reel |

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green"
- Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. Automotive products are AEC-Q101 qualified and are PPAP capable. Automotive, AEC-Q101 and standard products are electrically and thermally the ame, except where specified. For more information, please refer to http://www.diodes.com/quality/product_grade_definitions/.
- 5. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/

Marking Information



2P2 = Product Type Marking Code

YM = Date Code Marking for SAT (Shanghai Assembly/ Test site) YM = Date Code Marking for CAT (Chengdu Assembly/ Test site) Y or \overline{Y} = Year (ex: E = 2017)

M = Month (ex: 9 = September)

Date

| | Chengdu A/T Site | e Shan | ghai A/T Site | | | | | |
|------------|------------------|--------|---------------|------|------|------|------|---|
| e Code Key | | | | | | | | |
| Year | 2008 | 2009 | ~ | 2017 | 2018 | 2019 | 2020 | Γ |
| Code | V | W | ~ | F | F | G | Н | Γ |

2021 Month Feb Mar Aug Dec Jan Apr May Jun Jul Sep Oct Nov Code D

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Maximum Ratings ($@T_A = +25^{\circ}C$, unless otherwise specified.)

| Characteri | | Symbol | Value | Unit | |
|---|--|----------------|------------------|------|---|
| Drain-Source Voltage | | | V_{DSS} | -20 | V |
| Gate-Source Voltage | | | V _{GSS} | ±12 | V |
| Continuous Drain Current (Note 6) Steady $T_A = +25^{\circ}C$ State $T_A = +70^{\circ}C$ | | I _D | -2.6 -2 | А | |
| Pulsed Drain Current (Note 7) | | | I _{DM} | 8 | Α |

Thermal Characteristics

| Characteristic | Symbol | Value | Unit |
|--|------------------|-------------|------|
| Total Power Dissipation (Note 6) | P _D | 1.08 | W |
| Thermal Resistance, Junction to Ambient @T _A = +25°C (Note 6) | $R_{\theta JA}$ | 115 | °C/W |
| Operating and Storage Temperature Range | $T_{J_i}T_{STG}$ | -55 to +150 | °C |

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

| Characteristic | Symbol | Min | Тур | Max | Unit | Test Condition |
|-------------------------------------|----------------------|------------|-----|-------|-------|--|
| OFF CHARACTERISTICS (Note 8) | | | | | | |
| Drain-Source Breakdown Voltage | BV _{DSS} | -20 | | _ | V | $V_{GS} = 0V, I_D = -250\mu A$ |
| Zero Gate Voltage Drain Current | I _{DSS} | _ | _ | -800 | nΑ | $V_{DS} = -20V, V_{GS} = 0V$ |
| On-State Drain Current | | -6 | | _ | Α | $V_{DS} \le -5V$, $V_{GS} = -4.5V$ |
| On-State Drain Current | I _{D(ON)} | -3 | - | _ | | $V_{DS} \le -5V, V_{GS} = -2.5V$ |
| Gate-Source Leakage | I _{GSS} | 1 | _ | ±80 | nΑ | $V_{GS} = \pm 12V, V_{DS} = 0V$ |
| ON CHARACTERISTICS (Note 8) | | | | | | |
| Gate Threshold Voltage | V _{GS(TH)} | -0.45 | _ | -1.25 | V | $V_{DS} = V_{GS}, I_{D} = -250 \mu A$ |
| Static Drain-Source On-Resistance | | . 3 | 80 | 110 | mΩ | $V_{GS} = -4.5V, I_D = -2.6A$ |
| Static Dialif-Source Off-Resistance | R _{DS} (ON) | _ ` | 165 | 225 | 11122 | $V_{GS} = -2.5V, I_D = -2.0A$ |
| Forward Transfer Admittance | Y _{fs} | _ | 4 | | S | $V_{DS} = -5V, I_{D} = -2.6A$ |
| Diode Forward Voltage (Note 7) | V_{SD} | V – | _ | -1.26 | V | $V_{GS} = 0V, I_S = -2.6A$ |
| DYNAMIC CHARACTERISTICS (Note 9) | | • | | | | |
| Input Capacitance | C _{iss} | _ | 250 | _ | pF | 10)/)/ |
| Output Capacitance | Coss | _ | 88 | _ | pF | $V_{DS} = -10V, V_{GS} = 0V$ f = 1.0MHz |
| Reverse Transfer Capacitance | C_{rss} | _ | 58 | | рF | 1 = 1.000112 |
| Gate Resistance | R_{g} | _ | 12 | 16 | Ω | $V_{GS} = 0V$, $V_{DS} = 0V$, $f = 1MHz$ |
| Total Gate Charge | Qg | _ | 4.3 | 5.3 | • | 45)/)/ 40)/ |
| Gate-Source Charge | Q_{gs} | | 0.9 | | nC | $V_{GS} = -4.5V, V_{DS} = -10V,$ $I_{D} = -2.7A$ |
| Gate-Drain Charge | Q_gd | _ | 2.1 | _ | | ID = -2.7 A |

Notes:

- 6. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
 7. Repetitive rating, pulse width limited by junction temperature.
 8. Short duration pulse test used to minimize self-heating effect.
 9. Guaranteed by design. Not subject to production testing.



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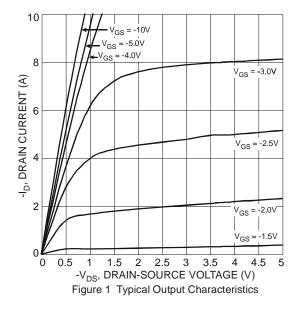
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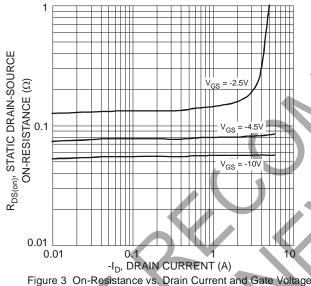
V_{DS} = -5V

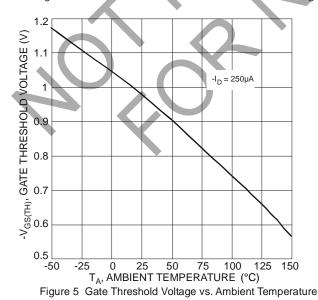
Pulsed

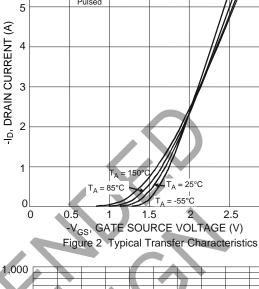
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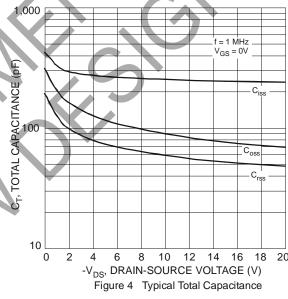
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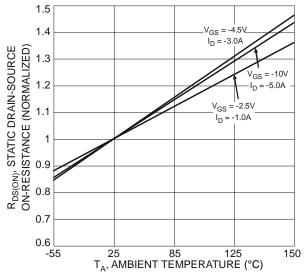


Figure 6 Normalized Static Drain-Source On-Resistance vs. Ambient Temperature







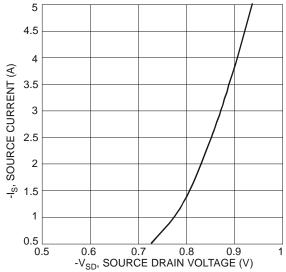
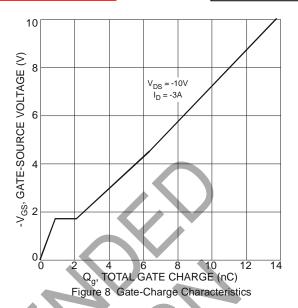
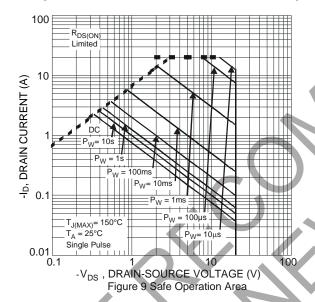
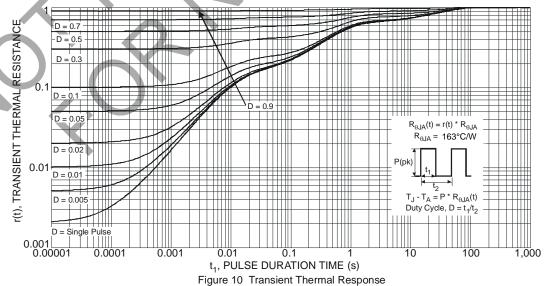


Figure 7 Reverse Drain Current vs. Source-Drain Voltage





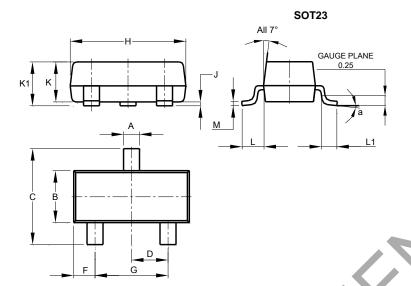


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Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

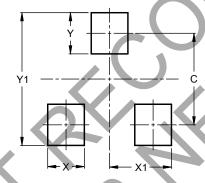


| SOT23 | | | | | |
|----------------------|-------|-------|-------|--|--|
| Dim | Min | Max | Тур | | |
| Α | 0.37 | 0.51 | 0.40 | | |
| В | 1.20 | 1.40 | 1.30 | | |
| C | 2.30 | 2.50 | 2.40 | | |
| D | 0.89 | 1.03 | 0.915 | | |
| F | 0.45 | 0.60 | 0.535 | | |
| G | 1.78 | 2.05 | 1.83 | | |
| H | 2.80 | 3.00 | 2.90 | | |
| 7 | 0.013 | 0.10 | 0.05 | | |
| K | 0.890 | 1.00 | 0.975 | | |
| K1 | 0.903 | 1.10 | 1.025 | | |
| L | 0.45 | 0.61 | 0.55 | | |
| L1 | 0.25 | 0.55 | 0.40 | | |
| М | 0.085 | 0.150 | 0.110 | | |
| а | ° | 8° | | | |
| All Dimensions in mm | | | | | |

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT23



| Dimensions | Value (in mm) |
|------------|---------------|
| С | 2.0 |
| Х | 0.8 |
| X1 | 1.35 |
| Υ | 0.9 |
| Y1 | 2.9 |



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