



#### N-CHANNEL ENHANCEMENT MODE MOSFET

#### **Product Summary**

V <sub>(BR)</sub> dss	R <sub>DS(on)</sub> max	l <sub>D</sub> max T <sub>A</sub> = +25°C
60V	5Ω @ V <sub>GS</sub> = 10V	115mA

# **Description and Applications**

This MOSFET is designed to minimize the on-state resistance (R<sub>DS(on)</sub>) and yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

- Load switches
- Power management functions
- Motor controls
- **PWM** applications

#### **Features**

- Low On-Resistance
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Small Surface Mount Package
- ESD Protected Gate, 1KV (HBM)
- Totally Lead-Free & Fully RoHS compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please contact us or your local Diodes representative.

https://www.diodes.com/quality/product-definitions/

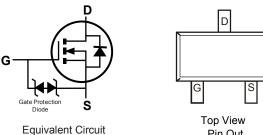
#### Mechanical Data

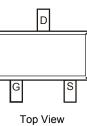
- Case: SOT523
- Case Material: Molded Plastic. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020D
- Terminals: Solderable per MIL-STD-202, Method 208 @3)
- Lead Free Plating (Matte Tin Finish annealed over Alloy 42 leadframe).
- Weight: 0.002 grams (approximate)





Top View





# Pin Out

#### Ordering Information (Note 4)

Part Number	Case	Packaging
DMN66D0LT-7	SOT523	3000/Tape & Reel

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. Notes:

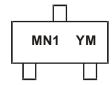
2. See https://www.diodes.com/guality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.



### **Marking Information**



MN1 = Product Type Marking Code YM = Date Code Marking Y or  $\overline{Y}$ = Year ex: I = 2021 M = Month ex: 9 = September

Dale Code Key												
Year	2008		2021	2022	2023	2024	2025	2026	2027	2028	2029	2030
Code	V			J	К	L	М	N	0	Р	R	S
				-					-			•
	.lan	1	Mar	Apr	Mav	Jun		-	-	Oct	Nov	Dec
Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec

#### **Maximum Ratings** (@ T<sub>A</sub> = +25°C unless otherwise specified)

	Characteristic	Symbol	Value	Units
Drain-Source Voltage		V <sub>DSS</sub>	60	V
Gate-Source Voltage	Continuous	Continuous	±20	V
Drain Current (Note 5)	Continuous Continuous @ +100°C Pulsed		115 73 800	mA

#### Thermal Characteristics (@ T<sub>A</sub> = +25°C unless otherwise specified)

Characteristic	Symbol	Value	Units
Total Power Dissipation (Note 5)	PD	200	mW
Thermal Resistance, Junction to Ambient (Note 5)	R <sub>0JA</sub>	625	°C/W
Operating and Storage Temperature Range	TJ, TSTG	-55 to +150	°C

### Electrical Characteristics (@ T<sub>A</sub> = +25°C unless otherwise specified)

Characteristic		Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 6)							
Drain-Source Breakdown Voltage		BV <sub>DSS</sub>	60	70		V	$V_{GS} = 0V, I_D = 10\mu A$
Zero Gate Voltage Drain Current	@ T <sub>C</sub> = +25°C @ T <sub>C</sub> = +125°C	I <sub>DSS</sub>	_		1.0 500	μA	V <sub>DS</sub> = 60V, V <sub>GS</sub> = 0V
Gate-Body Leakage		IGSS			±5	μA	$V_{GS} = \pm 20V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 6)							
Gate Threshold Voltage		V <sub>GS(th)</sub>	1.2	_	2.0	V	$V_{DS} = V_{GS}, I_D = 250 \mu A$
Statia Drain Sauras On Desistance	@ T <sub>C</sub> = +25°C			3.5	6	~	V <sub>GS</sub> = 5.0V, I <sub>D</sub> = 0.115A
Static Drain-Source On-Resistance	@ T <sub>C</sub> = +125°C	R <sub>DS(on)</sub>		3.0	5	Ω	V <sub>GS</sub> = 10V, I <sub>D</sub> = 0.115A
Forward Transconductance		<b>g</b> fs	80			mS	V <sub>DS</sub> = 10V, I <sub>D</sub> = 0.115A
DYNAMIC CHARACTERISTICS (Note 7)							
Input Capacitance		Ciss	_	23		pF	
Output Capacitance		Coss	_	3.4		pF	V <sub>DS</sub> = 25V, V <sub>GS</sub> = 0V, f = 1.0MHz
Reverse Transfer Capacitance		C <sub>rss</sub>		1.4		pF	
SWITCHING CHARACTERISTICS (Note 7)							
Turn-On Delay Time		t <sub>D(on)</sub>	_	10		ns	V <sub>DD</sub> = 30V, I <sub>D</sub> = 0.115A, R <sub>L</sub> = 150Ω,
Turn-Off Delay Time		t <sub>D(off)</sub>	_	33		ns	V <sub>GEN</sub> = 10V, R <sub>GEN</sub> = 25Ω

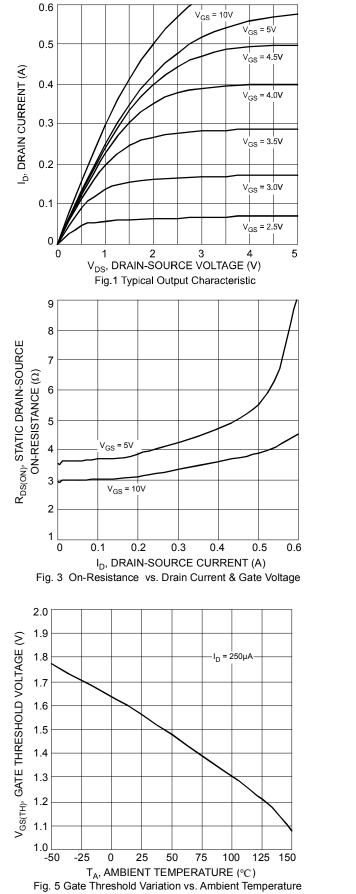
Notes: 5. Device mounted on 1" x 1" FR-4 PCB with high coverage 2oz. Copper, single sided.

6. Short duration pulse test used to minimize self-heating effect.

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## DMN66D0LT



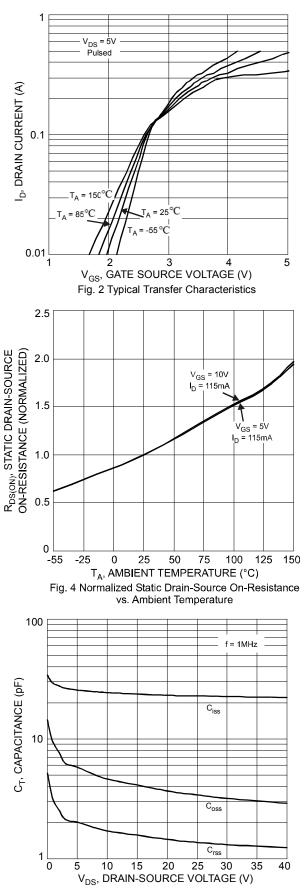
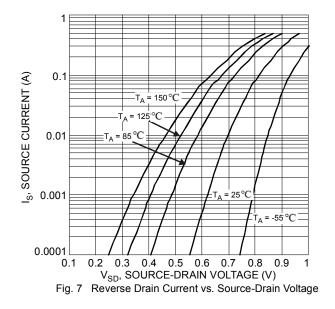


Fig. 6 Typical Total Capacitance

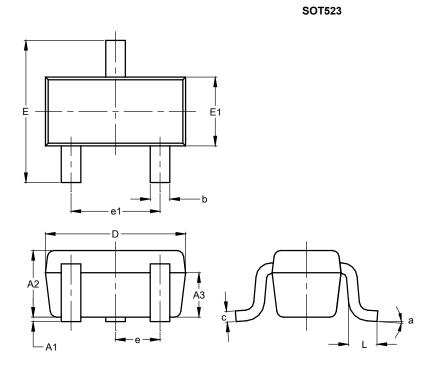






### **Package Outline Dimensions**

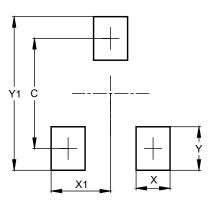
Please see http://www.diodes.com/package-outlines.html for the latest version.



SOT523								
Dim	Min Max Typ							
A1	0.00 0.10 0.05							
A2	0.60	0.60 0.80 0.75						
A3	0.45	0.65	0.50					
b	0.15	0.30	0.22					
С	0.10	0.10 0.20 0.12						
D	1.50	1.50 1.70 1.60						
Е	1.45	1.60						
E1	0.75	0.85	0.80					
е		0.50 BS	С					
e1	0.90	0.90 1.10 1.00						
L	0.20	0.40	0.33					
а	0°		8°					
Α	I Dimen	isions ir	n mm					

# Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.



Dimensions	Value (in mm)
С	1.29
Х	0.40
X1	0.70
Y	0.51
Y1	1.80

**SOT523** 



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