



#### **DUAL N-CHANNEL ENHANCEMENT MODE MOSFET**

## **Product Summary**

BV <sub>DSS</sub>	R <sub>DS(ON)</sub>	I <sub>D</sub> T <sub>A</sub> = +25°C
30V	$4.2\Omega$ @ $V_{GS} = 4.5V$	200mA
307	2.8Ω @ V <sub>GS</sub> = 10V	260mA

## **Description**

This new generation MOSFET has been designed to minimize the on-state resistance (R<sub>DS(ON)</sub>) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

### **Applications**

- DC-DC Converters
- Power Management Functions
- Battery Operated Systems and Solid-State Relays
- Drivers: Relays, Solenoids, Lamps, Hammers, Displays, Memories, Transistors, etc.

### **Features**

- Dual N-Channel MOSFET
- Low On-Resistance
- Low Input Capacitance
- · Fast Switching Speed
- Small Surface Mount Package
- · ESD Protected Gate
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q101, PPAP capable, and manufactured in IATF 16949 certified facilities), please contact us or your local Diodes representative.
- https://www.diodes.com/quality/product-definitions/

#### **Mechanical Data**

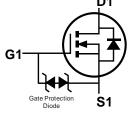
- Case: SOT363
- Case Material: Molded Plastic. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Matte Tin Finish Annealed over Alloy 42 Leadframe (Lead Free Plating). Solderable per MIL-STD-202, Method 208 (3)
- Terminal Connections: See Diagram
- Weight: 0.006 grams (Approximate)

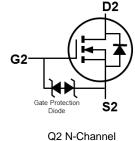


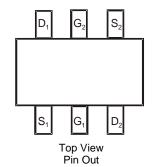


Top View









Ordering Information (Note 4)

Part Number	Case	Packaging
DMN63D8LDW-7	SOT363	3000/Tape & Reel
DMN63D8LDW-13	SOT363	10000/Tape & Reel

Q1 N-Channel

Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.

- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

# Marking Information



MM4 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: G = 2019) M = Month (ex: 9 = September)

Date Code Key

Date Code Itey												
Year	201	1	~		2019	20	20	2021		2022	1	2023
Code	Υ		~		G		Н	I		J		K
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



# **Maximum Ratings** (@ $T_A = +25^{\circ}C$ , unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			$V_{DSS}$	30	V
Gate-Source Voltage			$V_{GSS}$	±20	V
Continuous Drain Current (Note 5) V <sub>GS</sub> = 10V	Steady State	$T_A = +25$ °C $T_A = +70$ °C	I <sub>D</sub>	220 170	mA
Continuous Drain Current (Note 6) V <sub>GS</sub> = 10V	Steady State	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	ID	260 210	mA
Pulsed Drain Current (10μs Pulse, Duty Cycle = 1%)			I <sub>DM</sub>	800	mA

## Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Total Power Dissipation	(Note 5)	0	300	mW
Total Power Dissipation	(Note 6)	P <sub>D</sub>	400	IIIVV
Thermal Resistance, Junction to Ambient	(Note 5)	Б	435	
Thermal Resistance, Junction to Ambient	(Note 6)	$R_{\theta JA}$	330	°C/W
Thermal Resistance, Junction to Case	$R_{\theta JC}$	139		
Operating and Storage Temperature Range		T <sub>J,</sub> T <sub>STG</sub>	-55 to +150	°C

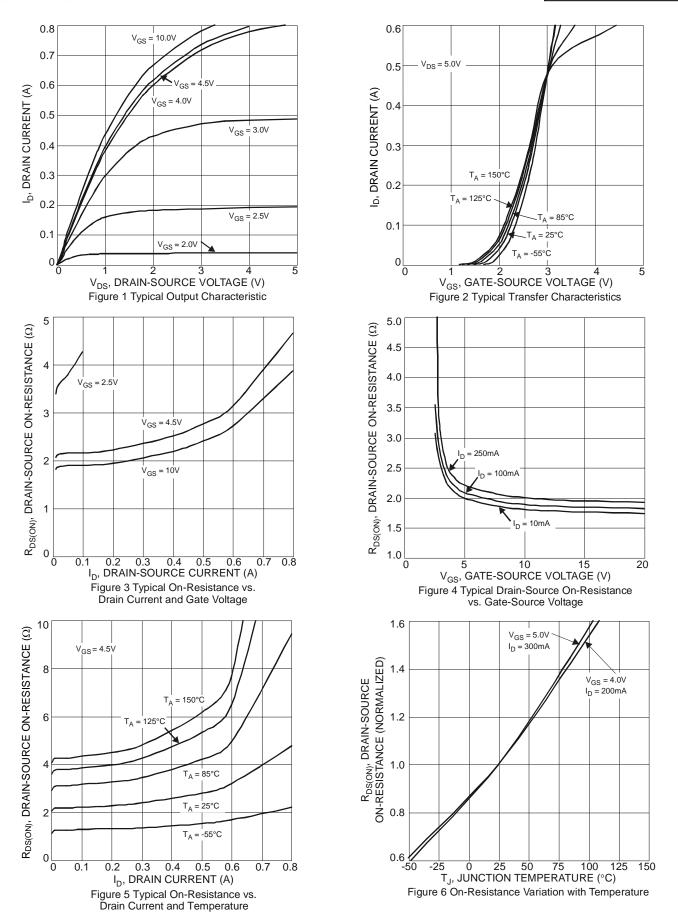
## Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	30		_	V	$V_{GS} = 0V, I_D = 250\mu A$	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	_		1.0	μΑ	$V_{DS} = 30V, V_{GS} = 0V$	
Gate-Body Leakage	I <sub>GSS</sub>	_		±10.0	μΑ	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	V <sub>GS(TH)</sub>	8.0		1.5	V	$V_{DS} = V_{GS}$ , $I_D = 250\mu A$	
				2.8		$V_{GS} = 10.0V, I_D = 250mA$	
				3.8		$V_{GS} = 5V, I_D = 250mA$	
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>	_	_	4.2	Ω	$V_{GS} = 4.5V, I_D = 250mA$	
	'	_		4.5		V <sub>GS</sub> = 4.0V, I <sub>D</sub> = 250mA	
		_		13		$V_{GS} = 2.5V, I_D = 10mA$	
Forward Transconductance	g <sub>FS</sub>	80		_	mS	V <sub>DS</sub> = 10V, I <sub>D</sub> = 0.115A	
Diode Forward Voltage	$V_{SD}$	_	8.0	1.2	V	$V_{GS} = 0V, I_{S} = 115mA$	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	Ciss		22.0	_		V <sub>DS</sub> = 25V, V <sub>GS</sub> = 0V, f = 1.0MHz	
Output Capacitance	Coss	_	3.2	_	pF		
Reverse Transfer Capacitance	C <sub>rss</sub>	_	2.0	_			
Gate Resistance	R <sub>G</sub>	_	79.9	_	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1.0MHz$	
Total Gate Charge (V <sub>GS</sub> = 10V)	$Q_g$	_	0.87	_			
Total Gate Charge (V <sub>GS</sub> = 4.5V)	Qg	_	0.43	_	nC	V 20V I 450A	
Gate-Source Charge	Qgs	_	0.11	_	nC	$V_{DS} = 30V, I_{D} = 150mA$	
Gate-Drain Charge	$Q_{qd}$	_	0.11				
Turn-On Delay Time	t <sub>D(ON)</sub>		3.3	_			
Turn-On Rise Time	t <sub>R</sub>	_	3.2			$V_{DD} = 30V, I_D = 0.115A, V_{GEN} = 10V.$	
Turn-Off Delay Time	t <sub>D(OFF)</sub>	_	12.0	_	ns	$R_{GEN} = 25\Omega$	
Turn-Off Fall Time	t <sub>F</sub>	_	6.3	_			

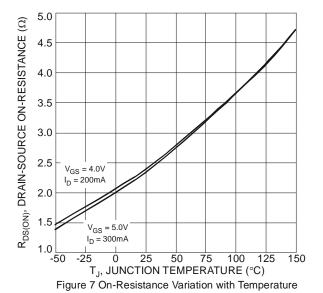
Notes:

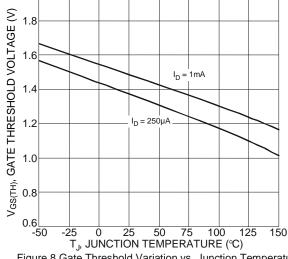
- 5. Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.
- 6. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper pad layout.
- 7. Short duration pulse test used to minimize self-heating effect.
- 8. Guaranteed by design. Not subject to production testing.





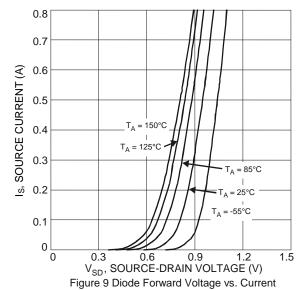






2.0

Figure 8 Gate Threshold Variation vs. Junction Temperature

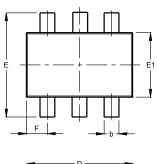


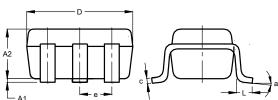


# **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.

#### **SOT363**



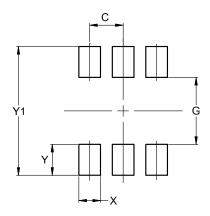


SOT363								
Dim	Min	Max	Тур					
A1	0.00	0.10	0.05					
A2	0.90	1.00	0.95					
b	0.10	0.30	0.25					
C	0.10	0.22	0.11					
D	1.80	2.20	2.15					
Е	2.00	2.20	2.10					
E1	1.15	1.35	1.30					
е	C	0.650 BSC						
F	0.40	0.45	0.425					
L	0.25	0.40	0.30					
а	0°	8°						
All Dimensions in mm								

# **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.

#### **SOT363**



Dimensions	Value (in mm)				
С	0.650				
G	1.300				
Х	0.420				
Y	0.600				
V1	2 500				



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6 of 6 DMN63D8LDW Document number: DS36021 Rev. 4 - 2

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