

## Product Summary

$BV_{DSS}$	$R_{DS(ON)}$ Max	$I_D$ $T_A = +25^\circ\text{C}$
60V	2Ω @ $V_{GS} = 4\text{V}$	310mA
	2.5Ω @ $V_{GS} = 2.5\text{V}$	295mA

## Description and Applications

This new generation MOSFET has been designed to minimize the on-state resistance ( $R_{DS(ON)}$ ) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

- DC-DC Converters
- Power Management Functions
- Battery Operated Systems and Solid-State Relays
- Drivers: Relays, Solenoids, Lamps, Hammers, Displays, Memories, Transistors, etc.

## Features and Benefits

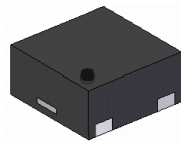
- Low On-Resistance
  - Low Input Capacitance
  - Fast Switching Speed
  - Low Input/Output Leakage
  - **ESD Protected**
  - **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
  - **Halogen and Antimony Free. "Green" Device (Note 3)**
  - **For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please [contact us](https://www.diodes.com/quality/product-definitions/) or your local Diodes representative.**
- <https://www.diodes.com/quality/product-definitions/>

## Mechanical Data

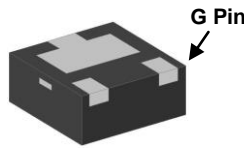
- Case: X1-DFN1212-3
- Case Material: Molded Plastic. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: NiPdAu over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (64)
- Terminal Connections: See Diagram
- Weight: 0.005 grams (Approximate)



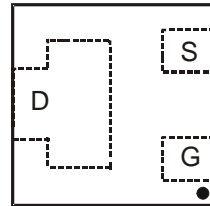
ESD PROTECTED



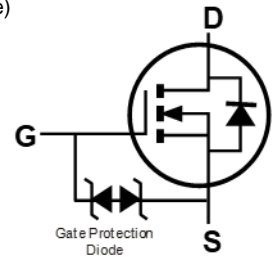
Top View



Bottom View



Pin-Out Top View



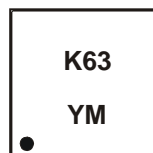
Equivalent Circuit

## Ordering Information (Note 4)

Part Number	Case	Packaging
DMN62D0LFD-7	X1-DFN1212-3	3,000/Tape & Reel
DMN62D0LFD-13	X1-DFN1212-3	10,000/Tape & Reel

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
  2. See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
  3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
  4. For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

## Marking Information



K63 = Product Type Marking Code  
 YM = Date Code Marking  
 Y = Year (ex: H = 2020)  
 M = Month (ex: 9 = September)

### Date Code Key

Year	2013	...	2020	2021	2022	2023	2024	2025	2026	2027	2028	2029
Code	A	...	H	I	J	K	L	M	N	O	P	R

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

**Maximum Ratings** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Drain-Source Voltage	V <sub>DSS</sub>	60	V	
Gate-Source Voltage	V <sub>GSS</sub>	±20	V	
Continuous Drain Current (Note 5) V <sub>GS</sub> = 4.0V	I <sub>D</sub>	T <sub>A</sub> = +25°C	310	mA
		T <sub>A</sub> = +70°C	260	
Pulsed Drain Current (Note 6) (10µs Pulse, Duty Cycle = 1%)	I <sub>DM</sub>	1.0	A	

**Thermal Characteristics**

Characteristic	Symbol	Max	Unit
Power Dissipation (Note 5)	P <sub>D</sub>	0.48	W
Thermal Resistance, Junction to Ambient @T <sub>A</sub> = +25°C (Note 5)	R <sub>θJA</sub>	265	°C/W
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C

**Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
<b>OFF CHARACTERISTICS (Note 7)</b>						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	60	—	—	V	V <sub>GS</sub> = 0V, I <sub>D</sub> = 250µA
Zero Gate Voltage Drain Current T <sub>J</sub> = +25°C	I <sub>DSS</sub>	—	—	1.0	µA	V <sub>DS</sub> = 60V, V <sub>GS</sub> = 0V
Gate-Source Leakage	I <sub>GSS</sub>	—	—	±100	nA	V <sub>GS</sub> = ±5V, V <sub>DS</sub> = 0V
		—	—	±500	nA	V <sub>GS</sub> = ±10V, V <sub>DS</sub> = 0V
		—	—	±2.0	µA	V <sub>GS</sub> = ±15V, V <sub>DS</sub> = 0V
<b>ON CHARACTERISTICS (Note 7)</b>						
Gate Threshold Voltage	V <sub>GS(TH)</sub>	0.6	—	1.0	V	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250µA
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>	—	1.3	2	Ω	V <sub>GS</sub> = 4V, I <sub>D</sub> = 100mA
		—	1.4	2.5		V <sub>GS</sub> = 2.5V, I <sub>D</sub> = 50mA
		—	1.8	3		V <sub>GS</sub> = 1.8V, I <sub>D</sub> = 50mA
		—	2.4	—		V <sub>GS</sub> = 1.5V, I <sub>D</sub> = 10mA
		—	—	—		—
Forward Transfer Admittance	Y <sub>fs</sub>	—	1.8	—	S	V <sub>DS</sub> = 10V, I <sub>D</sub> = 200mA
Diode Forward Voltage	V <sub>SD</sub>	—	0.8	1.3	V	V <sub>GS</sub> = 0V, I <sub>S</sub> = 115mA
<b>DYNAMIC CHARACTERISTICS (Note 8)</b>						
Input Capacitance	C <sub>iss</sub>	—	31	—	pF	V <sub>DS</sub> = 25V, V <sub>GS</sub> = 0V, f = 1.0MHz
Output Capacitance	C <sub>oss</sub>	—	4.3	—		
Reverse Transfer Capacitance	C <sub>rss</sub>	—	3.0	—		
Gate Resistance	R <sub>g</sub>	—	99	—	Ω	V <sub>DS</sub> = 0V, V <sub>GS</sub> = 0V, f = 1MHz
Total Gate Charge	Q <sub>g</sub>	—	0.5	—	nC	V <sub>GS</sub> = 4.5V, V <sub>DS</sub> = 10V, I <sub>D</sub> = 250mA
Gate-Source Charge	Q <sub>gs</sub>	—	0.09	—		
Gate-Drain Charge	Q <sub>gd</sub>	—	0.07	—		
Turn-On Delay Time	t <sub>D(ON)</sub>	—	2.6	—	ns	V <sub>GS</sub> = 10V, V <sub>DS</sub> = 30V, R <sub>L</sub> = 150Ω, R <sub>G</sub> = 25Ω, I <sub>D</sub> = 200mA
Turn-On Rise Time	t <sub>r</sub>	—	2.1	—		
Turn-Off Delay Time	t <sub>D(OFF)</sub>	—	18	—		
Turn-Off Fall Time	t <sub>f</sub>	—	8.7	—		

- Notes:
- Device mounted on FR-4 PCB with minimum recommended pad layout, single sided.
  - Repetitive rating, pulse width limited by junction temperature.
  - Short duration pulse test used to minimize self-heating effect.
  - Guaranteed by design. Not subject to production testing.

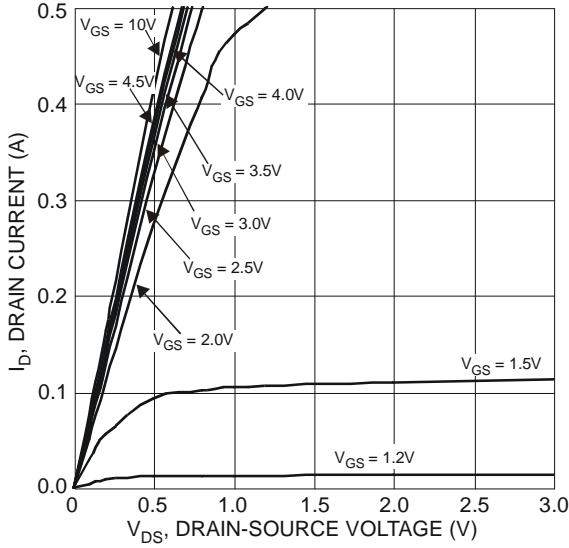


Figure 1 Typical Output Characteristics

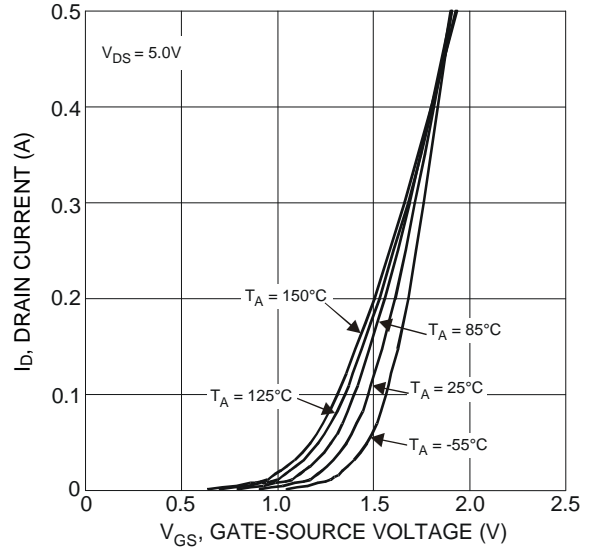


Figure 2 Typical Transfer Characteristics

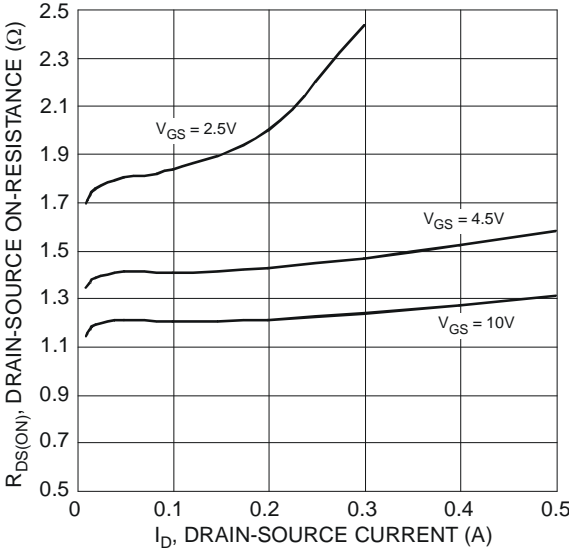


Figure 3 Typical On-Resistance vs. Drain Current and Gate Voltage

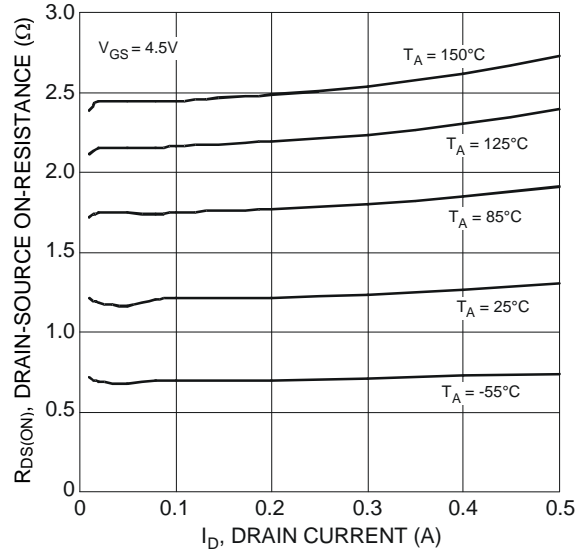


Figure 4 Typical On-Resistance vs. Drain Current and Temperature

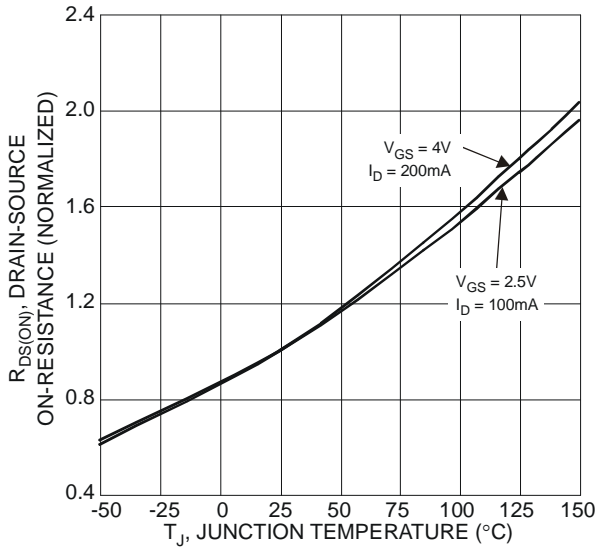


Figure 5 On-Resistance Variation with Temperature

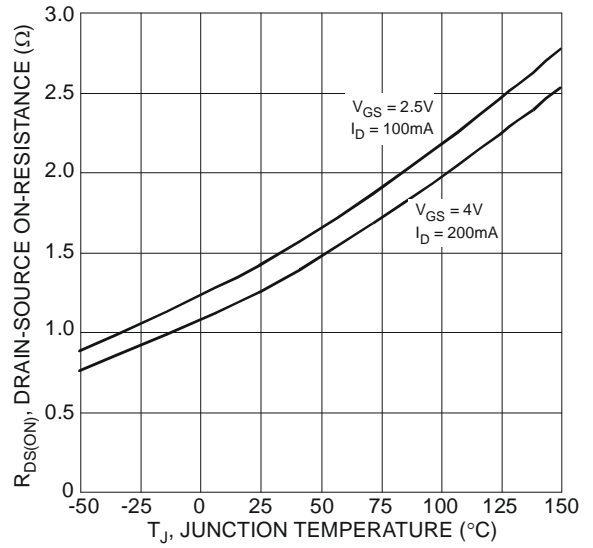


Figure 6 On-Resistance Variation with Temperature

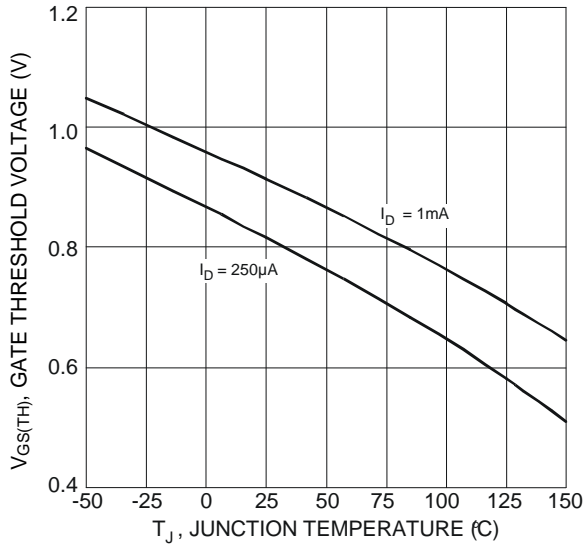


Figure 7 Gate Threshold Variation vs. Junction Temperature

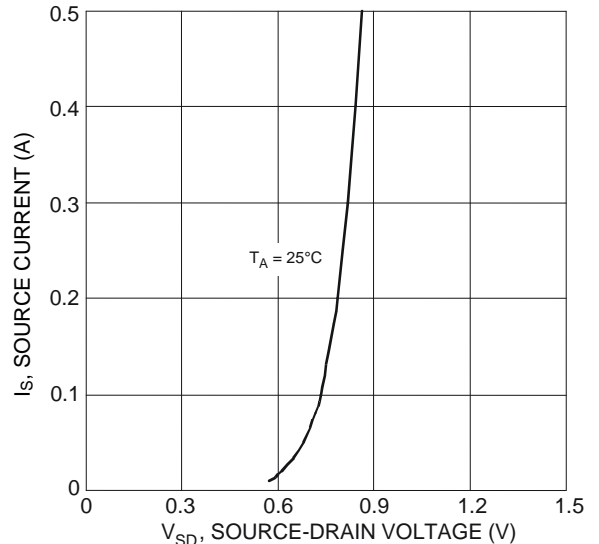


Figure 8 Diode Forward Voltage vs. Current

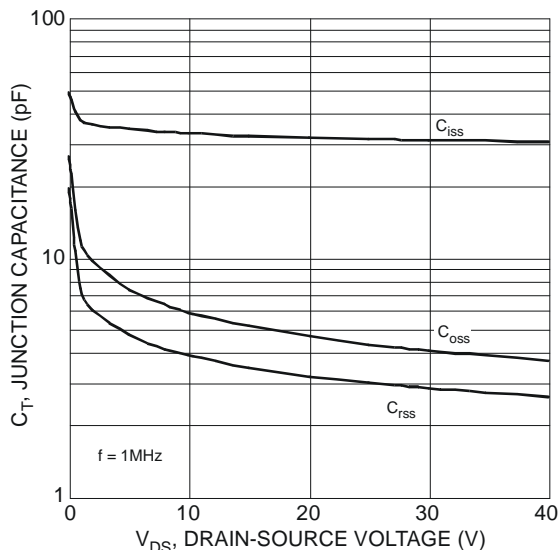


Figure 9 Typical Junction Capacitance

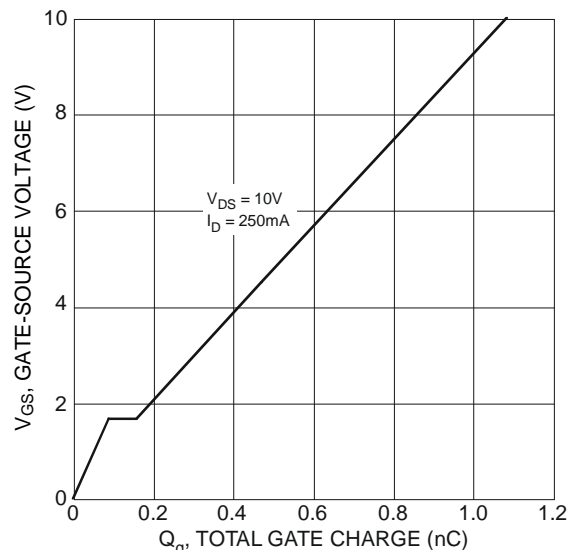


Figure 10 Gate Charge

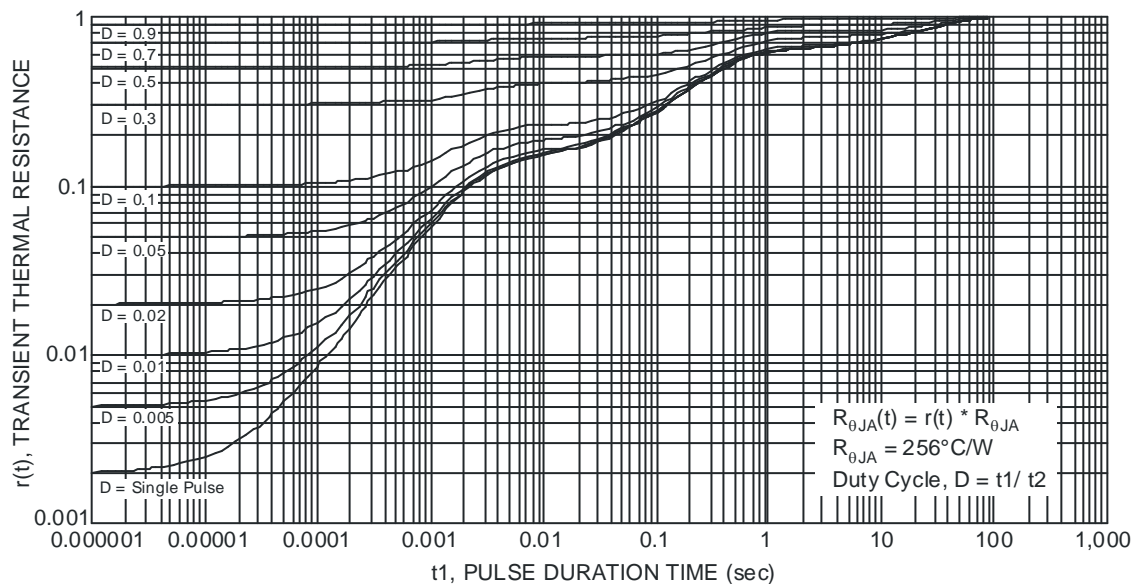
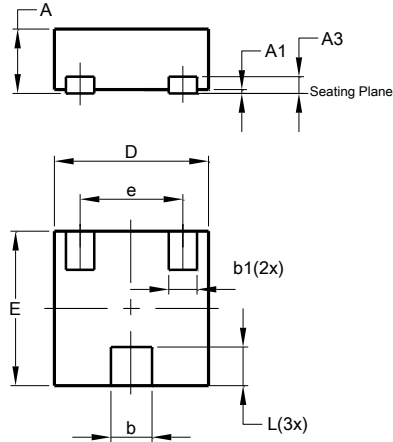


Figure 11 Transient Thermal Resistance

**Package Outline Dimensions**

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

**X1-DFN1212-3**

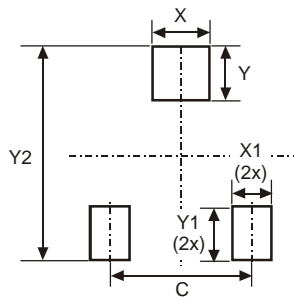


X1-DFN1212-3			
Dim	Min	Max	Typ
A	0.47	0.53	0.50
A1	0	0.05	0.02
A3	-	-	0.13
b	0.27	0.37	0.32
b1	0.17	0.27	0.22
D	1.15	1.25	1.20
E	1.15	1.25	1.20
e	-	-	0.80
L	0.25	0.35	0.30
All Dimensions in mm			

**Suggested Pad Layout**

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

**X1-DFN1212-3**



Dimensions	Value (in mm)
C	0.80
X	0.42
X1	0.32
Y	0.50
Y1	0.50
Y2	1.50

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