

#### PEAK 3A CMOS LDO REGULATOR WITH ENABLE AND POWER GOOD

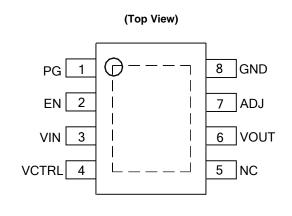
### Description

The AP2132A series are positive voltage regulator ICs fabricated by CMOS process. The ICs consist of a voltage reference, an error amplifier, a power transistor, a resistor network for setting output voltage, a current limit circuit for current protection, and a chip enable circuit.

The AP2132A have features of large current, low dropout voltage, high output voltage accuracy and low input voltage. The AP2132A provide a power good (PG) signal to indicate if the voltage level of  $V_{OUT}$  reaches 92% of its rating value. And it operates with a  $V_{IN}$  as low as 1.4V and  $V_{CTRL}$  voltage 5V with output voltage programmable as low as 0.6V.

The AP2132A are available in 1.2V, 1.5V, 1.8V, 2.5V fixed output voltage versions and adjustable output voltage version. The fixed versions integrate the adjust resistors. It is also available in an adjustable version, which can set the output voltage with external resistor. If the pin of adjustable output voltage is to ground, it will switch to fixed output voltage.

# Pin Assignments



PSOP-8

# Applications

Notebook

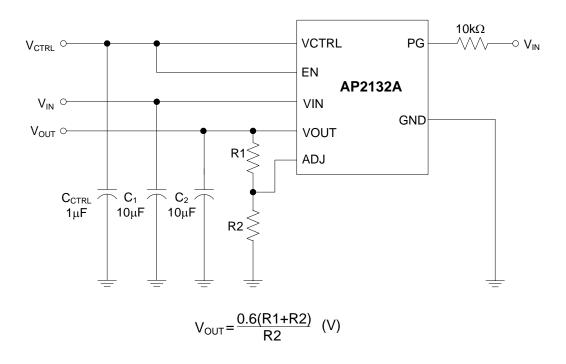
The AP2132A series are available in PSOP-8 package.

### Features

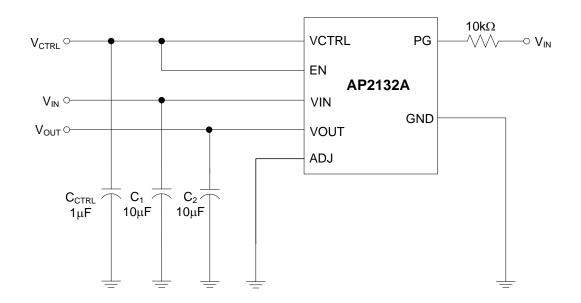
- Adjustable Output: 0.6V to 3.0V
- Low Dropout Voltage: 300mV at I<sub>OUT</sub> = 2A, V<sub>OUT</sub> = 1.2V
- Over Current and Over Temperature Protection
- Enable Pin
- PSOP-8 Package with Thermal Pad
- Maximum Output Current: 3A (Peak)
- High Output Voltage Accuracy: 2%
- V<sub>OUT</sub> Power Good Signal
- Excellent Line/Load Regulation



# **Typical Applications Circuit**



Adjustable Version



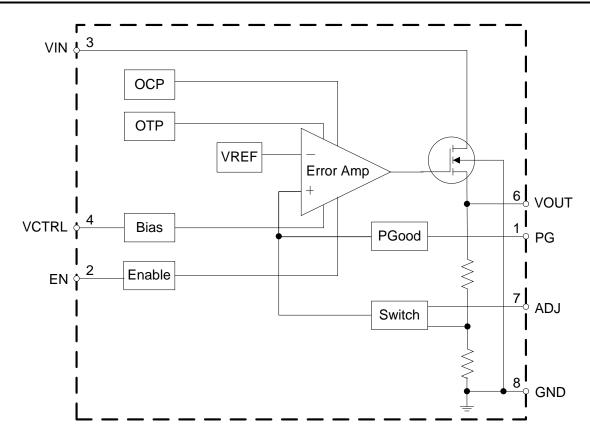
**Fixed Version** 



# **Pin Descriptions**

Pin Number	Pin Name	Function					
1	PG	Assert high once V <sub>OUT</sub> reaches 92% of its rating voltage					
2	EN	Enable input					
3	VIN	Input voltage					
4	VCTRL	Input voltage for controlling circuit					
5	NC	Not connected					
6	VOUT	Regulated output voltage					
7	ADJ	Adjust output: when connected to ground, the output voltage is set by internal resistors; when external feedback resistors are connected, the output voltage will be $V_{OUT} = 0.6V \times (R1+R2)/R2$ .					
8	GND	Ground					

# Functional Block Diagram





# Absolute Maximum Ratings (Note 1)

Symbol	Parameter	Rating	Unit
V <sub>IN</sub> V <sub>CTRL</sub>	Input Voltage Input Voltage for Controlling Circuit	6.0	V
V <sub>EN</sub>	Enable Input Voltage	-0.3 to 6.0	V
θ <sub>JA</sub>	Thermal Resistance (No Heatsink)	130	°C/W
TJ	Operating Junction Temperature	+150	°C
T <sub>STG</sub>	Storage Temperature Range	-65 to +150	°C
T <sub>LEAD</sub>	Lead Temperature (Soldering, 10sec)	+260	°C
_	ESD (Machine Model)	200	V
_	ESD (Human Body Model)	2000	V

Note 1: Stresses greater than those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "Recommended Operating Conditions" is not implied. Exposure to "Absolute Maximum Ratings" for extended periods may affect device reliability.

# **Recommended Operating Conditions**

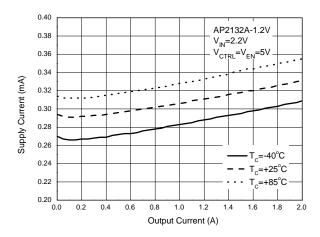
Symbol	Parameter	Min	Мах	Unit
V <sub>IN</sub>	Input Voltage	1.4	5.5	V
V <sub>CTRL</sub>	Input Voltage for Controlling Circuit	4.5	5.5	V
T <sub>A</sub>	Operating Ambient Temperature Range	-40	+85	°C



**Electrical Characteristics** (@ $V_{IN} = V_{OUT}+0.5V$ ,  $V_{CTRL} = V_{EN} = 5V$ ,  $T_A = +25^{\circ}C$ ,  $C_{IN} = C_{OUT} = 10\mu$ F,  $C_{CTRL} = 1\mu$ F,  $I_{OUT} = 10$ mA, **Bold** typeface applies over -40°C  $\leq T_A \leq +85^{\circ}C$ , unless otherwise specified.)

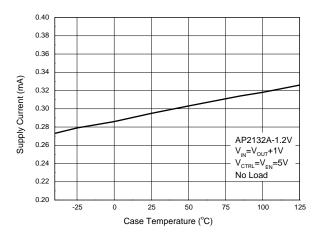
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
Vout	Output Voltage	V <sub>IN</sub> = V <sub>OUT</sub> +0.5V, I <sub>OUT</sub> = 10mA		V <sub>OUT</sub> × 98%	_	V <sub>OUT</sub> × 102%	V
V <sub>IN</sub>	Input Voltage	_		1.4	-	5.5	V
I <sub>LIMIT</sub>	Current Limit	V <sub>IN</sub> -V <sub>OUT</sub> = 1V		3		_	А
V <sub>RLOAD</sub>	Load Regulation	V <sub>IN</sub> = V <sub>OUT</sub> +0.5V, 10	$MA \le I_{OUT} \le 2A$	_	10	_	mV
VRLINE	Line Regulation	$V_{OUT}$ +0.5V $\leq V_{IN} \leq$ 5V , $I_{OUT}$ = 10mA		_	2	_	mV
		I <sub>OUT</sub> = 500mA		_	80	120	mV
V <sub>DROP</sub>	Dropout Voltage	I <sub>OUT</sub> = 1A		_	150	200	mV
		I <sub>OUT</sub> = 2A		_	300	450	mV
ISUPPLY	Supply Current	V <sub>IN</sub> = V <sub>OUT</sub> +0.5V, I <sub>OUT</sub> = 0mA		_	300	_	μA
ICTRLH		$V_{IN} = V_{OUT}$ +0.5V, $V_{CTRL} = V_{EN} = 5V$		_	250	500	μA
ICTRLL	V <sub>CTRL</sub> Current	$V_{IN} = V_{OUT}$ +0.5V, $V_{CTRL}$ =5V, $V_{EN}$ = 0V		_	0.1	1.0	μA
	Power Supply Rejection Ratio	Ripple 0.5Vp-p, V <sub>IN</sub> = V <sub>OUT</sub> +1V	f = 100Hz	_	60	_	dB
PSRR			f = 1kHz	_	60	_	dB
ΔV <sub>OUT</sub> V <sub>OUT</sub> <b>x</b> ΔT	Output Voltage Temperature Coefficient	I <sub>OUT</sub> = 10mA, -40°C ≤ T <sub>A</sub> ≤ +85°C		_	±100	_	ppm/°0
VREF	Reference Voltage	Adjust Short to VOUT		0.588	0.6	0.612	V
_	Enable "High" Voltage	Enable Input Voltage "High"		1.5		_	V
_	Enable "Low" Voltage	Enable Input Voltage "Low"		_	-	0.4	V
OTSD	Thermal Shutdown	_		_	+165	_	°C
_	Thermal Shutdown Hysteresis	_		_	+20	_	°C
VTHPG	Vout Power Good Voltage	_		_	92	_	%
_	V <sub>PG</sub> Hysteresis	_		_	7	_	%
_	Adjust Pin Threshold	_		_	200	_	mV
θJC	Thermal Resistance (Junction to Case)	PSOP-8		_	40	_	°C/W

# **Performance Characteristics**

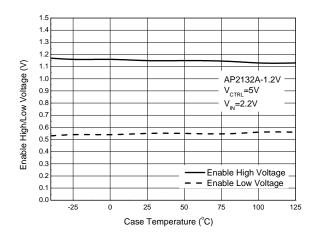


### Supply Current vs. Output Current

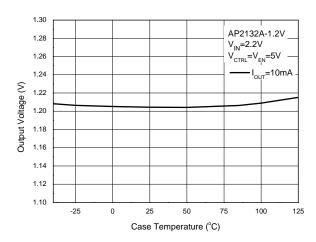
Supply Current vs. Case Temperature



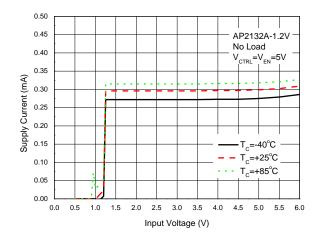
Enable High/Low Voltage vs. Case Temperature



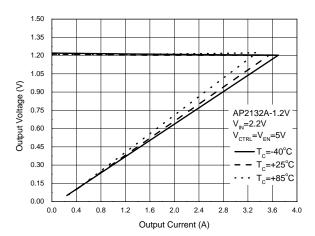
**Output Voltage vs. Case Temperature** 



Supply Current vs. Input Voltage

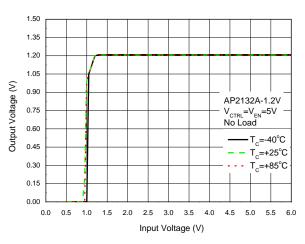


**Output Voltage vs. Output Current** 



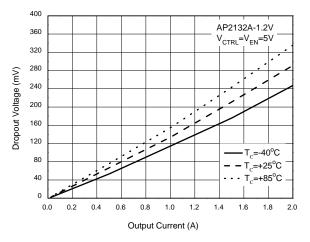


# Performance Characteristics (Cont.)

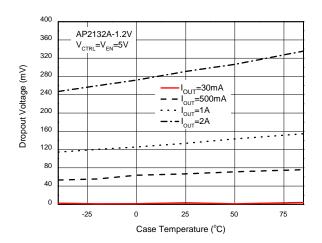


### Output Voltage vs. Input Voltage

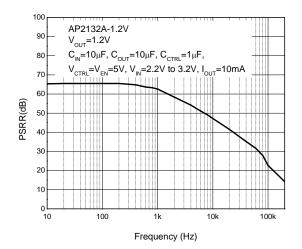
Dropout Voltage vs. Output Current



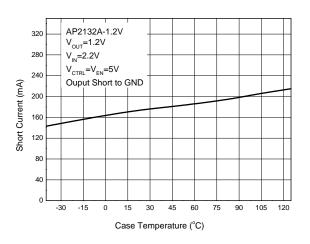
**Dropout Voltage vs. Case Temperature** 



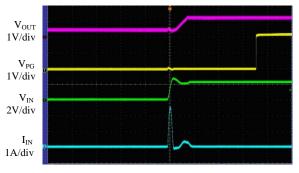
**PSRR vs. Frequency** 



Short Current vs. Case Temperature



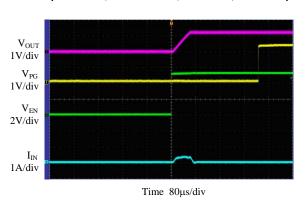
 $$V_{IN}$$  Start up Waveform (V\_{CTRL}=V\_{EN}=5V, V\_{IN}=0 to 2.2V, No Load)



Time 80µs/div

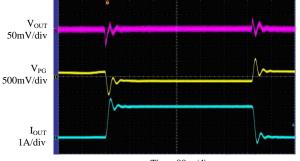


### Performance Characteristics (Cont.)



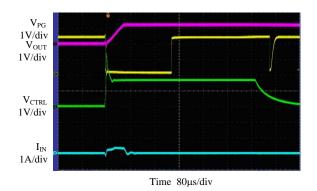
V<sub>EN</sub> Start up Waveform (V<sub>CTRL</sub>=5V, V<sub>EN</sub>=0 to 5V, V<sub>IN</sub>=2.2V, No Load)

Load Transient (V<sub>CTRL</sub>=V<sub>EN</sub>=5V, V<sub>IN</sub>=2.2V, I<sub>OUT</sub>=0 to 2A)

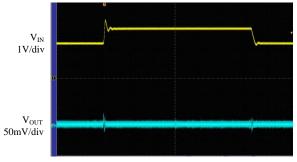


 $Time \ 80 \mu s/div$ 

 $V_{CTRL}$  Start up and Shut down Waveform (V\_{CTRL}=0 to 5V, V\_{EN}=5V, V\_{IN}=2.2V, No Load)



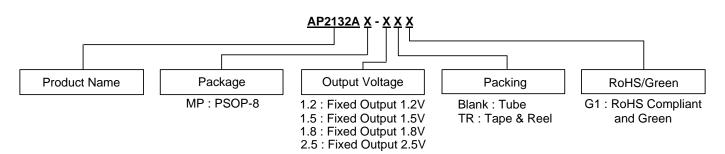
Line Transient (V<sub>CTRL</sub>=V<sub>EN</sub>=5V, C<sub>IN</sub>=C<sub>CTRL</sub>=1µF, C<sub>OUT</sub>=10µF, V<sub>IN</sub>=2.2V to 3.2V, I<sub>OUT</sub>=10mA)



Time 80µs/div



# Ordering Information

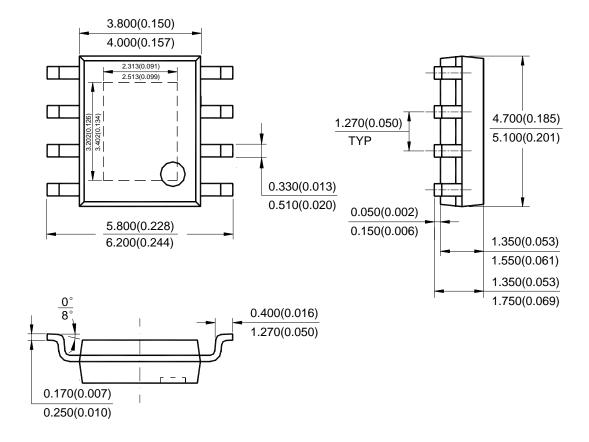


Package	Temperature Range	Version Description	Part Number	Marking ID	Packing
PSOP-8	-40°C to +85°C	Each fixed output version integrates ADJ version	AP2132AMP-1.2G1	2132A-1.2G1	Tube
			AP2132AMP-1.2TRG1	2132A-1.2G1	Tape & Reel
			AP2132AMP-1.5G1	2132A-1.5G1	Tube
			AP2132AMP-1.5TRG1	2132A-1.5G1	Tape & Reel
			AP2132AMP-1.8G1	2132A-1.8G1	Tube
			AP2132AMP-1.8TRG1	2132A-1.8G1	Tape & Reel
			AP2132AMP-2.5G1	2132A-2.5G1	Tube
			AP2132AMP-2.5TRG1	2132A-2.5G1	Tape & Reel



# Package Outline Dimensions (All dimensions in mm(inch).)

#### (1) Package Type: PSOP-8



Note: Eject hole, oriented hole and mold mark is optional.



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