



60V 175°C N-CHANNEL ENHANCEMENT MODE MOSFET PowerDI5060-8

Product Summary

| BV _{DSS} | R _{DS(ON)} max | I _D max T _C = +25°C |
|-------------------|--|--|
| 60V | $35m\Omega$ @ V _{GS} = $10V$ | 33A |
| 60 V | $44m\Omega$ @ V _{GS} = $4.5V$ | 29A |

Description and Applications

This MOSFET is designed to minimize the on-state resistance (RDS(ON)) yet maintain superior switching performance, making it ideal for high efficiency power management applications.

- Backlighting
- Power Management Functions
- DC-DC Converters

Features and Benefits

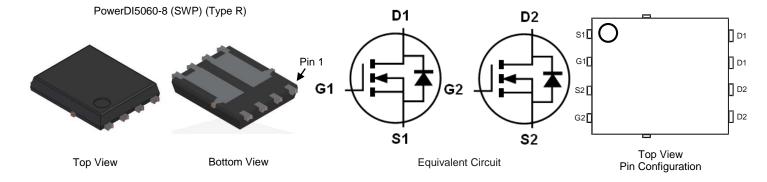
- Rated to +175°C Ideal for High Ambient Temperature Environments
- 100% Unclamped Inductive Switch (UIS) Test in Production
- Low Rds(ON) Minimizes On State Losses
- Low Input Capacitance
- Wettable Flank for Improved Optical Inspections
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e.: parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please refer to the related automotive grade (Q-suffix) part. A listing can be found at

https://www.diodes.com/products/automotive/automotive-products/.

- This part is qualified to JEDEC standards (as references in AEC-Q) for High Reliability.
 - https://www.diodes.com/quality/product-definitions/
- An Automotive-Compliant Part is Available Under Separate Datasheet (DMNH6035SPDWQ)

Mechanical Data

- Case: PowerDI®5060-8
- Case Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Annealed over Copper Leadframe.
 Solderable per MIL-STD-202, Method 208@3
- Weight: 0.097 grams (Approximate)



Ordering Information (Note 4)

| - 1 | | | |
|-----|-----------------|------------------------------|--------------------|
| | Part Number | Case | Packaging |
| | DMNH6035SPDW-13 | PowerDI5060-8 (SWP) (Type R) | 2500 / Tape & Reel |

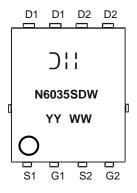
Notes:

- 1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

PowerDI is a registered trademark of Diodes Incorporated.



Marking Information



⊃¦¦= Manufacturer's Marking N6035SDW = Product Type Marking Code YYWW = Date Code Marking YY = Year (ex: 20 = 2020) WW = Week (01 to 53)

Maximum Ratings (@ $T_A = +25$ °C, unless otherwise specified.)

| Characteristic | Symbol | Value | Unit |
|--|------------------|----------|------|
| Drain-Source Voltage | VDSS | 60 | V |
| Gate-Source Voltage | V _{GSS} | ±20 | V |
| Continuous Drain Current, V _{GS} = 10V (Note 6) | lσ | 33 21 | А |
| Maximum Body Diode Forward Current (Note 6) | Is | 33 | Α |
| Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%) | I _{DM} | 132 | Α |
| Pulsed Source Current (10µs Pulse, Duty Cycle = 1%) | Ism | 132 | Α |
| Avalanche Current, L = 1mH | las | 21.4 | Α |
| Avalanche Energy, L = 1mH | Eas | 230 | mJ |

Thermal Characteristics

| Characteristic | Symbol | Value | Unit | |
|--|-----------------|-------------|------|---|
| Thermal Resistance, Junction to Ambient (Note 5) | $R_{\theta JA}$ | 62 | °C/W | |
| Total Power Dissipation $T_A = +25^{\circ}C$ | | PD | 2.4 | W |
| Thermal Resistance, Junction to Case (Note 6) | Rejc | 2.2 | °C/W | |
| Total Power Dissipation $T_C = +25^{\circ}C$ | | PD | 68 | W |
| Operating and Storage Temperature Range | TJ, TSTG | -55 to +175 | °C | |

Notes:

^{5.} Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate; measured with 1 channel active.

^{6.} Thermal resistance from junction to solder point (on the exposed drain pin); measured with 1 channel active.



Electrical Characteristics N-Channel (@Tc = +25°C, unless otherwise specified.)

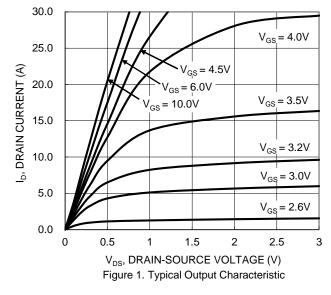
| Characteristic | Symbol | Min | Тур | Max | Unit | Test Condition | |
|---|-----------------|-----|------|------|-------|--|--|
| OFF CHARACTERISTICS (Note 7) | | | | | | | |
| Drain-Source Breakdown Voltage | BVDSS | 60 | _ | _ | V | $V_{GS} = 0V, I_{D} = 250\mu A$ | |
| Zero Gate Voltage Drain Current | IDSS | _ | _ | 1 | μΑ | V _{DS} = 60V, V _{GS} = 0V | |
| Gate-Source Leakage | Igss | _ | _ | ±100 | nA | Vgs = ±20V, Vps = 0V | |
| ON CHARACTERISTICS (Note 7) | | | • | • | • | | |
| Gate Threshold Voltage | Vgs(th) | 1 | _ | 3 | V | $V_{DS} = V_{GS}$, $I_D = 250\mu A$ | |
| Static Drain-Source On-Resistance | Descous | _ | 24 | 35 | mΩ | Vgs = 10V, ID = 15A | |
| Static Drain-Source On-Resistance | RDS(ON) | _ | 33 | 44 | 11177 | V _G S = 4.5V, I _D = 10A | |
| Diode Forward Voltage | V _{SD} | _ | 0.75 | 1.2 | V | V _{GS} = 0V, I _S = 2.6A | |
| DYNAMIC CHARACTERISTICS (Note 8) | | | • | • | | | |
| Input Capacitance | Ciss | _ | 879 | _ | | V _{DS} = 25V, V _{GS} = 0V, f = 1.0MHz | |
| Output Capacitance | Coss | _ | 227 | _ | pF | | |
| Reverse Transfer Capacitance | Crss | _ | 17 | _ | | | |
| Gate Resistance | Rg | _ | 2.4 | _ | Ω | V _{DS} = 0V, V _{GS} = 0V, f = 1.0MHz | |
| Total Gate Charge (VGS = 6V) | Qg | _ | 10 | _ | | | |
| Total Gate Charge (V _{GS} = 10V) | Qg | _ | 16 | _ | nC | V 00V I 00A | |
| Gate-Source Charge | Qgs | _ | 2 | _ | IIC | $V_{DS} = 30V, I_{D} = 20A$ | |
| Gate-Drain Charge | Qgd | _ | 4.9 | _ | | | |
| Turn-On Delay Time | td(ON) | _ | 3.8 | _ | | | |
| Turn-On Rise Time | t _R | _ | 7.7 | _ | | $V_{DD} = 30V$ | |
| Turn-Off Delay Time | tD(OFF) | _ | 19.5 | _ | ns | $V_{GS} = 10V, R_{G} = 4.7\Omega, I_{D} = 20A$ | |
| Turn-Off Fall Time | tF | _ | 5.8 | _ | | | |
| Body Diode Reverse Recovery Time | trr | _ | 28 | _ | ns | IF = 20A, di/dt = 100A/µs | |
| Body Diode Reverse Recovery Charge | Q _{RR} | - | 28 | _ | nC | $I_F = 20A$, $di/dt = 100A/\mu s$ | |

Notes:

^{7.} Short duration pulse test used to minimize self-heating effect.

^{8.} Guaranteed by design. Not subject to product testing.





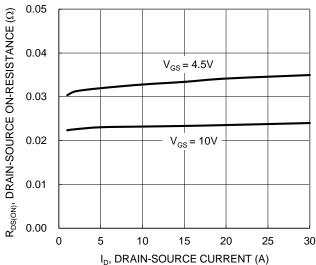


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

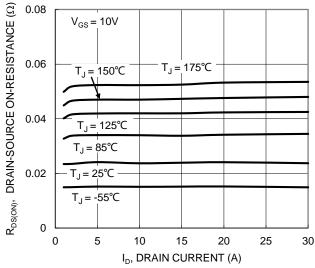


Figure 5. Typical On-Resistance vs. Drain Current and Temperature

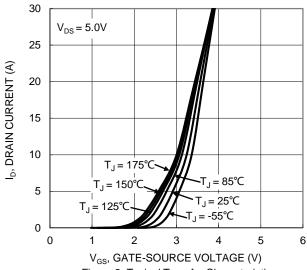


Figure 2. Typical Transfer Characteristic

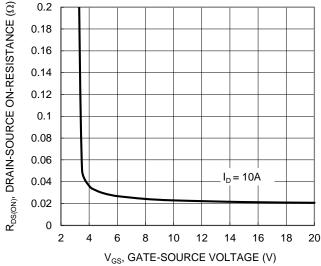


Figure 4. Typical Transfer Characteristic

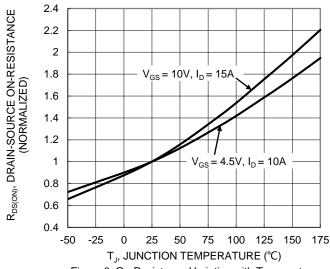


Figure 6. On-Resistance Variation with Temperature



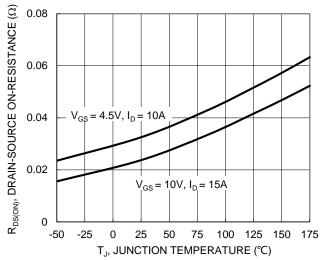
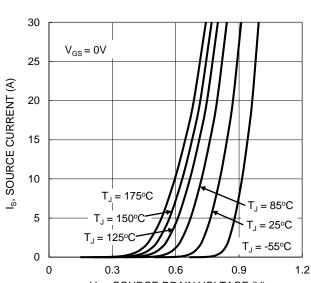


Figure 7. On-Resistance Variation with Temperature



V_{SD}, SOURCE-DRAIN VOLTAGE (V) Figure 9. Diode Forward Voltage vs. Current

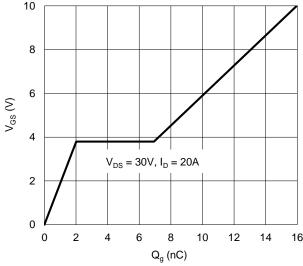


Figure 11. Gate Charge

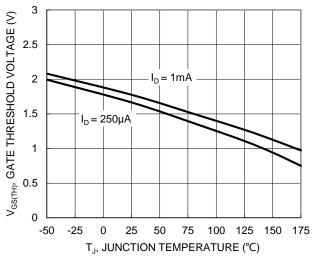
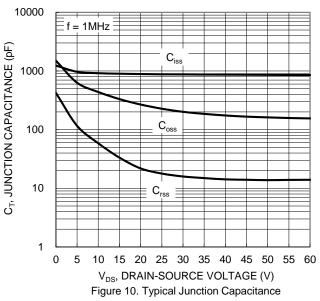


Figure 8. Gate Threshold Variation vs. Junction Temperature



1000 $R_{DS(ON)}$ ID, DRAIN CURRENT (A) 100 10 $P_W = 1 ms$ $T_{J(Max)} = 175^{\circ}C$ T_C = 25℃ $P_W = 10ms$ Single Pulse DUT on Infinite $P_W = 100 ms$ Heatsink $V_{GS} = 10V$ $P_W = 1s$ 0.1 10 100 V_{DS}, DRAIN-SOURCE VOLTAGE (V)

Figure 12. SOA, Safe Operation Area



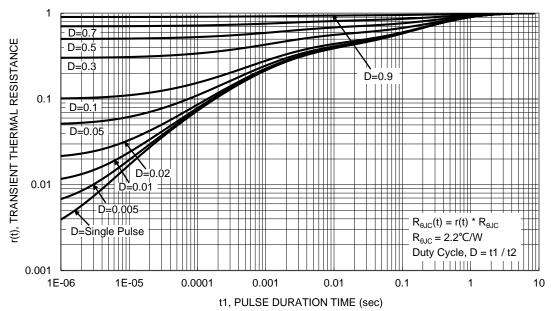
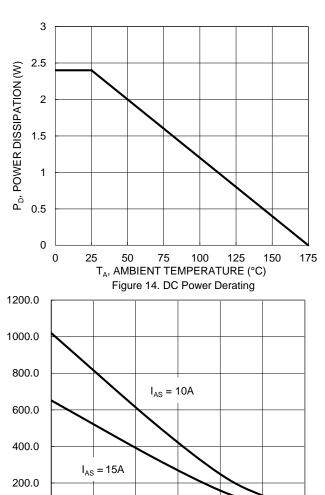
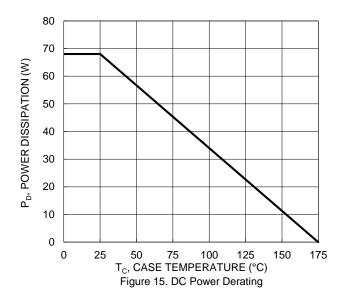


Figure 13. Transient Thermal Resistance





50

75

100

 $\label{eq:T_J (°C)} T_J \, (^{\circ}C)$ Figure 16. $E_{AS} \, vs. \, T_J$

125

150

0.0 L 25

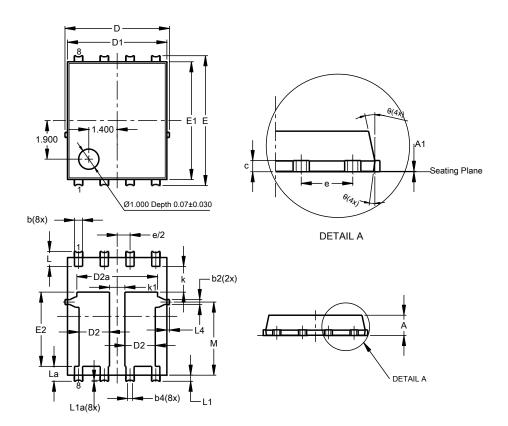
E_{AS} (mJ)



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI5060-8 (SWP) (Type R)

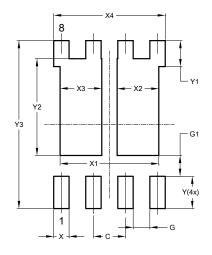


| PowerDI5060-8 (SWP) (Type R) | | | | | | |
|---------------------------------|----------------|---------|--------|--|--|--|
| Dim | Min | Max | Тур | | | |
| Α | 0.90 | 1.10 | 1.00 | | | |
| A1 | 0 | 0.05 | | | | |
| b | 0.30 | 0.50 | 0.41 | | | |
| b2 | 0.20 | 0.35 | 0.25 | | | |
| b4 | |).25REF | | | | |
| С | 0.230 | 0.330 | 0.277 | | | |
| D | 5 | .15 BS0 | \sim | | | |
| D1 | 4.70 | 5.10 | 4.90 | | | |
| D2 | 1.40 | 1.60 | 1.50 | | | |
| D2a | 3.78 4.18 3.98 | | | | | |
| Е | 6 | .40 BS0 |) | | | |
| E1 | 5.60 | 6.00 | 5.80 | | | |
| E2 | 3.46 | 3.86 | 3.66 | | | |
| е | 1 | .27BSC |) | | | |
| k | 1.05 | | 1 | | | |
| k1 | 0.56 | | | | | |
| L | 0.635 | 0.835 | 0.735 | | | |
| La | 0.635 | 0.835 | 0.735 | | | |
| L1 | 0.200 | 0.400 | 0.300 | | | |
| L1a | 0.050REF | | | | | |
| L4 | 0.025 | 0.225 | 0.125 | | | |
| М | 3.205 | 4.005 | 3.605 | | | |
| θ | 10° | 12° | 11° | | | |
| θ1 | 6° | 8° | 7° | | | |
| All Dimensions in mm | | | | | | |

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI5060-8 (SWP) (Type R)



| Dimensions | Value (in mm) | | |
|------------|------------------|--|--|
| | , , | | |
| С | 1.270 | | |
| G | 0.660 | | |
| G1 | 0.820 | | |
| Х | 0.610 | | |
| X1 | 3.910 | | |
| X2 | 1.650 | | |
| Х3 | 1.650 | | |
| X4 | 4.420 | | |
| Y | 1.270 | | |
| Y1 | 1.020 | | |
| Y2 | 3.810 | | |
| Y3 | 6.610 | | |



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