



DMN3042LFDF

Product Summary

BV _{DSS}	Rds(on) Max	I _D Max T _A = +25°C
	28mΩ @ V _{GS} = 10V	7.0A
30V	32mΩ @ V _{GS} = 4.5V	6.5A

Description and Applications

This new generation MOSFET is designed to minimize the on-state resistance ($R_{DS(ON)}$) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

- General Purpose Interfacing Switch
- Power Management Functions

30V N-CHANNEL ENHANCEMENT MODE MOSFET

Features and Benefits

- 0.6mm Profile Ideal for Low Profile Applications
- PCB Footprint of 4mm²
- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please <u>contact us</u> or your local Diodes representative. <u>https://www.diodes.com/quality/product-definitions/</u>

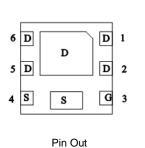
Mechanical Data

- Case: U-DFN2020-6
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 Per J-STD-020
- Terminals: Finish NiPdAu over Copper Leadframe. Solderable per MIL-STD-202, Method 208 @
- Weight: 0.007 grams (Approximate)

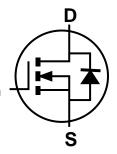
U-DFN2020-6 (Type F)

Top View

Bottom View



Bottom View



Internal Schematic

Ordering Information (Note 4)

Part Number	Case	Packaging
DMN3042LFDF-7	U-DFN2020-6 (Type F)	3,000/Tape & Reel
DMN3042LFDF-13	U-DFN2020-6 (Type F)	10,000/Tape & Reel

Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.</p>

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.



Marking Information

Site 1



S7 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: H = 2020)M = Month (ex: 9 = September)

Date	Code	Key

Year	2016		2020	2021	2022	2023	2024	2025	2026	2027	2028	2029
Code	D		Н	I	J	K	L	М	Ν	0	Р	R
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec

Site 2



S7 = Product Type Marking Code YWX = Date Code Marking

Y = Year (ex: 0 = 2020)W = Week (ex: a = Week 27; z Represents Week 52 and 53) X = Internal Code (ex: U = Monday)

Date Code Key

Dale	e Code Rey											
	Year	2016	 2020	2021	2022	2023	2024	2025	2026	2027	2028	2029
	Code	6	 0	1	2	3	4	5	6	7	8	9

Week	1-26	27-52	53
Code	A-Z	a-z	Z

Internal Code	Sun	Mon	Tue	Wed	Thu	Fri	Sat
Code	Т	U	V	W	Х	Y	Z



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			Vdss	30	V
Gate-Source Voltage			V _{GSS}	±12	V
Continuous Drain Current (Note 6) V_{GS} = 10V	Steady State	T _A = +25°C T _A = +70°C	lo	7.0 5.6	А
Maximum Continuous Body Diode Forward Curre	ent (Note 6)		ls	1.5	А
Pulsed Drain Current (10µs Pulse, Duty Cycle =	1%)		ldм	35	А
Avalanche Current (L = 0.1mH) (Note 7)			las	13	А
Avalanche Energy (L = 0.1mH) (Note 7)			Eas	9	mJ

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)	T _A = +25°C	PD	0.7	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	P	177	°C/W
Thermal Resistance, Junction to Ambient (Note 5)	t<10s	RθJA	124	C/W
Total Power Dissipation (Note 6)	T _A = +25°C	PD	2.1	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	D	61	°C/W
Thermal Resistance, Junction to Ambient (Note 6)	t<10s	R _{θJA}	43	C/W
Thermal Resistance, Junction to Case	Steady State	R _{θJC}	9.3	°C/W
Operating and Storage Temperature Range		TJ. TSTG	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Sympol	Mim	Turn	Mox	Unit	Test Condition	
OFF CHARACTERISTICS (Note 8)	Symbol	Min	Тур	Max	Unit	Test Condition	
Drain-Source Breakdown Voltage	D)/	30		1	V		
0	BVDSS			1		$V_{GS} = 0V, I_D = 250\mu A$	
Zero Gate Voltage Drain Current	IDSS	_		-	μA	$V_{DS} = 30V, V_{GS} = 0V$	
Gate-Source Leakage	lgss		_	±100	nA	$V_{GS} = \pm 12V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 8)			I.	I			
Gate Threshold Voltage	V _{GS(TH)}	0.6	—	1.4	V	$V_{DS} = V_{GS}, I_D = 250 \mu A$	
		_	17	28		$V_{GS} = 10V, I_D = 4.0A$	
Static Drain-Source On-Resistance	RDS(ON)	_	20	32	mΩ	$V_{GS} = 4.5V, I_D = 4.0A$	
	TCDS(ON)	_	24	42	11132	$V_{GS} = 3.0V, I_{D} = 4.0A$	
		—	28	50		V _{GS} = 2.5V, I _D = 4.0A	
Diode Forward Voltage	V _{SD}	—	0.7	1.2	V	$V_{GS} = 0V, I_{S} = 1A$	
DYNAMIC CHARACTERISTICS (Note 9)						-	
Input Capacitance	Ciss	—	570	—			
Output Capacitance	Coss	_	63	_	pF	VDS = 15V, VGS = 0V f = 1.0MHz	
Reverse Transfer Capacitance	Crss		53	_			
Gate Resistance	Rg		3.2	—	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1.0MHz$	
Total Gate Charge (V _{GS} = 10V)	Qg	_	13.3	—			
Total Gate Charge (V _{GS} = 4.5V)	Qg	_	6.1	—	-0		
Gate-Source Charge	Qgs		1.0	—	nC	$V_{DS} = 15V, I_D = 6.9A$	
Gate-Drain Charge	Q _{gd}		1.6	—			
Turn-On Delay Time	t _{D(ON)}		1.5	—			
Turn-On Rise Time	t _R		3.3	_		$V_{GS} = 10V, V_{DD} = 15V, R_g = 3\Omega,$	
Turn-Off Delay Time	t _{D(OFF)}	_	13.9	_	ns	$I_{D} = 6.9A$	
Turn-Off Fall Time	tF	_	4.9	—			
Body Diode Reverse Recovery Time	t _{RR}	_	7.8	—	ns	Is = 5A, dI/dt = 100A/µs	
Body Diode Reverse Recovery Charge	Q _{RR}	_	1.9	—	nC	$I_{s} = 5A, dI/dt = 100A/\mu s$	

Notes: 5. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.

6. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.

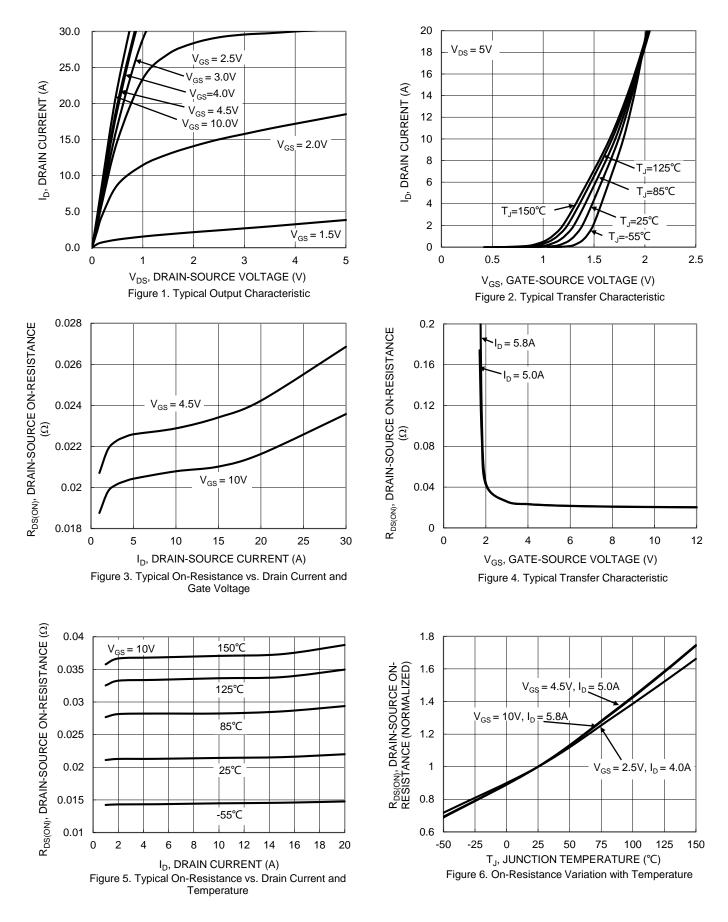
7. I_{AS} and E_{AS} ratings are based on low frequency and duty cycles to keep $T_J = +25^{\circ}C$.

8. Short duration pulse test used to minimize self-heating effect.

9. Guaranteed by design. Not subject to product testing.



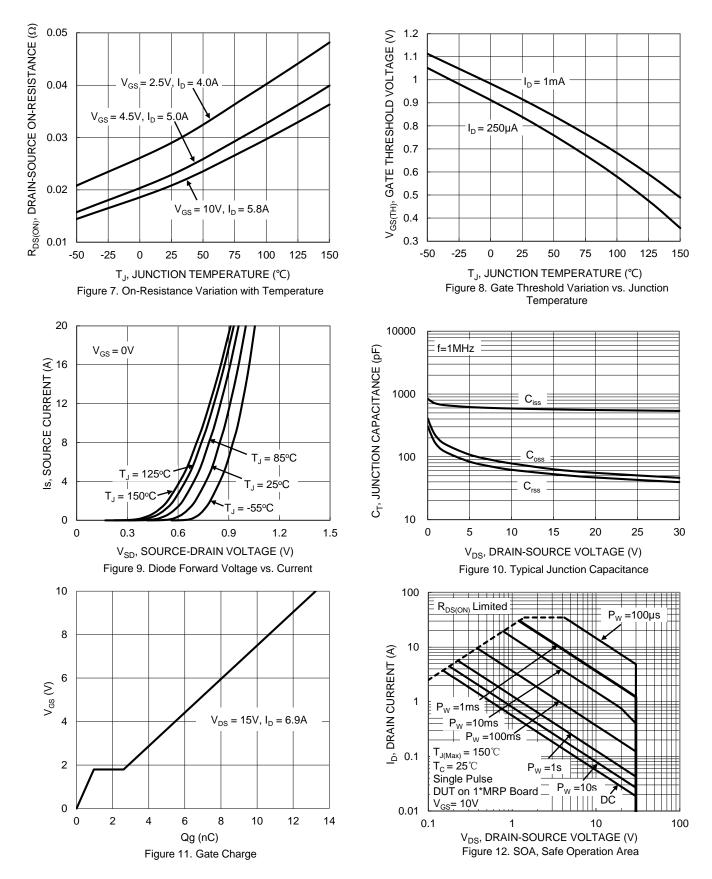
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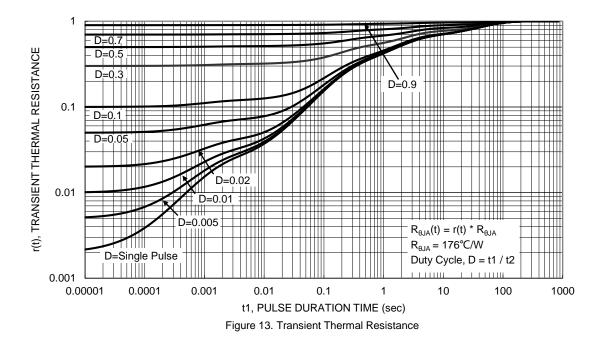
DMN3042LFDF Datasheet number: DS38985 Rev. 2 - 2



DMN3042LFDF



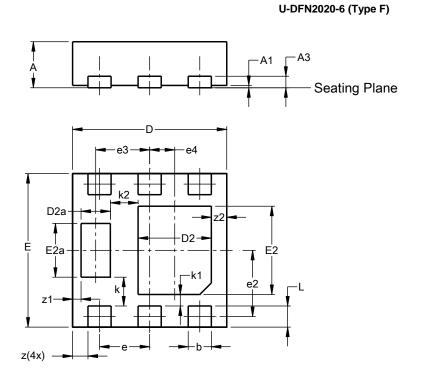






Package Outline Dimensions

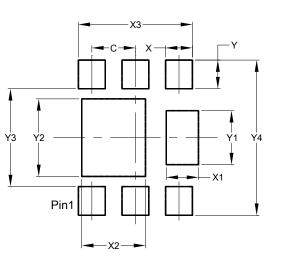
Please see http://www.diodes.com/package-outlines.html for the latest version.



	U-DFN2020-6 (Type F)							
Dim	Min	Max	Тур					
A	0.57	0.57 0.63 0.60						
A1	0.00	0.05	0.03					
A3	-	-	0.15					
b	0.25	0.35	0.30					
D	1.95	2.05	2.00					
D2	0.85	1.05	0.95					
D2a	0.33	0.43	0.38					
E	1.95	2.05	2.00					
E2	1.05	1.05 1.25 1.1						
E2a	0.65	0.75	0.70					
е		0.65 BS						
e2).863 BS						
e3		0.70 BS						
e4).325 BS						
k		0.37 BS	С					
k1		0.15 BS						
k2		0.36 BS						
L	0.225	0.325	0.275					
z		0.20 BS	-					
z1		0.110 BSC						
z2		0.20 BS	С					
All D	Dimens	ions in	mm					

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.



U-DFN2020-6 (Type F)

Dimensions	Value (in mm)
С	0.650
Х	0.400
X1	0.480
X2	0.950
X3	1.700
Y	0.425
Y1	0.800
Y2	1.150
Y3	1.450
Y4	2.300



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