

2.5/3.3V CML XO

NX506



5.0 x 3.2mm Ceramic SMD

Product Features

- Very low phase jitter < 1.0ps RMS max.
- Wide frequency range $5 \sim 1000 \text{MHz}$
- Thicker crystal for improved reliability
- Low supply current 70mA max.
- Industrial Temperature Range
- Pb-free & RoHS compliant

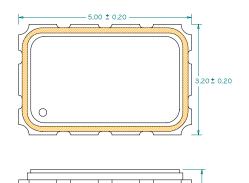
Product Description

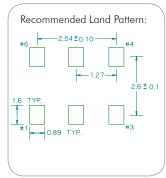
The NX506 XO series is a high performance CML crystal oscillator family with very low jitter performance. It supports various options including wider frequency range, 2.5/3.3 voltage, various stabilities, and different package sizes. It is designed to meet the clock source specifications for communication systems, and other high performance equipment.

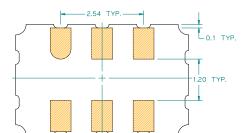
Applications

- Networking systems
- Servers and storage systems
- Profession video equipments
- Test and measurement
- FPGA/ASIC clock generation

Package: (Scale: none, Dimensions are in mm)







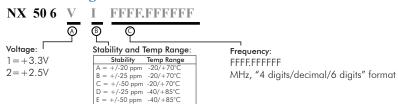
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Pin Functions:

Pin	Function					
1	OE Function					
2	N/C					
3	Ground					
4	Q					
5	Q					
6	V _{CC}					

*Extended high frequency power decoupling is recommended (see test circuit for minimum recommendation). To ensure optimal performance, do not route RF traces beneath the package.

Part Ordering Information:



SaRonix-eCera[™] is a Pericom® Semiconductor company

• US: +1-408-232-9100 TW: +886-3-4518888

www.pericom.com



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Ultra Low Jitter CML Crystal Oscillator 5.0 x 3.2mm

Electrical Performance

Parameter		Min.	Тур.	Max.	Units	Notes	
Output Frequency		5		1000	MHz		
Supply Voltage		3.135	3.3	3.465	3.7	Consideration	
		2.375	2.5	2.625	V	See ordering options	
Supply Current, Ou	tput Enabled			70	mA		
Supply Current, Out	tput Disabled only			40	mA		
Frequency Stability				±50	ppm	See ordering options	
Operating Temperat	Operating Temperature Range			+85	°C	See ordering options	
Output Load		100Ω connected between outputs				Output requires termination	
Differential Output	Voltage. V _{OD}	0.35		0.65	V		
Output Offset Volta	ge. V _{OS}	1.125		1.375	V		
Duty Cycle		45		55	%	Measured 50% V _{CC}	
Rise and Fall Time				400	ps	Measured 20/80% of waveform	
Jitter, Accumulated	Jitter, Accumulated, RMS (1-σ)			6	ps	20.000 adjacent periods	
Jitter, Phase, RMS	< 40MHz		0.4	1	ps	12kHz to 5 MHz frequency band	
	40 to 1000MHz		0.4	1	ps	12kHz to 20 MHz frequency band	
	125MHz, 156.25MHz		0.4	0.6	ps	12kHz to 20 MHz frequency band	
Jitter, pk-pk	Jitter, pk-pk			40	ps	100,000 random periods	

Notes:

- Stability includes all combinations of operating temperature, load changes, rated input (supply) voltage changes, initial calibration tolerance (25°C), aging (1 year at 25°C average effective ambient temperature), shock and vibration.
- 2. Phase jitter typical value is depending on output frequencies.
- 3. For specifications other than those listed, please contact sales.

Output Enable / Disable Function

Parameter	Min.	Тур.	Max.	Units	Notes
Input Voltage (pin 1), Output Enable	0.7 V _{CC}			V	or open
Input Voltage (pin 1), Output Disable (low power standby)			0.3 V _{CC}	V	Output is Hi-Z
Output Disable Delay			100	ns	
Output Enable Delay			100	ns	
Start up Time			10	ms	

Absolute Maximum Ratings

Parameter	Min.	Тур.	Max.	Units	Notes
Storage Temperature	-55		+125	°C	

For the latest product information visit: http://www.pericom.com/products/crystals-and-crystal-oscillators/hiflex-xo/?part=NX506

For test circuit go to: http://www.pericom.com/pdf/sre/tc-CML.pdf

For soldering reflow profile and reliability test ratings go to: http://www.pericom.com/pdf/sre/reflow.pdf

For tape and reel information go to: http://www.pericom.com/pdf/sre/tr 5032 xo.pdf



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