



P-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary (Typ. @ V_{GS} = -4.5V, T_A = +25°C)

| BV _{DSS} | R _{DS(ON)} | Qg | Q_{gd} | I _D |
|-------------------|---------------------|-------|----------|----------------|
| -8V | 8.2mΩ | 8.1nC | 1.8nC | -10A |

Description

This 3rd generation Lateral MOSFET (LD-MOS) is engineered to minimize on-state losses and switch ultra-fast, making it ideal for high-efficiency power transfer. It uses Chip-Scale Package (CSP) to increase power density by combining low thermal impedance with minimal R_{DS(ON)} per footprint area.

Applications

- DC-DC Converters
- **Battery Management**
- Load Switch

Features

- LD-MOS Technology with the Lowest Figure of Merit: $-R_{DS(ON)} = 8.2m\Omega$ to Minimize On-State Losses
 - -Q_q = 8.1nC for Ultra-Fast Switching
- V_{GS(th)} = -0.8V Typ. for a Low Turn-On Potential
- CSP with Footprint 1.5mm x 1.5mm
- Height = 0.60mm for Low Profile
- ESD = 6kV HBM Protection of Gate
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

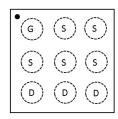
Mechanical Data

Case: U-WLB1515-9

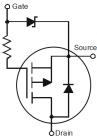
Terminal Connections: See Diagram Below

U-WLB1515-9 (Type B)





Top View Pin Configuration



Equivalent Circuit

Ordering Information (Note 4)

| | Part Number | Case | Packaging | | | | | |
|--------|---|----------------------|-------------------|--|--|--|--|--|
| | DMP1011UCB9-7 | U-WLB1515-9 (Type B) | 3,000/Tape & Reel | | | | | |
| Notes: | s: 1 No purposely added lead. Fully FLI Directive 2002/95/FC (RoHS). 2011/65/FLI (RoHS 2) & 2015/863/FLI (RoHS 3) compliant | | | | | | | |

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.

- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information

U-WLB1515-9 (Type B)



NX = Product Type Marking Code YM = Date Code Marking Y or \overline{Y} = Year (ex: G = 2019) M or \overline{M} = Month (ex: 9 = September)

Date Code Key

| Year | 2012 | | 2013 | 2014 | | 2015 | 2016 | | 2017 | 2018 | | 2019 |
|-------|------|-----|------|------|-----|------|------|-----|-------|------|-----|------|
| Code | Z | | Α | В | | С | D | | E | F | | G |
| Month | Jan | Feb | Mar | Apr | May | Jun | Jul | Aug | J Sep | Oct | Nov | Dec |
| Code | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 | 0 | N | D |



Maximum Ratings (@ $T_A = +25$ °C, unless otherwise specified.)

| Characteristic | | Symbol | Value | Unit | |
|---|-----------------|--|----------------|--------------|---|
| Drain-Source Voltage | | V _{DSS} | -8 | V | |
| Gate-Source Voltage | | V _{GSS} | -6 | V | |
| Continuous Drain Current (Note 5) V _{GS} = -4.5V | Steady State | $T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$ | I _D | -10 -8 | А |
| Continuous Drain Current (Note 6) V _{GS} = -4.5V | Steady State | $T_A = +25$ °C $T_A = +70$ °C | I _D | -7.4 -6.0 | А |
| Pulsed Drain Current (Pulse Duration 10µs, Duty C | ycle ≤1%) | I _{DM} | -50 | Α | |
| Continuous Source Pin Current (Note 6) | | Is | -2 | Α | |
| Pulsed Source Pin Current (Pulse Duration 10µs, D | outy Cycle: | Ism | -15 | Α | |
| Continuous Gate Current | | | I _G | -0.5 | Α |

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

| Characteristic | Symbol | Value | Unit |
|--|-------------------|-------------|------|
| Total Power Dissipation (Note 5) | P _D | 0.89 | W |
| Total Power Dissipation (Note 6) | P _D | 1.57 | W |
| Thermal Resistance, Junction to Ambient (Note 5) | $R_{\theta JA}$ | 142.1 | °C/W |
| Thermal Resistance, Junction to Ambient (Note 6) | R _{0JA} | 80.5 | °C/W |
| Operating and Storage Temperature Range | $T_{J_i} T_{STG}$ | -55 to +150 | °C |

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

| Characteristic | Symbol | Min | Тур | Max | Unit | Test Condition | |
|---|---------------------|------|------|-------|------|--|--|
| OFF CHARACTERISTICS (Note 7) | | | | | | | |
| Drain-Source Breakdown Voltage | BV _{DSS} | -8 | | _ | V | $V_{GS} = 0V, I_D = -250\mu A$ | |
| Gate to Source Voltage | BV _{SGS} | 6 | | _ | V | $V_{DS} = 0V, I_{S} = 250\mu A$ | |
| Zero Gate Voltage Drain Current @T _C = +25°C | I _{DSS} | _ | _ | -1 | μΑ | $V_{DS} = -4.0V, V_{GS} = 0V$ | |
| Gate-Source Leakage | I _{GSS} | l | | -100 | nA | $V_{GS} = -4.0V, V_{DS} = 0V$ | |
| ON CHARACTERISTICS (Note 7) | | | | | | | |
| Gate Threshold Voltage | V _{GS(TH)} | -0.4 | -0.8 | -1.1 | V | $V_{DS} = V_{GS}, I_{D} = -250 \mu A$ | |
| | | | 8.2 | 10 | | $V_{GS} = -4.5V$, $I_D = -2A$ | |
| Static Drain-Source On-Resistance | R _{DS(ON)} | _ | 10 | 13 | mΩ | $V_{GS} = -3.0V, I_D = -2A$ | |
| | | | 11 | 14 | | $V_{GS} = -2.5V, I_D = -2A$ | |
| Forward Transfer Admittance | Y _{fs} | _ | 16.8 | _ | S | $V_{DS} = -4V, I_{D} = -2A$ | |
| Diode Forward Voltage (Note 6) | V_{SD} | - | -0.7 | -1 | V | $V_{GS} = 0V, I_{S} = -2A$ | |
| Reverse Recovery Charge | Q_{RR} | _ | 6.3 | _ | nC | $V_{DD} = -5V, I_F = -2A,$ | |
| Reverse Recovery Time | t _{RR} | _ | 18.5 | _ | ns | di/dt = 200A/µs | |
| DYNAMIC CHARACTERISTICS (Note 8) | | | | | | | |
| Input Capacitance | C _{iss} | 1 | 817 | 1,060 | pF | \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ | |
| Output Capacitance | Coss | 1 | 595 | 770 | pF | $V_{DS} = -4V, V_{GS} = 0V,$ f = 1.0MHz | |
| Reverse Transfer Capacitance | C _{rss} | _ | 269 | 350 | pF | 1 = 1.000112 | |
| Series Gate Resistance | R_{G} | _ | 1.9 | _ | Ω | $V_{DS} = 0V, V_{GS} = 0V, f = 1.0MHz$ | |
| Total Gate Charge | Qg | _ | 8.1 | 10.5 | nC | \ | |
| Gate-Source Charge | Q _{gs} | _ | 0.9 | _ | nC | $V_{GS} = -4.5V, V_{DS} = -4V,$ | |
| Gate-Drain Charge | Q_{gd} | _ | 1.8 | _ | nC | $I_D = -2A$ | |
| Turn-On Delay Time | t _{D(ON)} | _ | 6.2 | 10 | ns | | |
| Turn-On Rise Time | t _R | _ | 22.6 | _ | ns | $V_{DD} = -4V$, $V_{GS} = -4.5V$, | |
| Turn-Off Delay Time | t _{D(OFF)} | _ | 30.1 | 48 | ns | $I_{DS} = -2A$, $R_G = 10\Omega$ | |
| Turn-Off Fall Time | t _F | _ | 22.7 | _ | ns | | |

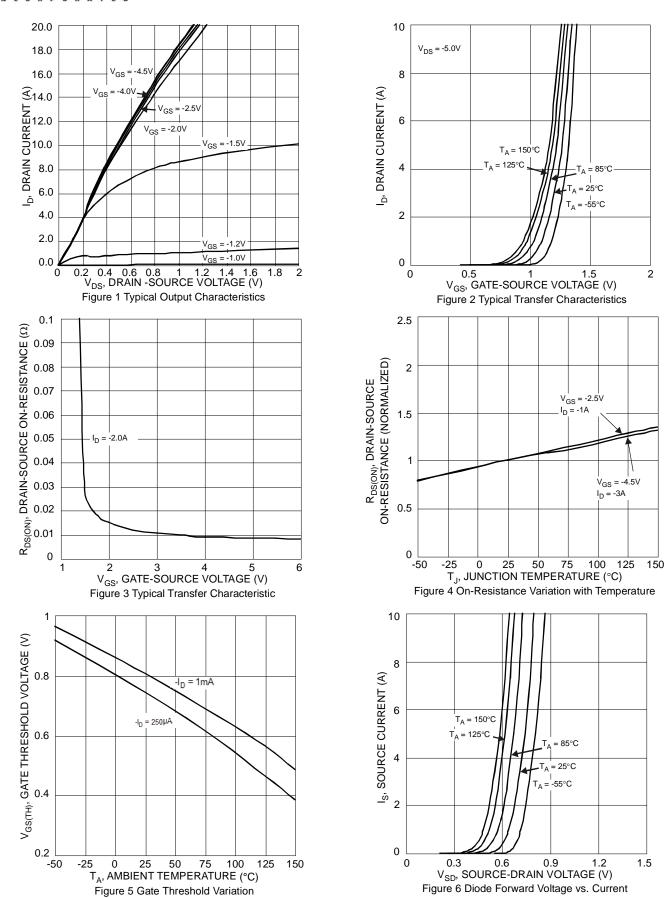
Notes: 5. Device mounted on FR-4 PCB with minimum recommended pad layout.

6. Device mounted on FR-4 material with 1-inch² (6.45cm²), 2oz (0.071mm thick) Cu.

7. Short duration pulse test used to minimize self-heating effect.

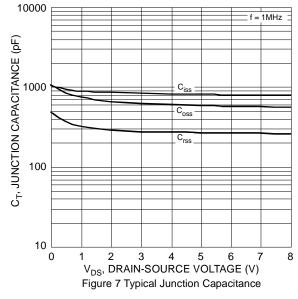
8. Guaranteed by design. Not subject to production testing.

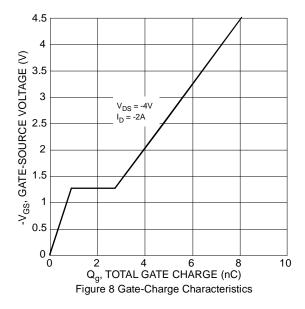


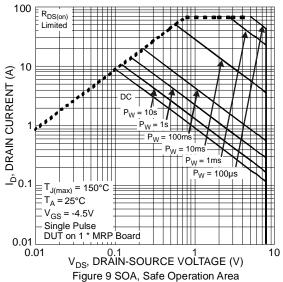


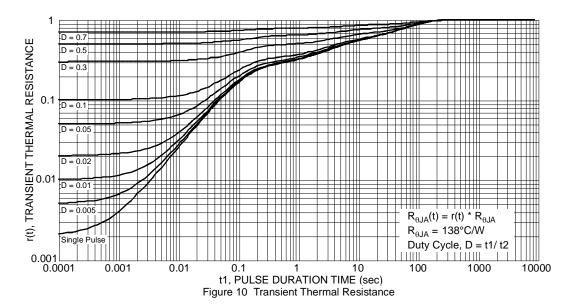
vs. Ambient Temperature









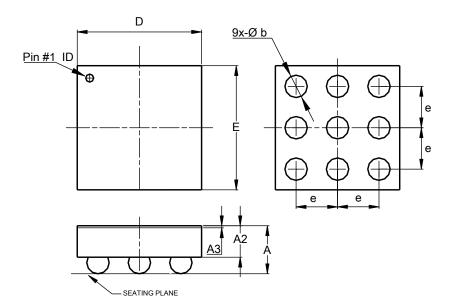




Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

U-WLB1515-9 (Type B)

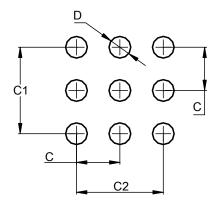


| U-WLB1515-9 (Type B) | | | | | | | | | |
|-------------------------|-------|-------|-------|--|--|--|--|--|--|
| Dim | | | | | | | | | |
| Α | | 0.60 | | | | | | | |
| A2 | | 0.36 | 0.36 | | | | | | |
| A3 | 0.020 | 0.030 | 0.025 | | | | | | |
| b | 0.22 | 0.32 | 0.27 | | | | | | |
| D | 1.47 | 1.50 | 1.49 | | | | | | |
| Е | 1.47 | 1.50 | 1.49 | | | | | | |
| е | | | 0.50 | | | | | | |
| All Dimensions in mm | | | | | | | | | |

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

U-WLB1515-9 (Type B)



| Dimensions | Value (in mm) |
|------------|------------------|
| С | 0.50 |
| C1 | 1.00 |
| C2 | 1.00 |
| D | 0.25 |



IMPORTANT NOTICE

DIODES INCORPORATED MAKES NO WARRANTY OF ANY KIND, EXPRESS OR IMPLIED, WITH REGARDS TO THIS DOCUMENT, INCLUDING, BUT NOT LIMITED TO, THE IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A PARTICULAR PURPOSE (AND THEIR EQUIVALENTS UNDER THE LAWS OF ANY JURISDICTION).

Diodes Incorporated and its subsidiaries reserve the right to make modifications, enhancements, improvements, corrections or other changes without further notice to this document and any product described herein. Diodes Incorporated does not assume any liability arising out of the application or use of this document or any product described herein; neither does Diodes Incorporated convey any license under its patent or trademark rights, nor the rights of others. Any Customer or user of this document or products described herein in such applications shall assume all risks of such use and will agree to hold Diodes Incorporated and all the companies whose products are represented on Diodes Incorporated website, harmless against all damages.

Diodes Incorporated does not warrant or accept any liability whatsoever in respect of any products purchased through unauthorized sales channel. Should Customers purchase or use Diodes Incorporated products for any unintended or unauthorized application, Customers shall indemnify and hold Diodes Incorporated and its representatives harmless against all claims, damages, expenses, and attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized application.

Products described herein may be covered by one or more United States, international or foreign patents pending. Product names and markings noted herein may also be covered by one or more United States, international or foreign trademarks.

This document is written in English but may be translated into multiple languages for reference. Only the English version of this document is the final and determinative format released by Diodes Incorporated.

LIFE SUPPORT

Diodes Incorporated products are specifically not authorized for use as critical components in life support devices or systems without the express written approval of the Chief Executive Officer of Diodes Incorporated. As used herein:

- A. Life support devices or systems are devices or systems which:
 - 1. are intended to implant into the body, or
 - 2. support or sustain life and whose failure to perform when properly used in accordance with instructions for use provided in the labeling can be reasonably expected to result in significant injury to the user.
- B. A critical component is any component in a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or to affect its safety or effectiveness.

Customers represent that they have all necessary expertise in the safety and regulatory ramifications of their life support devices or systems, and acknowledge and agree that they are solely responsible for all legal, regulatory and safety-related requirements concerning their products and any use of Diodes Incorporated products in such safety-critical, life support devices or systems, notwithstanding any devices- or systems-related information or support that may be provided by Diodes Incorporated. Further, Customers must fully indemnify Diodes Incorporated and its representatives against any damages arising out of the use of Diodes Incorporated products in such safety-critical, life support devices or systems.

Copyright © 2019, Diodes Incorporated

www.diodes.com

DMP1011UCB9
Document number: DS37852 Rev. 5 - 2

单击下面可查看定价,库存,交付和生命周期等信息

>>Diodes Incorporated(达迩科技(美台))