



Summary

BV _{DSS}	R _{DS(ON)} Max	I _D Max T _A = +25°C
60V	85mΩ @ V _{GS} = 10V	2.5A
60V	120mΩ @ V_{GS} = 4.5V	2.0A

Description

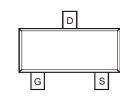
This new generation MOSFET is designed to minimize the on-state resistance ($R_{DS(ON)}$) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

- DC-DC Converters
- Power Management Functions
- Backlighting

SOT23

Top View



Top View

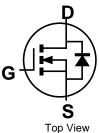
60V N-CHANNEL ENHANCEMENT MODE MOSFET

Features and Benefits

- N MOSFET
- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

Mechanical Data

- Case: SOT23
- Case Material: Molded Plastic, "Green" Molding Compound; UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram Below
- Terminals: Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 ³
- Weight: 0.008 grams (Approximate)



Pin Configuration

Ordering Information (Note 4)

Product	Reel Size (inches)	Tape Width (mm)	Quantity per Reel
DMN6075S-7	7	8	3,000
DMN6075S-13	13	8	10,000

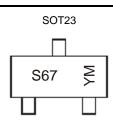
Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.

 See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



S67 = Product Type Marking Code YM = Date Code Marking Y or \overline{Y} = Year (ex: F = 2018) M = Month (ex: 9 = September)

Date Code Key

Dale Coue	Rey												
Year	2014	1	~	2017	2018	2019	2020	202	1 20	22	2023	2024	2025
Code	В		~	E	F	G	Н	I		J	K	L	М
Mont	h	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	;	1	2	3	4	5	6	7	8	9	0	N	D



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit	
Drain-Source Voltage		V _{DSS}	60	V	
Gate-Source Voltage	V _{GSS}	±20	V		
Continuous Drain Current (Note 5) V _{GS} = 10V	Steady State	T _A = +25°C T _A = +70°C	ID	2.0 1.5	A
Continuous Drain Current (Note 6) $V_{GS} = 10V$ Steady State $T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$		ID	2.5 2.0	A	
Maximum Body Diode Forward Current (Note 5)			Is	2.0	А
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%	I _{DM}	12	A		

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit	
Total Power Dissipation (Note 5)	T _A = +25°C	D-	0.8	W	
Total Fower Dissipation (Note 3)	T _A = +70°C	PD	0.5	vv	
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	$R_{ heta JA}$	157	°C/W	
Total Power Dissipation (Note 6)	T _A = +25°C	D	1.15	W	
Total Power Dissipation (Note 6)	T _A = +70°C	PD	0.7	vv	
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	$R_{ extsf{ heta}JA}$	110	°C/W	
Operating and Storage Temperature Range		T _{J,} T _{STG}	-55 to +150	°C	

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)			,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,			
Drain-Source Breakdown Voltage	BV _{DSS}	60			V	$V_{GS} = 0V, I_D = 250 \mu A$
Zero Gate Voltage Drain Current TJ = +25°C	I _{DSS}	_	—	1.0	μA	$V_{DS} = 60V, V_{GS} = 0V$
Gate-Source Leakage	I _{GSS}	_	—	±100	nA	$V_{GS} = \pm 16V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V _{GS(TH)}	1	—	3	V	$V_{DS} = V_{GS}, I_D = 250 \mu A$
Static Drain-Source On-Resistance	Deserve		69	85	mΩ	$V_{GS} = 10V, I_D = 3.2A$
	Rds(on)	_	75	120	11152	$V_{GS} = 4.5V, I_D = 2.8A$
Diode Forward Voltage	V _{SD}	_	_	1.2	V	$V_{GS} = 0V, I_{S} = 2.5A$
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C _{iss}	—	606	—	pF	
Output Capacitance	C _{oss}	—	32.6	—	pF	$V_{DS} = 20V, V_{GS} = 0V,$ - f = 1.0MHz
Reverse Transfer Capacitance	Crss	_	24.6	_	pF	
Gate Resistance	Rg	_	1.5	_	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1MHz$
Total Gate Charge (V _{GS} = 10V)	Qg	_	12.3	_	nC	
Total Gate Charge (V _{GS} = 4.5V)	Qg	_	5.6	_	nC	
Gate-Source Charge	Q _{gs}	_	1.7	—	nC	$-V_{DS} = 30V, I_{D} = 3A$
Gate-Drain Charge	Q _{gd}	_	1.9	_	nC	
Turn-On Delay Time	t _{D(ON)}	_	3.5	_	ns	
Turn-On Rise Time		_	4.1	_	ns	V _{GS} = 10V, V _{DS} = 30V,
Turn-Off Delay Time	t _{D(OFF)} — 35 —		ns	$R_g = 20\Omega, R_L = 50\Omega$		
Turn-Off Fall Time	t _F	_	11		ns	

5. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout. Notes:

Device mounted on FR-4 substrate PC board, 2oz copper, with thermal vias to bottom layer 1-inch square copper plate.
Short duration pulse test used to minimize self-heating effect.

8. Guaranteed by design. Not subject to product testing.

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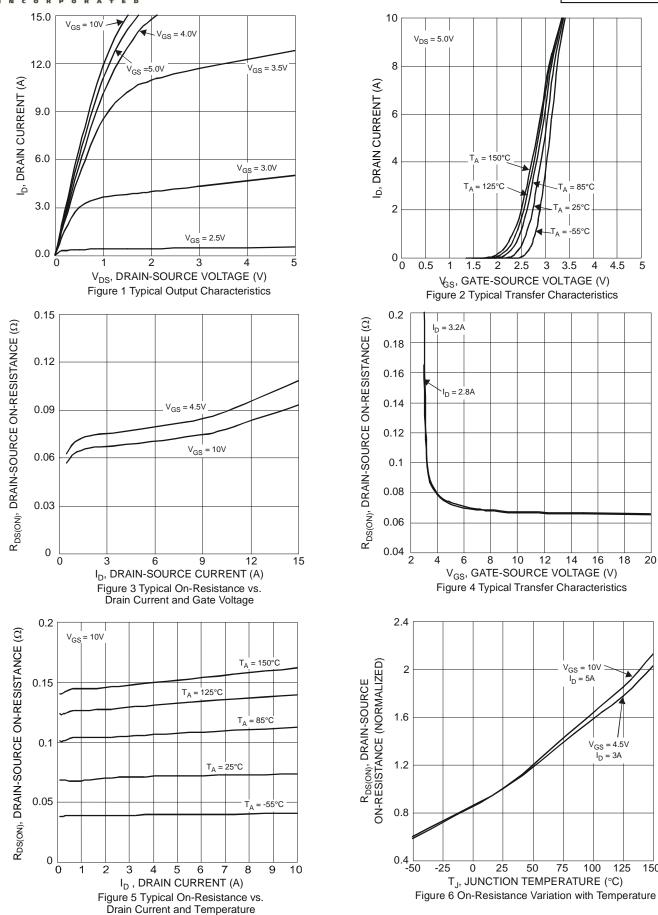


4 4.5 5

16 18 20

V_{GS} = 4.5V $I_D = 3A$

100

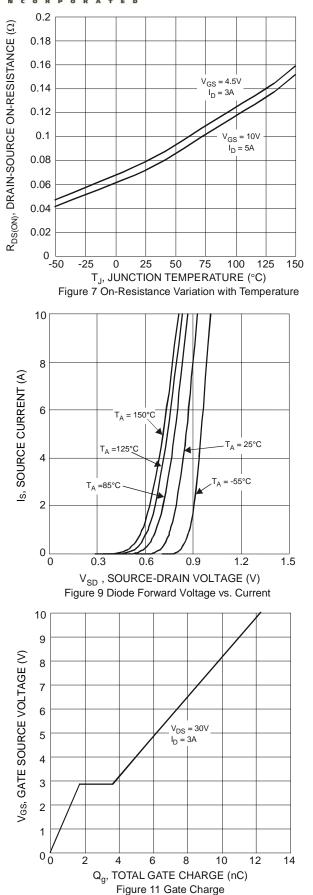


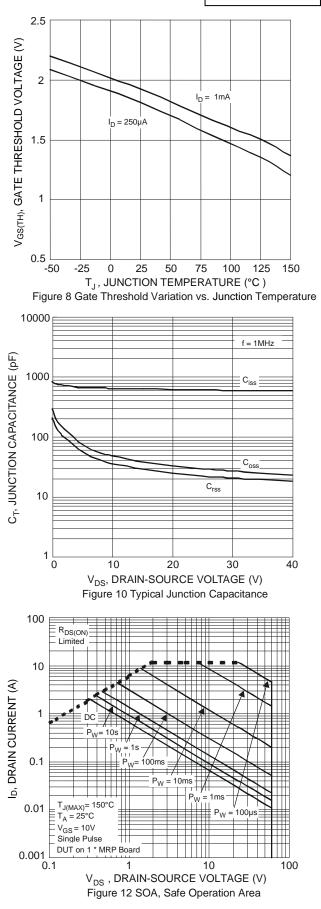
125

150

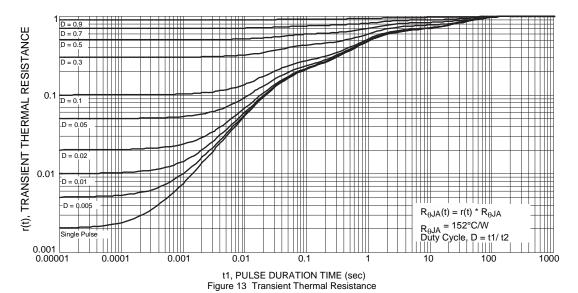


DMN6075S





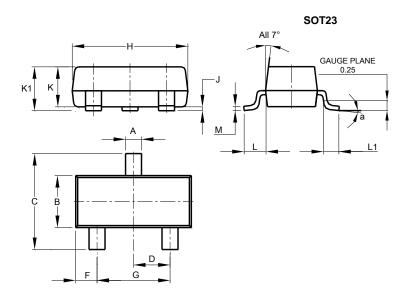






Package Outline Dimensions

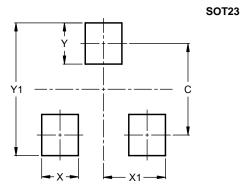
Please see http://www.diodes.com/package-outlines.html for the latest version.



	SOT23							
Dim	Min	Max	Тур					
Α	0.37	0.51	0.40					
В	1.20	1.40	1.30					
С	2.30	2.50	2.40					
D	0.89	1.03	0.915					
F	0.45	0.60	0.535					
G	1.78	2.05	1.83					
Н	2.80	3.00	2.90					
J	0.013	0.10	0.05					
К	0.890	1.00	0.975					
K1	0.903	1.10	1.025					
L	0.45	0.61	0.55					
L1	0.25	0.55	0.40					
М	0.085	0.150	0.110					
а	0°	8°						
All	Dimens	ions in	mm					

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.



Dimensions	Value (in mm)
С	2.0
Х	0.8
X1	1.35
Y	0.9
Y1	2.9



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