

### N-CHANNEL ENHANCEMENT MODE MOSFET

## **Product Summary**

BV <sub>DSS</sub>	R <sub>DS(ON)</sub>	I <sub>D</sub> T <sub>A</sub> = +25°C
001/	2Ω @ V <sub>GS</sub> = 4V	400mA
60V	2.5Ω @ V <sub>GS</sub> = 2.5V	350mA

## **Features and Benefits**

- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- ESD Protected
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e.: parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please refer to the related automotive grade (Q-suffix) part. A listing can be found at

https://www.diodes.com/products/automotive/automotive-products/.

 This part is qualified to JEDEC standards (as references in AEC-Q) for High Reliability.

https://www.diodes.com/quality/product-definitions/

 An Automotive-Compliant Part is Available Under Separate Datasheet (DMN62D1LFDQ)

## **Description and Applications**

This new generation MOSFET is designed to minimize the on-state resistance (R<sub>DS(ON)</sub>) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

- DC-DC Converters
- Power Management Functions
- Battery Operated Systems and Solid-State Relays
- Drivers: Relays, Solenoids, Lamps, Hammers, Displays, Memories, Transistors, etc.

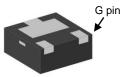
### **Mechanical Data**

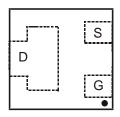
- Case: U-DFN1212-3
- Case Material: Molded Plastic. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: NiPdAu over Copper Leadframe. Solderable per MIL-STD-202, Method 208@4
- Terminal Connections: See Diagram
- Weight: 0.005 grams (Approximate)

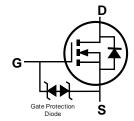
U-DFN1212-3 (Type C)











Top View Bottom View

Pin-Out Top View

**Equivalent Circuit** 

### **Ordering Information** (Note 4)

Part Number	Case	Packaging
DMN62D1LFD-7	U-DFN1212-3 (Type C)	3,000/Tape & Reel
DMN62D1LFD-13	U-DFN1212-3 (Type C)	10,000/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/



## **Marking Information**

Site 1:





K64 = Product Type Marking Code K63 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: H = 2020) M = Month (ex: 9 = September)

Date Code Key

Date Code Key												
Year	2014		2020	2021	2022	2023	2024	2025	2026	2027	2028	2029
Code	В		Н	I	J	K	L	М	N	0	Р	R
Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec

Site 2:



K63 YWX

K64 = Product Type Marking Code K63 = Product Type Marking Code YWX = Date Code Marking Y = Year (ex: 0 = 2020)

W = Week (ex: a = week 27; z represents week 52 and 53)

X = Internal Code (ex: U = Monday)

Date Code Key

Vac-	2044		2020	2024	2022	2022	2024	2025	2020	2027	2020	2020
Year	2014	•••	2020	2021	2022	2023	2024	2025	2026	2027	2028	2029
Code	4		0	1	2	3	4	5	6	7	8	9
Week	Week 1-26					27	52			5	2	
	1-20				27-52				53			
Code	A-Z			Code A-Z a-z z					<u>z</u>			
Internal Code	Sun		Mon		Tue	W	ed	Thu		Fri		Sat
Code	T		U		V	V		X		Y		Z



# **Maximum Ratings** (@ $T_A = +25^{\circ}C$ , unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Drain-Source Voltage		V <sub>DSS</sub>	60	V
Gate-Source Voltage	V <sub>GSS</sub>	±20	V	
Continuous Drain Current (Note 5) V <sub>GS</sub> = 4V	$T_A = +25$ °C $T_A = +70$ °C	ID	400 310	mA
Pulsed Drain Current (Note 6)		I <sub>DM</sub>	1	Α

## **Thermal Characteristics**

Characteristic	Symbol	Max	Unit
Power Dissipation (Note 5)	PD	0.5	W
Thermal Resistance, Junction to Ambient @T <sub>A</sub> = +25°C (Note 5)	Reja	237	°C/W
Operating and Storage Temperature Range	TJ, TSTG	-55 to +150	°C

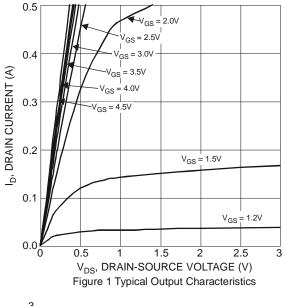
# **Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

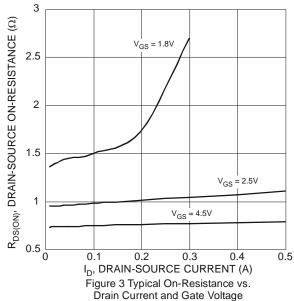
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)	•		•		•	
Drain-Source Breakdown Voltage	BVDSS	60	_	_	V	V <sub>G</sub> S = 0V, I <sub>D</sub> = 250μA
Zero Gate Voltage Drain Current T <sub>J</sub> = +25°C	IDSS	_	_	1	μΑ	V <sub>DS</sub> = 60V, V <sub>GS</sub> = 0V
		_	_	±100	nA	$V_{GS} = \pm 5V$ , $V_{DS} = 0V$
Gate-Source Leakage	Igss	_	_	±500	nA	$V_{GS} = \pm 10V$ , $V_{DS} = 0V$
		_	_	±2	μΑ	$V_{GS} = \pm 15V$ , $V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	Vgs(TH)	0.6	_	1	V	$V_{DS} = V_{GS}$ , $I_D = 250\mu A$
			0.8	2		V <sub>G</sub> S = 4V, I <sub>D</sub> = 100mA
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>	_	1	2.5	Ω	$V_{GS} = 2.5V, I_{D} = 50mA$
Static Dialii-Source Off-Resistance		-	1.4	3	12	V <sub>G</sub> S = 1.8V, I <sub>D</sub> = 50mA
		_	1.8	_		$V_{GS} = 1.5V, I_D = 10mA$
Forward Transfer Admittance	Y <sub>fs</sub>	_	1.8	_	S	$V_{DS} = 10V, I_D = 200mA$
Diode Forward Voltage	V <sub>SD</sub>	_	0.8	1.3	V	V <sub>GS</sub> = 0V, I <sub>S</sub> = 115mA
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	Ciss	_	36	_		V 05V V 0V
Output Capacitance	Coss	1	4.6	_	pF	$V_{DS} = 25V, V_{GS} = 0V,$ f = 1MHz
Reverse Transfer Capacitance	Crss	_	3.6	_		1 – 1101112
Gate Resistance	Rg	_	59.8	_	Ω	$V_{DS} = 0V$ , $V_{GS} = 0V$ , $f = 1MHz$
Total Gate Charge	Qg	_	0.55	_		
Gate-Source Charge	Qgs	_	0.08	_	nC	$V_{GS} = 4.5V, V_{DS} = 10V,$ $I_{D} = 250mA$
Gate-Drain Charge	Qgd	_	0.12	_		ID = 250IIIA
Turn-On Delay Time	t <sub>D</sub> (ON)	_	2.1	_	ns	
Turn-On Rise Time	t <sub>R</sub>	_	2.8	_	ns	Vgs = 10V, Vps = 30V,
Turn-Off Delay Time	tD(OFF)	_	21	_	ns	$R_L = 150Ω$ , $R_G = 25Ω$ , $I_D = 200mA$
Turn-Off Fall Time	tF	-	13.9	_	ns	- 10 - 200IIIA

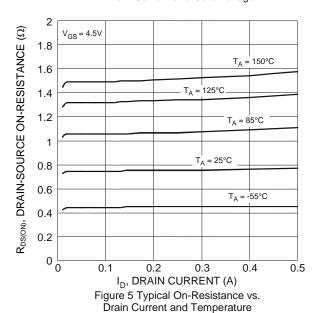
Notes:

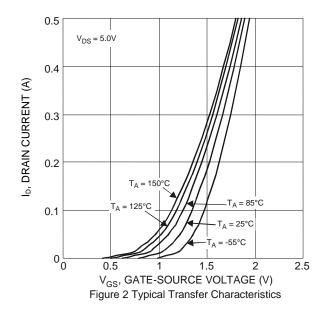
- 5. Device mounted on FR-4 PCB with minimum recommended pad layout, single sided.
- Repetitive rating, pulse width limited by junction temperature.
   Short duration pulse test used to minimize self-heating effect.
- 8. Guaranteed by design. Not subject to production testing.

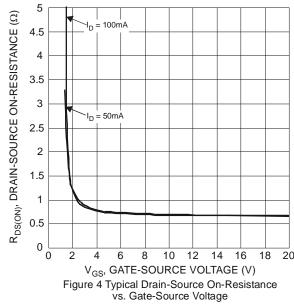












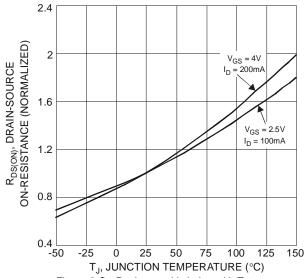
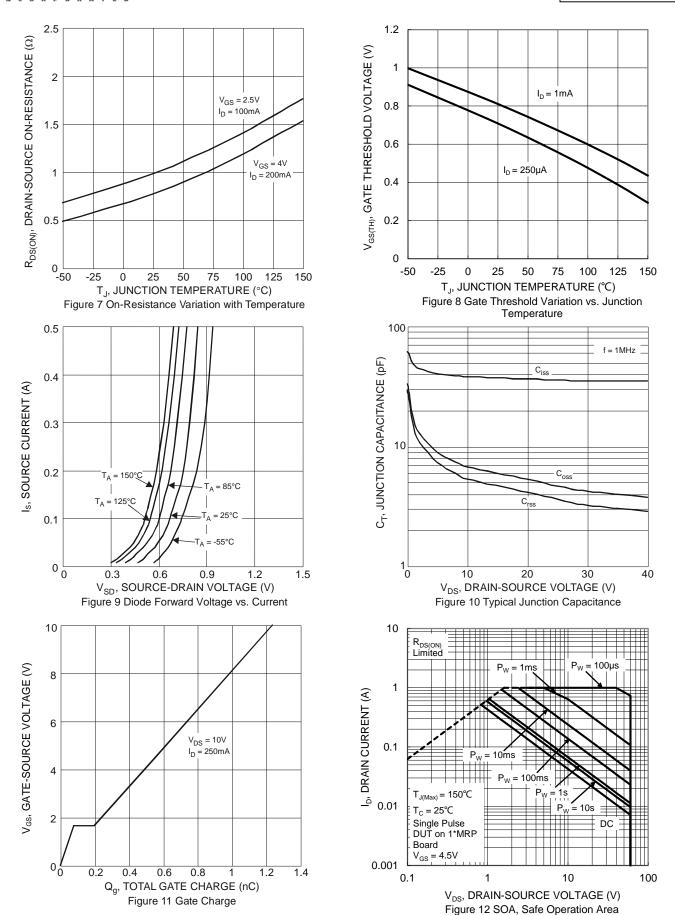


Figure 6 On-Resistance Variation with Temperature







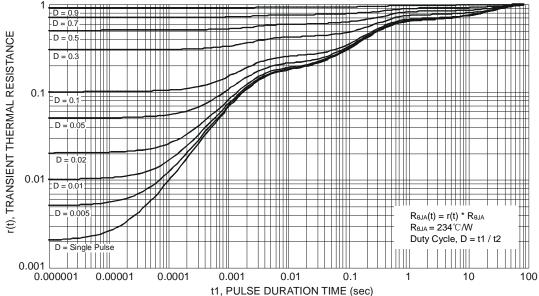


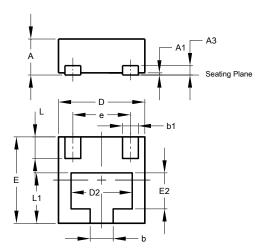
Figure 13 Transient Thermal Resistance



## **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.

### U-DFN1212-3 (Type C)

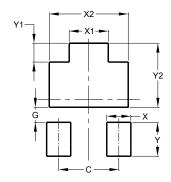


	U-DFN1212-3							
	Type C							
Dim	Dim Min Max							
Α	0.47	0.53	0.50					
A1	0	0.05	0.02					
A3	-	-	0.13					
b	0.27	0.37	0.32					
b1	0.17	0.27	0.22					
D	1.15	1.25	1.20					
D2	0.75	0.95	0.85					
е	-	-	0.80					
Е	1.15	1.25	1.20					
E2	0.40	0.60	0.50					
L	0.25	0.35	0.30					
L1	0.65	0.75	0.70					
All	Dimens	sions in	mm					

# **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.

### U-DFN1212-3 (Type C)



<b>Dimensions</b>	value			
Dilliensions	(in mm)			
С	0.800			
G	0.200			
Х	0.320			
X1	0.520			
X2	1.050			
Υ	0.450			
Y1	0.250			
Y2	0.850			



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