



**B0520LW** 

#### 0.5A SURFACE MOUNT SCHOTTKY BARRIER RECTIFIER

### **Features**

- Low Forward Voltage Drop
- Guard Ring Construction for Transient Protection
- **High Conductance**
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Notes 3 & 4)
- Qualified to AEC-Q101 Standards for High Reliability

## **Mechanical Data**

- Case: SOD123
- Case Material: Molded Plastic. UL Flammability Classification
- Moisture Sensitivity: Level 1 per J-STD-020
- Polarity: Cathode Band
- Lead Free Plating (Matte Tin Finish Annealed over Alloy 42 Leadframe). Solderable per MIL-STD-202, Method 208 (63)
- Weight: 0.01 grams (Approximate)

SOD123



Top View

## **Ordering Information** (Note 5)

Part Number	Case	Packaging
B0520LW-7-F	SOD123	3000/Tape & Reel
B0520LWQ-7-F	SOD123	3000/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3.Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. Product manufactured with Date Code V9 (week 33, 2008) and newer are built with Green Molding Compound. Product manufactured prior to Date Code V9 are built with Non-Green Molding Compound and may contain Halogens or Sb<sub>2</sub>O<sub>3</sub> fire retardants.
- 5. For packaging details, see http://www.diodes.com.

# **Marking Information**



SD = Product Type Marking Code YM = Date Code Marking Y = Year (ex: F = 2018) M = Month (ex: 9 = September)

Date Code Key

Code         J         K         L         M         N         P         R          F         G         BH         I         J         K         L           Month         Jan         Feb         Mar         Apr         May         Jun         Jul         Aug         Sep         Oct         Nov         Dec           Code         1         2         3         4         5         6         7         8         9         O         N         D	Year	1998	1999	2000	2001	2002	2003	2004		2018	2019	2020	2021	2022	2023	2024
	Code	J	K	L	М	N	Р	R		F	G	BH	I	J	K	L
Code         1         2         3         4         5         6         7         8         9         O         N         D	Month	Jan	Fe	b	Mar	Apr	May	Ju	n	Jul	Aug	Sep	Oc	t	Nov	Dec
	Code	1	2		3	4	5	6		7	8	9	0	)	N	D

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### Maximum Ratings (@TA = +25°C, unless otherwise specified.)

Single phase, half wave, 60Hz, resistive or inductive load. For capacitance load, derate current by 20%.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V <sub>RRM</sub> V <sub>RWM</sub> V <sub>R</sub>	20	V
RMS Reverse Voltage	V <sub>R(RMS)</sub>	14	V
Average Rectified Output Current @ T <sub>L</sub> = +90°C	lo	0.5	Α
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine Wave Superimposed on Rated Load	I <sub>FSM</sub>	5.5	A

### **Thermal Characteristics**

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 6)	P <sub>D</sub>	410	mW
Typical Thermal Resistance Junction to Ambient (Note 6)	$R_{\Theta JA}$	244	°C/W
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-65 to +125	°C

## Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	Test Conditions
Minimum Reverse Breakdown Voltage (Note 7)	$V_{(BR)R}$	20	V	$I_R = 250\mu A$
Maximum Forward Voltage Drop	$V_{FM}$	0.300 0.385 0.220 0.330	V	I <sub>F</sub> = 0.1A, T <sub>J</sub> = +25°C I <sub>F</sub> = 0.5A, T <sub>J</sub> = +25°C I <sub>F</sub> = 0.1A, T <sub>J</sub> = +100°C I <sub>F</sub> = 0.5A, T <sub>J</sub> = +100°C
Maximum Leakage Current (Note 8)	I <sub>RM</sub>	75 250	μA	$V_R = 10V, T_J = +25$ °C $V_R = 20V, T_J = +25$ °C
Maximum Leakage Current (Note 6)	I <sub>RM</sub>	5.0 8.0	mA	V <sub>R</sub> = 10V, T <sub>J</sub> = +100°C V <sub>R</sub> = 20V, T <sub>J</sub> = +100°C
Typical Total Capacitance	Ст	170	pF	$V_R = 0V DC$ , $f = 1MHz$

Notes:

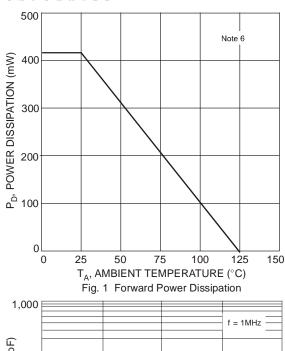
- 6. Device mounted on FR-4 PC board, 2"x 2", 2 oz. Copper, single sided, Cathode pad dimensions 0.75" x 1.0", Anode pad dimensions 0.25" x 1.0".
- 7. Pulse Test: Pulse width = 300 $\mu$ s, Duty Cycle  $\leq$  2%.
- 8. No purposefully added lead. Halogen and Antimony Free.

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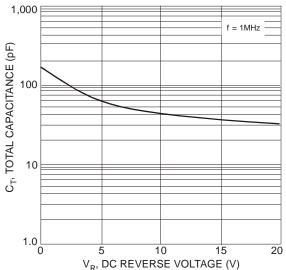
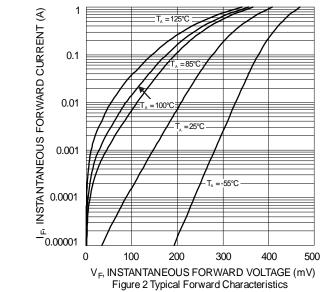
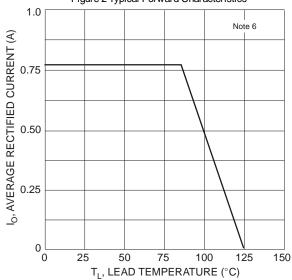


Fig. 3 Total Capacitance vs. Reverse Voltage



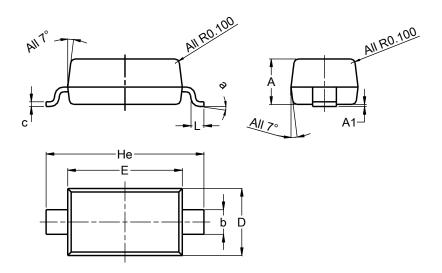




## **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.

#### SOD123

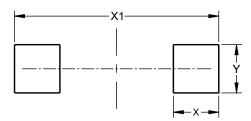


SOD123							
Dim	Min	Max	Тур				
Α	1.00	1.35	1.05				
A1	0.00	0.10	0.05				
b	0.52	0.62	0.57				
С	0.10	0.15	0.11				
D	1.40	1.70	1.55				
Е	2.55	2.85	2.65				
He	3.55	3.85	3.65				
٦	0.25	0.40	0.30				
а	00	8°					
All Dimensions in mm							

# **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.

### SOD123



Dimensions	Value (in mm)
Х	0.900
X1	4.050
Υ	0.950



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