

2.0A SURFACE MOUNT SCHOTTKY BARRIER RECTIFIER

PowerDI[®]123

DFLS240

Features

- Guard Ring Die Construction for Transient Protection
- High Current Capability
- Low Leakage Current
- Patented Interlocking Clip Design for High Surge Current Capacity
- Lead Free Finish, RoHS Compliant (Note 4)
- "Green" Molding Compound (No Br, Sb)
- Qualified to AEC-Q101 Standards for High Reliability

Mechanical Data

- Case: PowerDl[®]123
- Case Material: Molded Plastic, "Green" Molding Compound UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020D
- Terminal Connections: Cathode Band
- Terminals: Finish Matte Tin Annealed Over Copper leadframe. Solderable per MIL-STD-202, Method 208 ³
- Marking Information: See Page 2
- Ordering Information: See Page 2
- Weight: 0.01 grams (approximate)



Top View

Maximum Ratings @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V _{RRM} V _{RWM} V _R	40	V
RMS Reverse Voltage	V _{R(RMS)}	28	V
Average Forward Current	I _{F(AV)}	2.0	А
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load	I _{FSM}	40	А

Thermal Characteristics

Characteristic	Symbol	Тур	Мах	Unit
Thermal Resistance, Junction to Ambient Air (Note 1)	$R_{\theta JA}$	73	_	°C/W
Thermal Resistance, Junction to Soldering Point (Note 2)	$R_{\theta JS}$	—	13	°C/W
Operating Temperature Range	TJ	-65 to	°C	
Storage Temperature Range	T _{STG}	-65 to) +150	°C

Electrical Characteristics @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Reverse Breakdown Voltage (Note 3)	V _{(BR)R}	40		_	V	$I_R = 20\mu A$
Forward Voltage	VF		0.52	0.58	V	I _F = 1.0A
Forward Voltage		_	0.65	0.7		I _F = 2.0A
Leakage Current (Note 3)		_	_	20	μΑ	$V_{R} = 40V, T_{A} = 25^{\circ}C$
Leakage Current (Note 3)	IR		_	6.0	mA	$V_{R} = 40V, T_{A} = 100^{\circ}C$
Total Capacitance	CT		28	_	pF	$V_{R} = 10V, f = 1.0MHz$

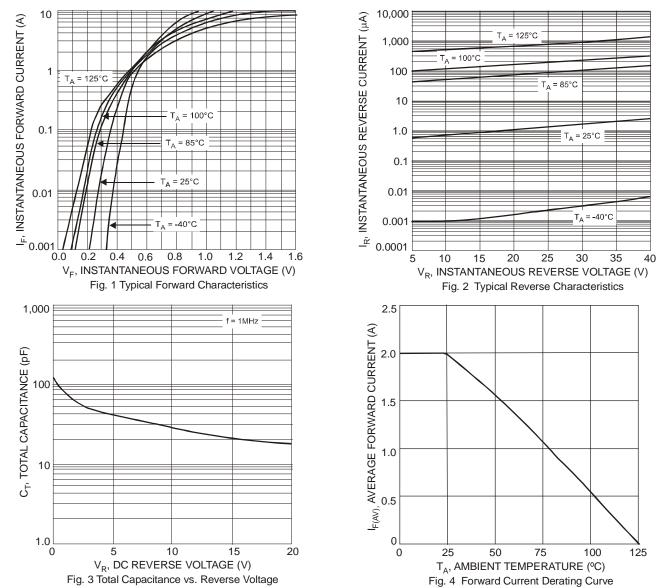
Notes: 1. Part mounted on Polymide board with 2 oz., copper, $74mm^2$ pad layout. $T_A = 25^{\circ}C$

2. Theoretical R_{BJS} calculated from the top center of the die straight down to the PCB/cathode tab solder junction.

3. Short duration pulse test used to minimize self-heating effect.

4. EU Directive 2002/95/EC (RoHS). All applicable RoHŠ exemptions applied, see EU Directive 2002/95/EC Annex Notes.





Ordering Information (Note 5)

Part Number	Case	Packaging
DFLS240-7	PowerDI [®] 123	3000/Tape & Reel

Notes: 5. For packaging details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

Marking Information



F04A = Product Type Marking Code YM = Date Code Marking Y = Year (ex: R = 2004) M = Month (ex: 9 = September)

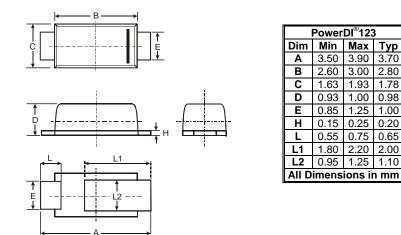
Date Code Key												
Year	2003	2004	20	005	2006	2007	2008	2009	20	10	2011	2012
Code	Р	R		S	Т	U	V	W		Х	Y	Z
Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D

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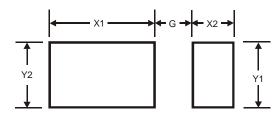
DFLS240



Package Outline Dimensions



Suggested Pad Layout



Dimensions	Value (in mm)
G	1.0
X1	2.2
X2	0.9
Y1	1.4
Y2	1.4

3.70

2.80

1.78

0.98

1.00

0.20

0.65

2.00

1.10

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