



**ZXTP720MA**

**40V PNP LOW SATURATION SWITCHING TRANSISTOR**

**Features and Benefits**

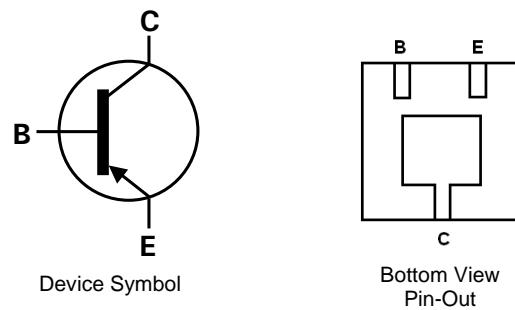
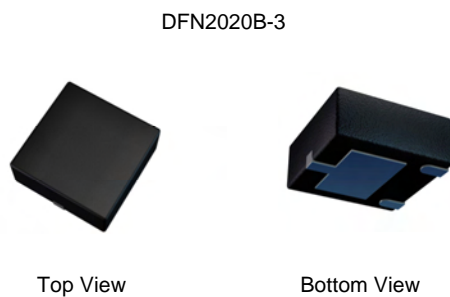
- $BV_{CEO} > -40V$
- $I_C = -3A$  Continuous Collector Current
- Low Saturation Voltage (-220mV max @ -1A)
- $R_{SAT} = 104 m\Omega$  for a low equivalent On-Resistance
- $h_{FE}$  specified up to -3A for high current gain hold up
- Low profile 0.6mm high package for thin applications
- $R_{\theta JA}$  efficient, 60% lower than SOT23
- 4mm<sup>2</sup> footprint, 50% smaller than SOT23
- **Lead-Free, RoHS Compliant (Note 1)**
- **Halogen and Antimony Free. "Green" Device (Note 2)**
- **Qualified to AEC-Q101 Standards for High Reliability**

**Mechanical Data**

- Case: DFN2020B-3
- Case material: Molded Plastic. "Green" Molding Compound.
- Terminals: Pre-Plated NiPdAu leadframe.
- Nominal package height: 0.6mm
- UL Flammability Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Weight: 0.01 grams (approximate)

**Applications**

- MOSFET Gate Driving
- DC-DC Converters
- Charging Circuits
- Power switches
- Motor control

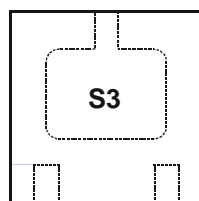


**Ordering Information** (Note 3)

Product	Marking	Reel size (inches)	Tape width (mm)	Quantity per reel
ZXTP720MATA	S3	7	8	3000

- Notes:
1. No purposefully added lead.
  2. Diodes Inc's "Green" policy can be found on our website at <http://www.diodes.com>
  3. For Packaging Details, go to our website at <http://www.diodes.com>.

**Marking Information**



Top View

S3 = Product Type Marking code

**Maximum Ratings** @ $T_A = 25^\circ\text{C}$  unless otherwise specified

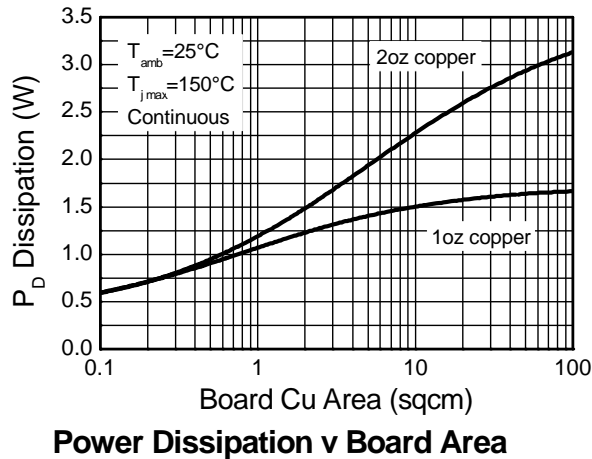
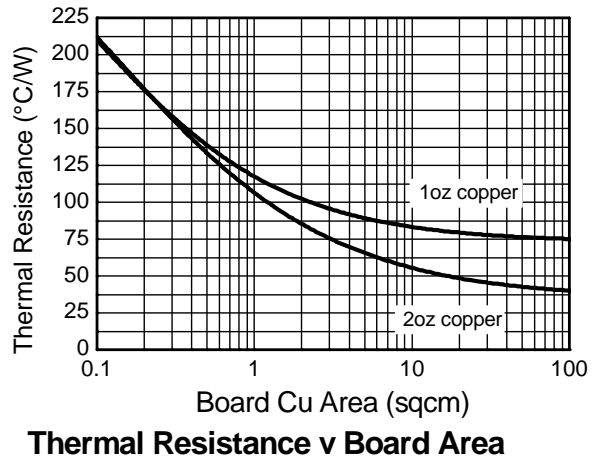
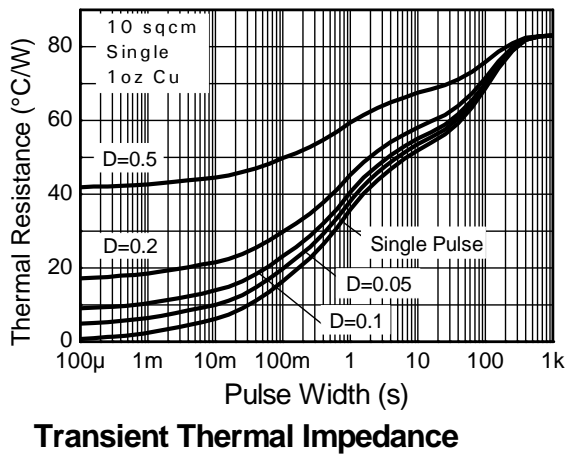
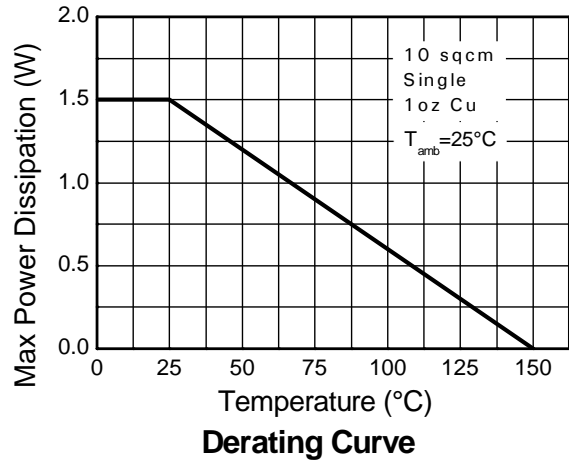
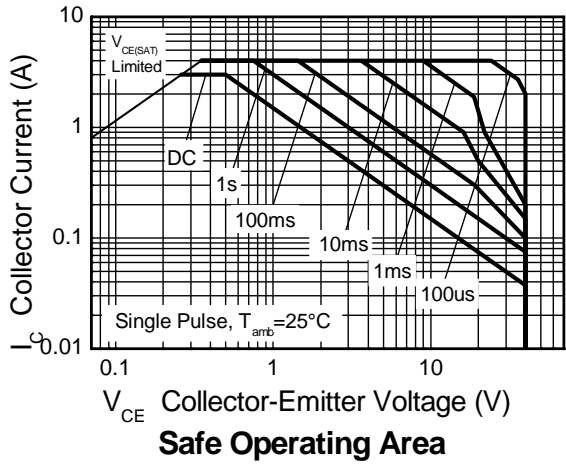
Parameter	Symbol	Limit	Unit
Collector-Base Voltage	$V_{CBO}$	-50	V
Collector-Emitter Voltage	$V_{CEO}$	-40	
Emitter-Base Voltage	$V_{EBO}$	-7	
Peak Pulse Current	$I_{CM}$	-4	A
Continuous Collector Current	(Note 4)	-3	
	(Note 5)	-3.3	
Base Current	$I_B$	-1	

**Thermal Characteristics** @ $T_A = 25^\circ\text{C}$  unless otherwise specified

Characteristic	Symbol	Value	Unit
Power Dissipation Linear Derating Factor	$P_D$	1.5	W
		12	
		2.45	mW/ $^\circ\text{C}$
		19.6	
Thermal Resistance, Junction to Ambient	$R_{\theta JA}$	83	$^\circ\text{C/W}$
		51	
Thermal Resistance, Junction to Lead	$R_{\theta JL}$	16.8	
Operating and Storage Temperature Range	$T_J, T_{STG}$	-55 to +150	

- Notes:
4. For a device surface mounted on 31mm x 31mm (10cm<sup>2</sup>) FR4 PCB with high coverage of single sided 1oz copper, in still air conditions; the device is measured when operating in a steady-state condition. The entire exposed collector pad is attached to the heatsink.
  5. Same as note (4), except the device is measured at  $t \leq 5$  sec.
  6. For a single device, thermal resistance from junction to solder-point (at the end of the drain lead).

**Thermal Characteristics**

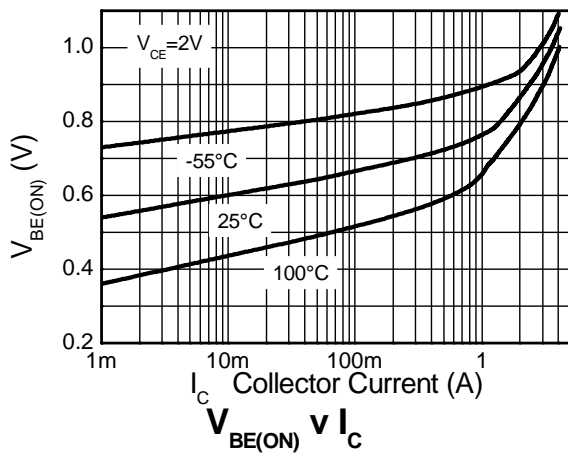
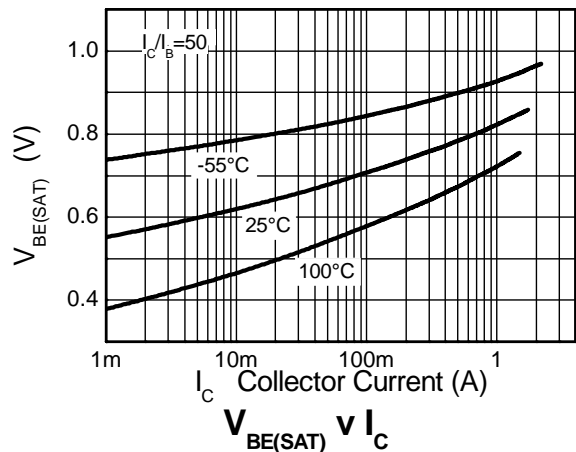
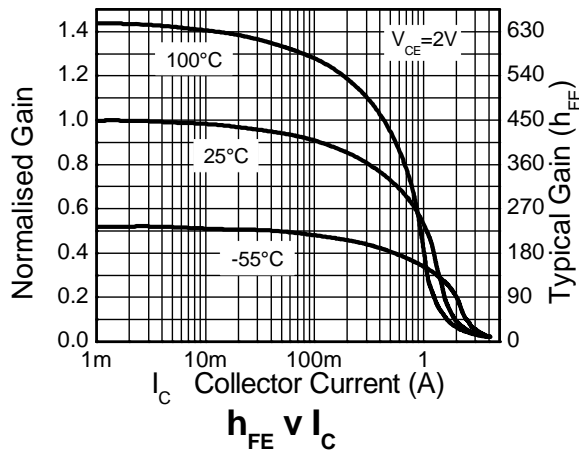
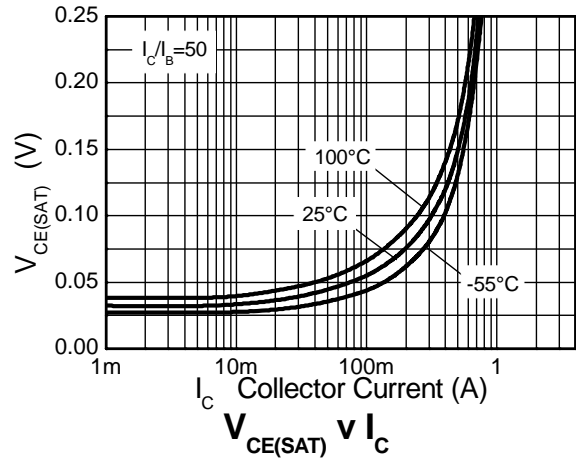
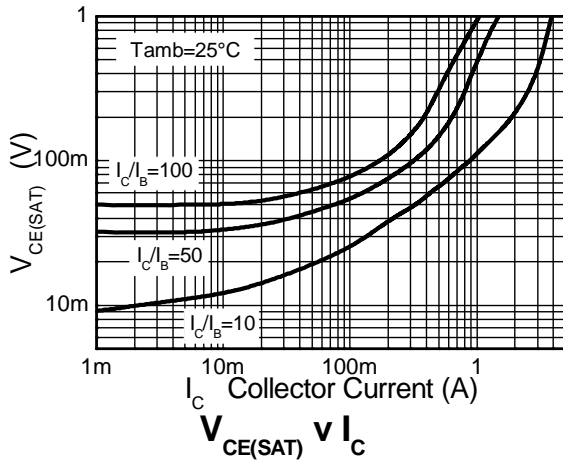


**Electrical Characteristics** @T<sub>A</sub> = 25°C unless otherwise specified

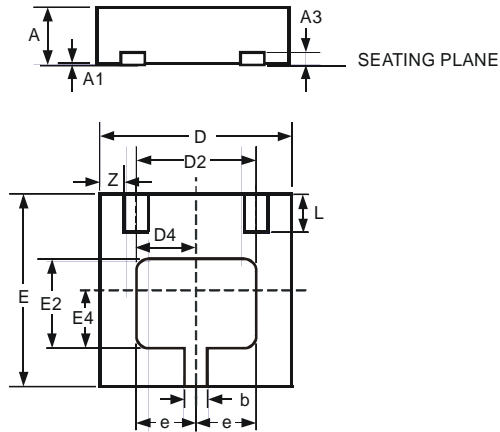
Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
Collector-Base Breakdown Voltage	BV <sub>CBO</sub>	-50	-80	-	V	I <sub>C</sub> = -100μA
Collector-Emitter Breakdown Voltage (Note 7)	BV <sub>CEO</sub>	-40	-70	-	V	I <sub>C</sub> = -10mA
Emitter-Base Breakdown Voltage	BV <sub>EBO</sub>	-7	-8.5	-	V	I <sub>E</sub> = -100μA
Collector Cutoff Current	I <sub>CBO</sub>	-	-	-100	nA	V <sub>CB</sub> = -40V
Emitter Cutoff Current	I <sub>EBO</sub>	-	-	-100	nA	V <sub>EB</sub> = -6V
Collector Emitter Cutoff Current	I <sub>CES</sub>	-	-	-100	nA	V <sub>CES</sub> = -32V
Static Forward Current Transfer Ratio (Note 7)	h <sub>FE</sub>	300	480	-	-	I <sub>C</sub> = -10mA, V <sub>CE</sub> = -2V
		300	450	-		I <sub>C</sub> = -100mA, V <sub>CE</sub> = -2V
		180	290	-		I <sub>C</sub> = -1A, V <sub>CE</sub> = -2V
		60	130	-		I <sub>C</sub> = -1.5A, V <sub>CE</sub> = -2V
		12	22	-		I <sub>C</sub> = -3A, V <sub>CE</sub> = -2V
Collector-Emitter Saturation Voltage (Note 7)	V <sub>CE(sat)</sub>	-	-25	-40	mV	I <sub>C</sub> = -0.1A, I <sub>B</sub> = -10mA
		-	-150	-220		I <sub>C</sub> = -1A, I <sub>B</sub> = -50mA
		-	-195	-300		I <sub>C</sub> = -1.5A, I <sub>B</sub> = -100mA
		-	-210	-300		I <sub>C</sub> = -2A, I <sub>B</sub> = -200mA
		-	-260	-370		I <sub>C</sub> = -2.5A, I <sub>B</sub> = -250mA
Base-Emitter Turn-On Voltage (Note 7)	V <sub>BE(on)</sub>	-	-0.89	-0.95	V	I <sub>C</sub> = -2.5A, V <sub>CE</sub> = -2V
Base-Emitter Saturation Voltage (Note 7)	V <sub>BE(sat)</sub>	-	-0.97	-1.05	V	I <sub>C</sub> = -2.5A, I <sub>B</sub> = -250mA
Output Capacitance	C <sub>obo</sub>	-	19	25	pF	V <sub>CB</sub> = -10V, f = 1MHz
Transition Frequency	f <sub>T</sub>	150	190	-	MHz	V <sub>CE</sub> = -10V, I <sub>C</sub> = -50mA, f = 100MHz
Turn-On Time	t <sub>on</sub>	-	40	-	ns	V <sub>CC</sub> = -15V, I <sub>C</sub> = -0.75A
Turn-Off Time	t <sub>off</sub>	-	435	-	ns	I <sub>B1</sub> = I <sub>B2</sub> = -15mA

Notes: 7. Measured under pulsed conditions. Pulse width ≤ 300 μs. Duty cycle ≤ 2%.

**Typical Electrical Characteristics**

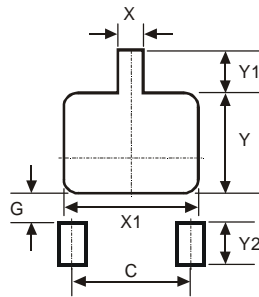


**Package Outline Dimensions**



DFN2020B-3			
Dim	Min	Max	Typ
A	0.57	0.63	0.60
A1	0	0.05	0.02
A3	—	—	0.152
b	0.20	0.30	0.25
D	1.95	2.075	2.00
D2	1.22	1.42	1.32
D4	0.56	0.76	0.66
e	—	—	0.65
E	1.95	2.075	2.00
E2	0.79	0.99	0.89
E4	0.48	0.68	0.58
L	0.25	0.35	0.30
Z	—	—	0.225
All Dimensions in mm			

**Suggested Pad Layout**



Dimensions	Value (in mm)
C	1.30
G	0.24
X	0.35
X1	1.52
Y	1.09
Y1	0.47
Y2	0.50

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