

50V PNP PRE-BIASED SMALL SIGNAL TRANSISTOR IN DFN1006

Product Summary

Part Number	R1 (NOM)	R2 (NOM)	Marking
DDTA114YLP	10kΩ	47kΩ	P3

Features

- Epitaxial Planar Die Construction
- Ultra-Small Leadless Surface Mount Package
- Ideally Suited for Automated Assembly Processes
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

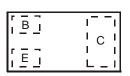
X1-DFN1006-3



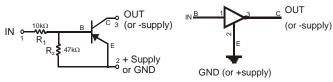
- Case: X1-DFN1006-3
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish NiPdAu
- Solderable per MIL-STD-202, Method 208 @4
- Weight: 0.0009 grams (Approximate)







Top View Pin-Out



Device Symbol

Equivalent Inverter Circuit

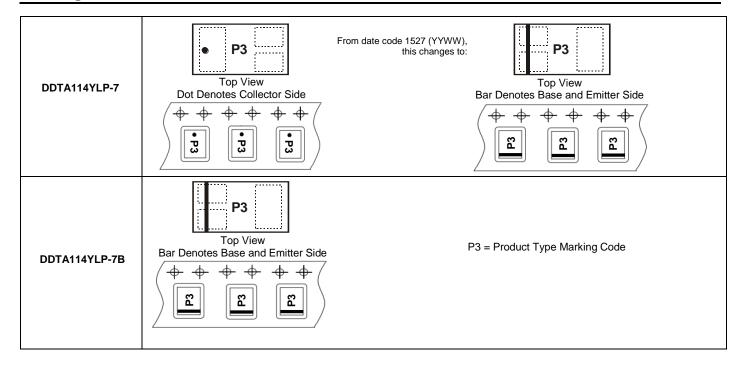
Ordering Information (Note 4)

Product	Marking	Reel size (inches)	Tape width (mm)	Quantity per reel
DDTA114YLP-7	P3	7	8	3,000
DDTA114YLP-7B	P3	7	8	10,000

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com.

Marking Information





Absolute Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Supply Voltage	V _{CC}	-50	V
Input Voltage	V _{IN}	+6 to -40	V
Output Current	l ₀	-70	mA
Output (Collector) Current	I _{C(MAX)}	-100	mA

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 5)	P_{D}	250	mW
Power Derating above +25°C	P _{der}	2	mW/°C
Thermal Resistance, Junction to Ambient Air (Note 5) (Equivalent to one heated junction of PNP)	$R_{ hetaJA}$	500	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

Electrical Characteristics: Discrete PNP Transistor (Q1) (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Off Characteristics (Note 6)						
Collector-Base Breakdown Voltage	BV_{CBO}	-50			٧	$I_C = -100\mu A, I_E = 0$
Collector-Emitter Breakdown Voltage	BV_CEO	-50	_		V	$I_C = -10.0 \text{mA}, I_B = 0$
Collector-Base Cut Off Current	I _{CBO}			-0.1	μΑ	$V_{CB} = -50V, I_{E} = 0$
Collector-Emitter Cut Off Current, IO(off)	ICES			-0.1	μΑ	$V_{CB} = -50V, I_B = 0$
Emitter-Base Cut Off Current	I _{EBO}			-0.2	mA	$V_{EB} = 5V, I_{C} = 0$
Input Off Voltage	$V_{I(off)}$	-0.3			V	$V_{CC} = -5V, I_{O} = -100\mu A$
On Characteristics (Note 6)						
Input-On Voltage	$V_{I(on)}$			-1.4	٧	$V_O = -0.3V$, $I_O = I_C = 1mA$
Input Current	l _l			-0.88	mA	$V_I = -5V$
DC Current Gain	h _{FE}	80	_	_	_	$V_{CE} = -5V$, $I_C = -5mA$
Collector-Emitter Saturation Voltage	V _{CE(sat)}	_	_	-0.25	V	$I_C = -50 \text{mA}, I_B = -2.5 \text{mA}$
Output On Voltage (Same as V _{CE(sat)})	V _{O(on)}	_	-0.1	-0.3	V	$I_{I} = -0.25 \text{mA}, I_{O} = -5 \text{mA}$
Input Resistance	R1	7	10	13	kΩ	_
Resistance Ratio	(R2/R1)	3.7	4.7	5.7	_	_
Small Signal Characteristics						
Current Gain-Bandwidth Product	f _T	_	250	_	MHz	$V_{CE} = -10V$, $I_{E} = -5mA$, $f = 100 MHz$

 For the device mounted on minimum recommended pad layout 1oz copper that is on a single-sided 1.6mm FR4 PCB; device is measured under still air conditions whilst operating in steady state condition. The entire exposed collector pad is attached to the heatsink.
 Measured under pulsed conditions. Pulse width ≤ 300μs. Duty cycle ≤ 2%. Notes:



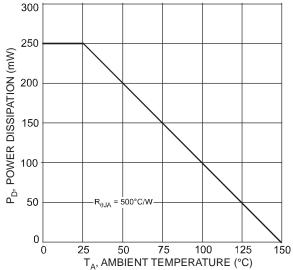


Fig. 1 Power Dissipation vs. Ambient Temperature (Note 4)

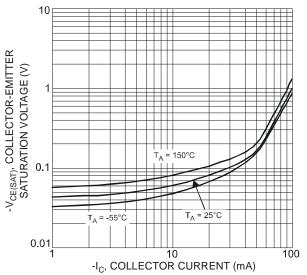


Fig. 3 Typical Collector-Emitter Saturation Voltage vs. Collector Current

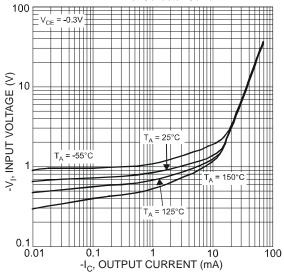


Fig. 5 Typical Input Voltage vs. Output Current (On Characteristics)

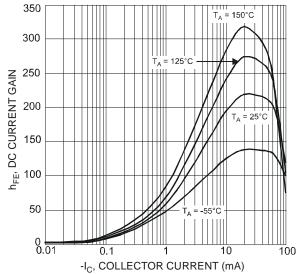


Fig. 2 Typical DC Current Gain vs. Collector Current

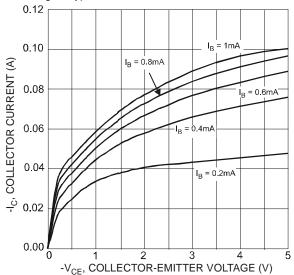


Fig. 4 Typical Collector Current vs. Collector-Emitter Voltage

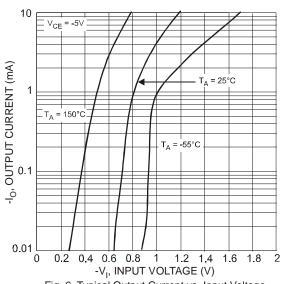
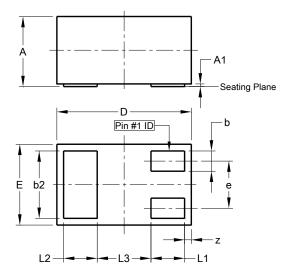


Fig. 6 Typical Output Current vs. Input Voltage (Off Characteristics)



Package Outline Dimensions

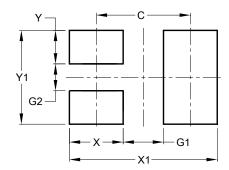
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.



X1-DFN1006-3				
Dim	Min	Max	Тур	
Α	0.47	0.53	0.50	
A1	0.00	0.05	0.03	
b	0.10	0.20	0.15	
b2	0.45	0.55	0.50	
D	0.95	1.075	1.00	
Е	0.55	0.675	0.60	
е	-	-	0.35	
L1	0.20	0.30	0.25	
L2	0.20	0.30	0.25	
L3	ı	-	0.40	
Z	0.02	0.08	0.05	
All Dimensions in mm				

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)		
С	0.70		
G1	0.30		
G2	0.20		
Х	0.40		
X1	1.10		
Y	0.25		
Y1	0.70		



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