

CUSTOMER

NOMINAL FREQUENCY

PRODUCT TYPE

27.000000 MHz

TYPE HX 2.5×2.0 SEAM SEALED CRYSTAL CLOCK OSCILLATOR

SPEC. NO. ( P/N )

CUSTOMER P/N

ISSUE DATE

VERSION

HX2127002Q

June 8, 2018

В

APPROVED	PREPARED	QA
Brenda	nich	Dong Yang

## **Diodes Incorporated**

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- \*RoHS Compliant
- \*HF-Halogen Free
- \*REACH Compliant
- \*AEC-Q200 Compliant

## HX2127002Q

VER. B 8-Jun-18

# **VERSION HISTORY**

Version No.	Version Date	Description	Notes
А	Sep.24,2015	Initial Release	
в	Jun.8,2018	Updated logo	



## HX2127002Q

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### **ELECTRICAL SPECIFICATIONS**

SRe Part Number : HX2127002Q

ltem	Symbol	Specifications	Units	Notes
Nominal Frequency	Fo	27.000000	MHz	
Frequency Stability	FT	± 25	ppm	**See note
Operating Temperature Range	TR	<b>-</b> 40 to +105	r	
Supply Voltage	V <sub>DD</sub>	+3.3 ± 5.0%	V	
Logic Type	LT	LVCMOS		
Supply Current, Output Enabled	I <sub>DD</sub> /OE	20	mA	Мах
Supply Current, Output Disabled	I <sub>DD</sub> /OD	100	μA	Мах
Duty Cycle (Symmetry)	DC/SY	45 / 55	%	Measured 50% of Waveform
Rise / Fall Time	T <sub>R</sub> /T <sub>F</sub>	8	ns	Max. measured 20 / 80% of Waveform
Output Voltage "0" Level	V <sub>OL</sub>	10% V <sub>DD</sub>	V	Мах
Output Voltage "1" Level	V <sub>OH</sub>	90% V <sub>DD</sub>	V	Min
Output Load	CL	15	pF	Мах
Jitter, Phase	RMS	1	ps	Max. 12kHz ~ 5MHz Frequency Band
Jitter, Accumulated	RMS(1-σ)	4	ps	Max. 20,000 Consecutive Periods
Jitter, Peak to Peak	Pk-Pk	40	ps	Max. 100,000 Random Periods
Start Up Time		10	ms	Max.
Storage Temperature Range		-55 to +125	r	

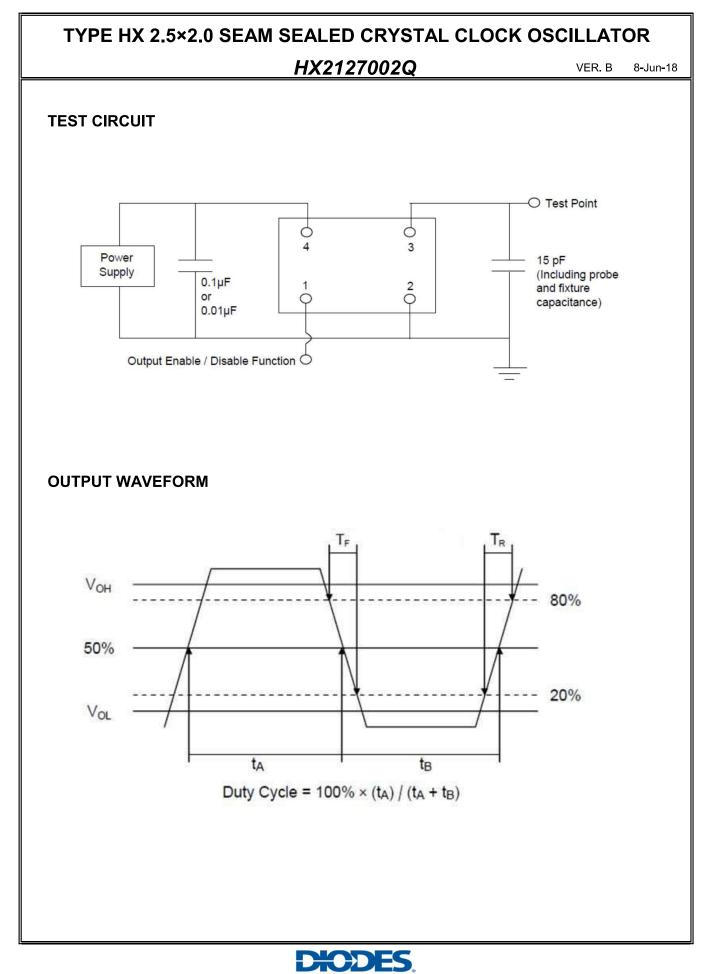
💥 This product doesn't include harmful substance that stipulated by SONY SS-00259 Level 1 and S-AT2-001 Level 1 standard. RoHS Compliant (Pb - Free).

\*\*Stability includes all combinations of Operating Temperature, Load changes, rated Input (Supply) Voltage changes, Initial Calibration Tolerance (25°C), Aging (1 year at 25°C Average Effective Ambient Temperature), Shock and Vibration.

#### Output Enable / Disable Function

Parameter	Min.	Тур.	Max.	Units	Notes
Input Voltage (Pin1), Output Enable	$0.7V_{DD}$			V	Or Open
Input Voltage (Pin1), Output Disable (low power standby)			$0.3V_{DD}$	V	Output is Hi-Z
Internal Pullup Resistance	30			KΩ	
Output Disable Delay			50	ns	





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### AEC-Q200 RELIABILITY TEST SPECIFICATIONS:

#### 1. Initial

- 1.1 Physical Dimensions: JESD22, Method JB1-100
- 1.2 External Visual: MIL-STD-883, Method 2009
- 1.3 Freq. Vs. Temperature: Per Specification/Datasheet

#### 2. Mechanical

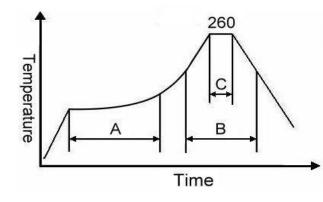
- 2.1 Mechanical Shock: MIL-STD-202 Method 213
- 2.2 Vibration: MIL-STD-202 Method 204
- 2.3 Solderability: J-STD-002
- 2.4 Board Flex: AEC Q200-005
- 2.5 Terminal Strength (SMD): AEC Q200-006

#### 3.Environmental

- 3.1 Temp Cycle: JESD22, Method JA-104
- 3.2 Resistance to Solder Heat: MIL-STD-202 Method 210
- 3.3 High Temperature Operating Life: MIL-STD-202, Method 108
- 3.4 High Temp Exposure: MIL-STD-202, Method 108
- 3.5 High Temp & High Humidity: MIL-STD-202, Method 103
- 3.6 Thermal Shock: MIL-STD-202, Method 107

#### SUGGESTED IR REFLOW PROFILE

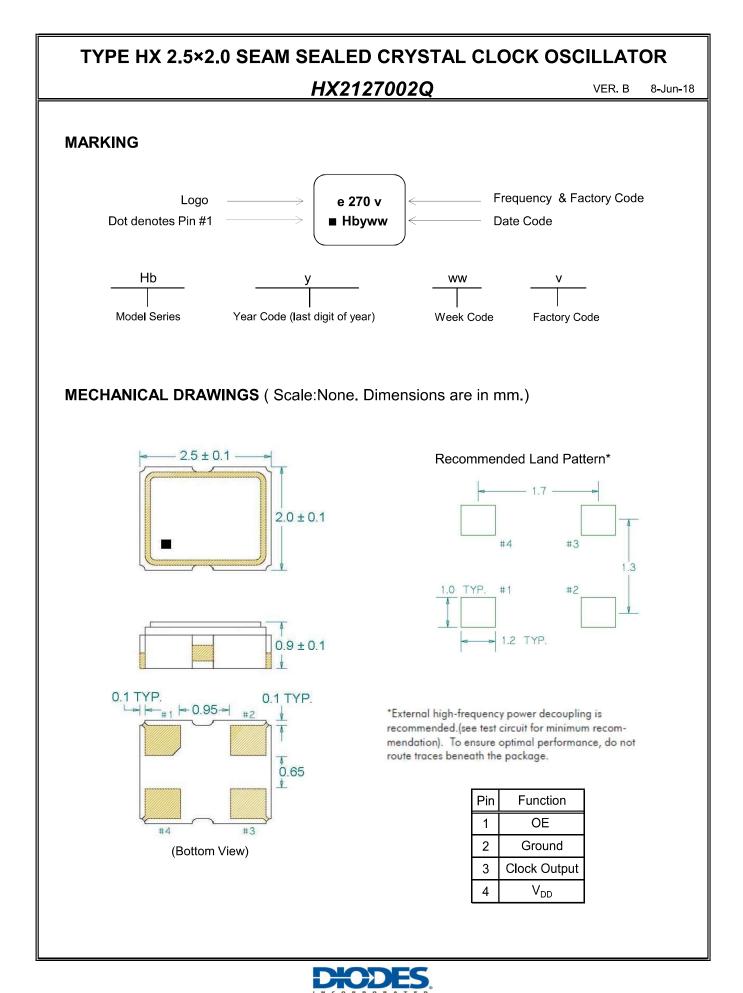
\*As per IPC-JEDEC J-STD-020D



Note.					
	Stage Temperat		Time		
Α	Preheat	150~200°C	60~120 Sec		
В	Primary Heat	217°C	60~150 Sec		
С	Peak	260°C	10 Sec		



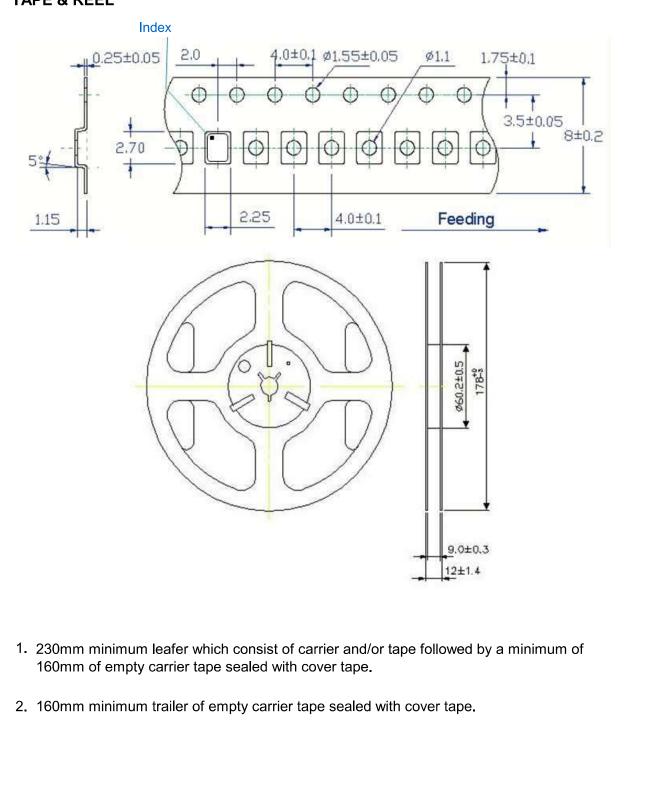
Maker



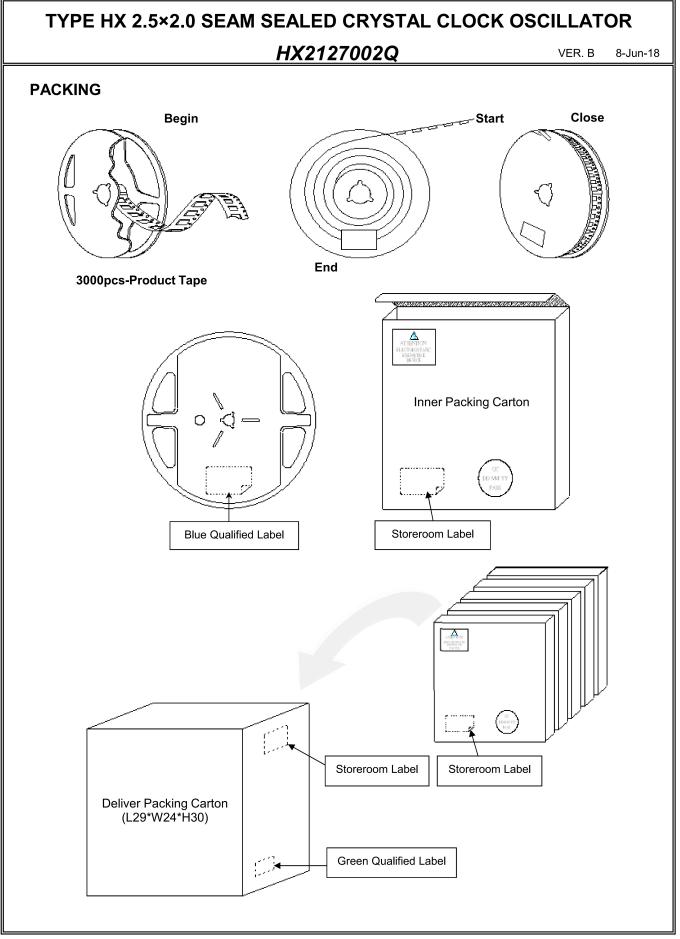
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**TAPE & REEL** 









单击下面可查看定价,库存,交付和生命周期等信息

>>Diodes Incorporated(达迩科技(美台))